Sabre Drive
ST8741J, ST8851J
ST81123J, ST81236J
User's Manual
(SMD Interface)

WARNING

Do not attempt to install, operate, or repair the unit before you read the important safety information located directly after the revision record in this manual. Failure to follow that and other safety precautions in this manual could cause injury to yourself and others.

This equipment generates, uses and can radiate radio frequency energy and if not installed and used in accordance with the instructions manual, may cause interference to radio communications. It has been tested and found to comply with the limits for a Class A computing device pursuant to Subpart J of Part 15 of the FCC Rules, which are designed to provide reasonable protection against such interference when operated in a commercial environment. Operation of this equipment in a residential area is likely to cause interference, in which case the user, at his own expense, will be required to take whatever measures may be required to correct the interference.

If the operator or status/control panel (component assembly) is not installed in the inner drawer, it is your responsibility to provide any additional RFI shielding or grounding needed to ensure FCC Class A compliance.

This digital apparatus does not exceed the Class A limits for radio noise for digital apparatus set out in the Radio Interference Regulations of the Canadian Department of Communications.

VORSICHT

Versuchen Sie nicht, das Gerät zu installieren, zu betreiben oder zu reparieren bevor Sie die in der Anlage C aufgeführten Installations- und Betriebserfordernisse sorgfältig gelesen haben. Die Nichtbeachtung dieser und weiterer in diesem Manual gegebenen Informationen kann Verletzungen Ihrer selsbt und Anderer zur Folge haben.

AVERTISSEMENT

Ne tentez pas d'installer, de mettre en marche, ou de dépanner cet appareil avant d'avoir lû les instructions d'installation et de fonctionnement données dans l'appendice D. A défaut d'observer ceci ainsi que les autres informations de ce manuel vous pourriez mettre votre propre santé en danger, ou celle d'autrui.

Le présent appareil numérique n'émet pas de bruits radioélectriques dépassant les limites applicables aux appareils numériques de la classe A prescrites dans le Règlement sur le brouillage radioélectrique édicté par le ministère des Communications du Canada.

PRECAUCION

No intente instala, operar ó reparar está unidad antes de leer los requerimientos de instalación y operación dados en el Apéndice E. Cualquier negli gencia en seguir esta u otra información dada en esta manual puede causarle daños a uno mismo u otros.

Sabre Drive

ST8741J ST8851J ST81123J ST81236J

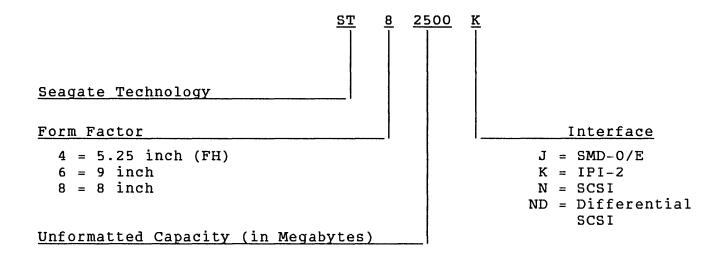
User's Manual (SMD Interface)

General Description
Operation
Installation and Checkout

Publication Number: 83325710-G



Seagate Disc Drive Product Numbers



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PREFACE

This manual contains maintenance information for the Seagate Sabre disc drives listed later in this preface. It provides instructions to all personnel who operate the drive and to customer engineers who install and check out the drive. Customer engineers who troubleshoot and repair drives should obtain copies of the manuals listed below.

The information in this manual is presented as follows:

- Section 1 General Description. Describes equipment functions and specifications.
- Section 2 Operation. Describes and illustrates the location and use of all controls and indicators, and provides operating procedures.
- Section 3 Installation and Checkout. Describes site requirements, unpackaging and inspection, installation and checkout.
- Appendix A Diagnostic Testing and Status Code Summary.

 Provides simplified troubleshooting information.
- Appendix B Reference Material for Sector Selection.

 Provides additional information on setting sector switches.
- Appendix C German Instructions. Contains basic installation and operation information in the German language.
- Appendix D French Instructions. Contains basic installation and operation information in the French language.
- Appendix E Spanish Instructions. Contains basic installation and operation information in the Spanish language.

New features, technical changes, additions, and deletions in this manual are indicated as follows:

- A vertical bar in the outer margin of a page marks the changed area.
- A dot by the page number indicates the entire page contains new or changed information.

 A vertical bar by the page number indicates the information was moved from another page, but there were no technical or editorial changes.

The following manuals apply to the SABRE and are available for purchase from:

Seagate Technology, Inc. Customer Services 12701 Whitewater Drive Minnetonka, MN 55343

Phone: (612) 931-8612 Fax: (612) 931-8817

Publication No.	<u>Title</u>
83325690	Theory Manual (contains information on power and control functions, interface functions, servo surface decoding, seek functions, read/write functions, and error detection and analysis)
83325700	Parts Data Manual (contains listings of field replaceable parts, manufacturer's recommended spare parts, and accessories)
83325710	User's Manual (contains general description, operation, installation & checkout information)
83325720	Maintenance Manual (contains general maintenance information, trouble analysis, and repair & replacement)
83325730	Diagrams Manual
83325660	Pocket Reference (summarizes status codes and diagnostic operation for drives with the SMD-E interface)

For more information about the drive and interface described in this manual, you can request copies of the following specification from your Seagate sales representative:

Specification No. Title

64712402 SMD-E Interface Specification

The following is a list of drives that this manual applies to:

Equipment Number	Model Number	Interface	Data Capa- city (MB)	Sector Length
PA8G1A	ST8741J	Single Channel SMD	741	Unspecified
PA8G2A	ST8741J	Dual Channel SMD	741	Unspecified
PA8G2C	ST8741J	Dual Channel SMD	741	Unspecified
PA8G2D	ST8741J	Dual Channel SMD	741	Unspecified
PA8G2E	ST8741J	Dual Channel SMD	741	Unspecified
PA8K1A	ST8851J	Single Channel SMD	851	Unspecified
PA8K2A	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2D	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2E	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2F	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2G	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2H	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2J	ST8851J	Dual Channel SMD	851	Unspecified
PA8K2K	ST8851J	Dual Channel SMD	851	Unspecified
PA8N1A	ST81236J	Single Channel SMD	1236	Unspecified
PA8N2A	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2B	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2C	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2D	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2E	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2F	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2G	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2H	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2J	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2K	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2L	ST81236J	Dual Channel SMD	1236	Unspecified
PA8N2M	ST81236J	Dual Channel SMD	1236	Unspecified
		Continued —		

Equipment Number	Model Number	Interface	Data Capa— city (MB)	Sector Length
PA8W2A	ST81123J	Dual Channel SMD	1123	Unspecified
PA8W2B	ST81123J	Dual Channel SMD	1123	Unspecified
PA8W2C	ST81123J	Dual Channel SMD	1123	Unspecified
PA8W2D	ST81123J	Dual Channel SMD	1123	Unspecified

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IMPORTANT SAFETY INFORMATION AND PRECAUTIONS

Use of proper safety and repair techniques is important for safe, reliable operation of this unit. Service should be done only by qualified persons. We recommend the procedures in this manual as effective ways of servicing the unit. Some procedures require the use of special tools. For proper maintenance and safety, you must use these tools as recommended.

The procedures in this manual and labels on the unit contain warnings and cautions that must be carefully read and followed to minimize or eliminate the risk of personal injury. The warnings point out conditions or practices that may endanger you or others. The cautions point out conditions or practices that may damage the unit, possibly making it unsafe for use.

You must also understand that these warnings and cautions are not exhaustive. We cannot possibly know, evaluate, and advise you of all the ways in which maintenance might be performed or the possible risk of each technique. Consequently, we have not completed any such broad evaluation. If you use a non-approved procedure or tool, first ensure that the method you choose will not risk either your safety or unit performance.

For the safety of yourself and others, observe the following warnings and precautions.

- Perform all maintenance by following the procedures in this manual.
- Follow all cautions and warnings in the procedures and on unit labels.
- Use the special tools called out in the procedures.
- Use sound safety practices when operating or repairing the unit.
- Use caution when troubleshooting a unit that has voltages present. Remove power from unit before servicing or replacing parts.
- Wear safety glasses when servicing units.
- Wear safety shoes when removing or replacing heavy parts.
- Use only designated Seagate replacement parts. Non-Seagate replacement parts can adversely affect safety in addition to degrading reliability, increasing maintenance downtime, and voiding warranty coverage.

- Use care while working with the power supply because line voltages are always present when the ac power cord is connected to a power source. Setting the power supply switch to position " "disables dc power to the drive but has no effect on ac power within the supply. For complete safety, remove the ac power plug from the site power outlet.
- The site power outlet shall be installed near the equipment and shall be easily accessible.
- In case of fire or other emergency, isolate the drive from main power by removing the drive power plug from the ac outlet. In situations where pulling the plug is not possible or practical, use the system main power disconnect to isolate the drives from main power.
- In an IT power system, a disconnect device shall be provided which simultaneously interrupts all phase and neutral conductors.
- WARNING: In IT power systems, the unearthed neutral is considered as a phase conductor. For such IT power systems, double pole/neutral fusing is required.
- When the drive is mounted in an equipment rack or cabinet, ensure that the internal temperature of the rack or cabinet will not exceed the limits defined for the drive. Where units are stacked vertically, pay special attention to the top where temperatures are usually highest.
- This drive is designed to be installed and operated in accordance with UL1950, IEC380, IEC950, EN 60950, CSA C22.2 154, CSA C22.2 220, and VDE0806.
- Follow the precautions listed under Protecting the Drive From Electrostatic Discharge.
- If the power supply is placed on a bench for testing, position the supply so all ventilation holes are open, to allow proper air flow to internal components.
- Do not attempt to disassemble the module. It is not field repairable. Replace the entire module assembly if it is defective.
- Always deenergize drive before removing or installing circuit boards, cables, or any other electrical components.

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- Do not operate the drive over an extended period of time without the top cover installed.
- The power supply is designed to be connected to an IT network.
- Do not attempt to disassemble the power supply. It is not field repairable. Replace the entire supply if it is defective.
- If you do not use a recommended Seagate power supply, ensure the supply meets the specifications in this manual and is designed to be used in accordance with UL1950, IEC380, IEC950, EN 60950, CSA C22.2 154, CSA C22.2 220, and VDE0806.

Section 1 General Description

INTRODUCTION

The Seagate Sabre disc drives are high speed, random access digital data storage devices that connect to a central processor through a controller. Equipment specifications for the drive are listed in table 1-1.

The remainder of this section provides a general description of the drive and is divided into the following areas:

- Equipment Interface Description -- Describes available drive interfaces.
- Equipment Functional Description -- Explains the basic function of the drive.
- Equipment Physical Description -- Provides a basic description of the drive's physical characteristics.
- Equipment Configuration -- Describes the various drive configurations and how to identify them.

EQUIPMENT INTERFACE DESCRIPTION

The drive can be configured to operate with either a standard (SMD-0) or an enhanced (SMD-E) interface. Refer to section 3 of this manual for definitions of signals on the interface cables (under Interface Requirements). Section 3 also contains instructions on selecting the various interface options available (under Setting Circuit Board Switches). Refer to the theory manual for a complete description of interface functions.

EQUIPMENT FUNCTIONAL DESCRIPTION

The drive contains all the circuits and mechanical devices necessary to record data on and recover it from its discs. The necessary power for this is provided by the power supply, which receives its input power from the site main power source.

83325710 G GENERAL DESCRIPTION 1-1

TABLE 1-1. DRIVE SPECIFICATIONS

Characteristics	Conditions	Specifications	
Size	Dimensions	See Space Requirements in section 3	
	Weight (Drive only)	14.8 kg (32.8 lb)	
	Weight (Power Supply only)	3.6 kg (8.0 lb)	
Interface	SMD-0/SMD-E		
Recording	Total Capacity ST8741J drives ST8851J drives ST81123J drives ST81236J drives	(Unformatted) 741.63 MB 851.14 MB 1123.05 MB 1236.06 MB	
	Bytes per track ST8741J drives ST8851J drives ST81123J drives ST81236J drives	(Unformatted) 30 240 bytes 41 088 bytes 45 792 bytes 50 400 bytes	
	Number of discs	9	
	Movable data heads	15	
	Servo Heads	1	
	Physical heads per surface	1	
	Logical cylinders per head/disc assy ST8741J drives ST8851J drives ST81123J drives ST81236J drives Modulation	1635 (0-1634) 1381 (0-1380) 1635 (0-1634) 1635 (0-1634) 2-7 code	
Table Continued on Next Page			

TABLE 1-1. DRIVE SPECIFICATIONS (Contd)

Characteristics	Conditions	Specifications
Transfer rate	Disc speed at 3600 r/min ST8741J drives ST8851J drives ST81123J drives ST81236J drives	14.52 MHz (1.814 MB/s) 19.72 MHz (2.465 MB/s) 21.98 MHz (2.750 MB/s) 24.19 MHz (3.024 MB/s)
Latency		Latency is time to reach a particular track address after positioning is complete.
	Average	8.33 milliseconds (disc rotation speed at 3600 r/min)
	Maximum	16.83 milliseconds (disc rotation speed at 3564 r/min)
Seek Time	Full	35 milliseconds maximum
	Average	15 milliseconds
	Single Track	4 milliseconds average
Start Time		90 seconds maximum
Stop Time		60 seconds maximum

All functions performed by the drive are done under direction of the controller. The controller communicates with the drive via the interface which consists of a number of I/O lines carrying the necessary signals to and from the drive.

Some interface lines, including those that carry commands to the drive, are not enabled unless the drive is selected by the controller. Unit selection allows the controller, which can be connected to more than one drive, to initiate and direct an operation on a specific drive.

All operations performed by the drive are related to data storage and recovery (normally referred to as writing and reading). The actual reading and writing is performed by electromagnetic devices called heads that are positioned over the recording surfaces of the rotating discs. There is one head for each disc surface. The heads are positioned in such a way that data is written in concentric tracks around the disc surfaces (see figure 1-1).

Before any read or write operation can be performed, the controller must instruct the drive to position the heads over the desired cylinder (called seeking) and use the head located over the surface (head selection) where the operation is to be performed.

After selecting a head and arriving at the data track, the controller must locate that portion of the track where the data is to be written or read. This is called track orientation and is done by using the Index and Sector signals generated by the drive. The Index signal indicates the logical beginning of each track. The Sector signals are used by the controller to determine the position of the head on the track with respect to Index.

When the desired location is reached, the controller commands the drive to actually read or write the data. During a read operation, the drive recovers data from the discs and transmits it to the controller. During a write operation, the drive receives data from the controller, processes it and writes it on the discs.

The drive is also capable of recognizing certain errors that may occur during its operation. When an error is detected, it is indicated either by a signal to the controller or by a maintenance indicator on the drive itself.

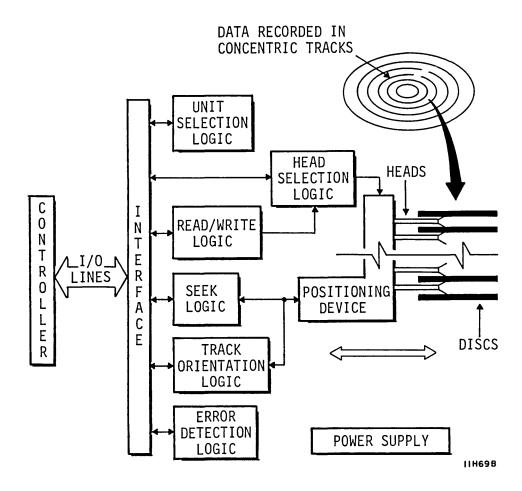
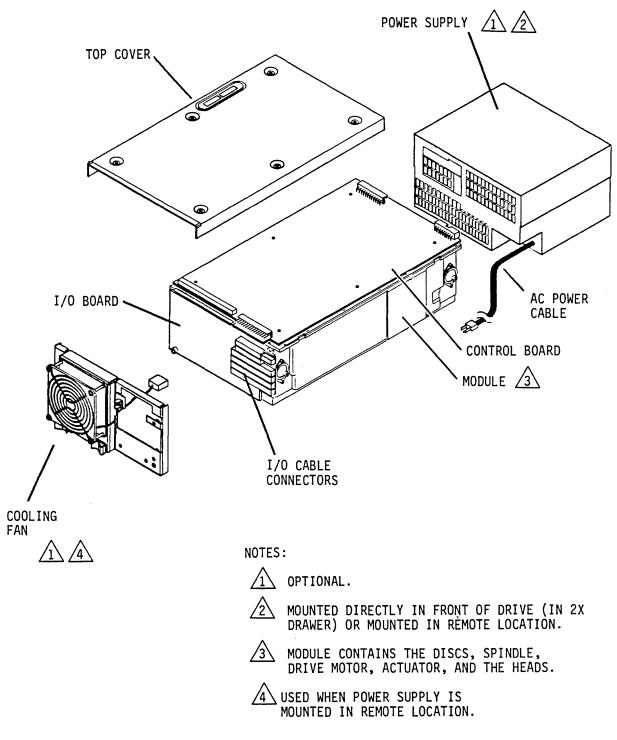


Figure 1-1. Drive Functional Block Diagram

EQUIPMENT PHYSICAL DESCRIPTION

The following paragraphs provide a physical description of the drive. The components mentioned in this discussion are identified in figure 1-2.

A drive installation requires a drive, interconnecting cabling, and a power supply. Site power enters the power supply via the ac power cable. The power supply develops the dc voltages required by the drive. These voltages are supplied to the drive by the dc power cable.



IIH70C

Figure 1-2. Drive Major Assemblies (Sheet 1 of 2)

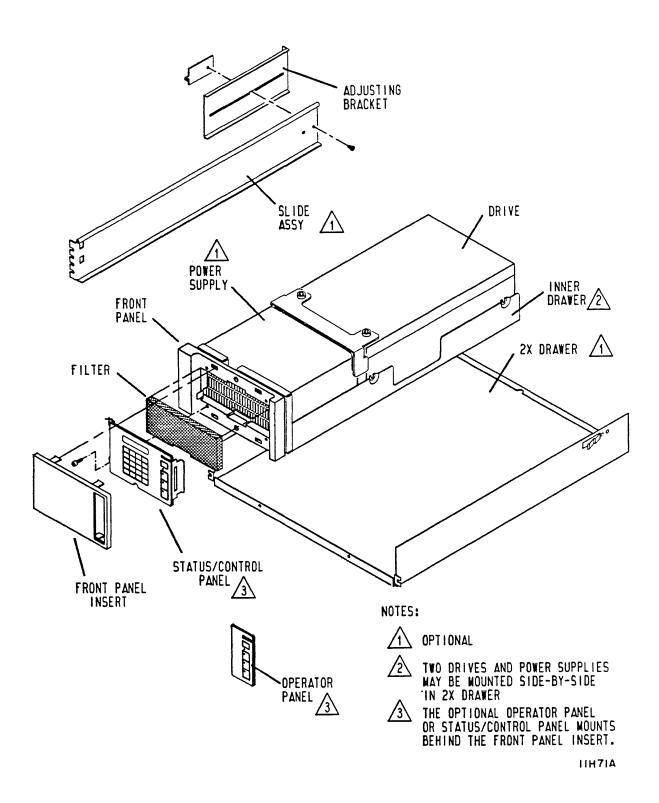


Figure 1-2. Drive Major Assemblies (Sheet 2)

The drive package includes a top cover, rear panel, module, and two circuit boards. Air flow is provided by a cooling fan. The cooling fan is an integral part of the optional power supply, and provides cooling air to the drive when the power supply is mounted directly in front of the drive. When the power supply is mounted in a remote location, drive cooling is provided by an optional fan and rear panel assembly.

Two optional panels provide external control of the drive. The operator panel contains basic switches and indicators for the operator. The status/control panel contains these same switches and indicators and, for troubleshooting, a diagnostic keyboard and a liquid crystal display. These panels can be mounted either inline with the drive and power supply or in a remote location.

The drive's internal components include two circuit boards and a module. The circuit boards are mounted on the module, and they contain the electronics required for drive operation. The module is a sealed unit containing the electromechanical components used for data storage and retrieval. These components include the discs, spindle, drive motor, actuator and heads.

The nine discs provide the recording medium for the drive. These discs are mounted on a spindle, and the spindle is coupled directly to the drive motor. When activated, the drive motor rotates the discs at 3600 r/min and also produces a circulation of air within the sealed module.

The actuator is the assembly that holds the heads and moves the heads over the rotating discs. There are 16 heads; a servo head to sense actuator positioning, and 15 data heads used for data transfers to and from the discs. The actuator has a voice coil that rotates in and out of a permanent magnetic field in response to signals from the servo positioning circuitry. The voice coil moves the heads in an arc across the rotating discs. When the drive is not in use, the heads rest on the disc surface in the preassigned landing zone (beyond the data zone). The actuator is automatically latched in this position at shutdown for moving or shipping protection. When the drive is activated to bring the discs up to speed, the heads fly on a cushion of air close to the disc surface.

The drive may be mounted either in a cabinet or rack. An optional 2X drawer with a front panel for each drive is available for mounting two drives and power supplies side-by-side.

A complete listing of field-replaceable parts is given in the parts data manual. Refer to the theory manual for theory of operation of drive components.

EQUIPMENT CONFIGURATION

The equipment configuration is identified by the equipment identification label and by the Equipment Configuration Log. It is necessary to identify the equipment configuration to determine if the manuals being used are applicable to the equipment.

EQUIPMENT IDENTIFICATION

The equipment is identified by labels attached to the drive top cover and to the power supply. The label on the drive top cover identifies the basic mechanical and logical configuration of the drive at the time it leaves the factory. To maintain drive configuration whenever the top cover is removed, always identify which drive the top cover was removed from. The label on the power supply references the components making up the drive installation and lists the site power requirements for the power supply. The information contained on these labels includes the following:

- Seagate Model Number -- Identifies a basic family of drives having the same capacity, form factor, and interface. These numbers are defined in the front of the manual. (Example = ST81236J)
- Equipment Identification Number -- Indicates the basic functional capabilities of the drive and unique characteristics that are necessary to adapt a drive to specific system requirements. A group of drives with the same Seagate model number may have different equipment identification numbers. (Example = PA8N2A)
- Series Code -- Represents a time period within which a unit is built. All units are interchangeable at the system level, regardless of series code; however, parts differences may exist within units built in different series codes.
- Part Numbers -- Several part numbers are used to designate the drive with its options and the drive alone. They are defined in the parts data manual.
- Serial Number -- Serial numbers are assigned sequentially within a family of drives.

EQUIPMENT CONFIGURATION LOG

Engineering Change Orders (ECOs) are electrical or mechanical changes that are performed at the factory. When the factory installs an ECO early (prior to a series code change), it is logged on the unit's configuration log.

Field Change Orders (FCOs) are electrical or mechanical changes that may be performed either at the factory or in the field. FCO changes do not affect the series code but are indicated by an entry on the Equipment Configuration Log that accompanies each machine. The components of a unit with an FCO installed may not be interchangeable with those of a unit without the FCO; therefore, it is important that the you enter the FCO on the Equipment Configuration Log when you install the FCO.

Throughout the life cycle of a machine, changes are made, either in the factory build (a series code change) or by FCOs installed in the field. All of these changes are also reflected in changes to the manual package.

• 1-10 GENERAL DESCRIPTION 83325710 G

Section 2 Operation

THE CONTROL OF THE CO

INTRODUCTION

This section provides the information and instructions to operate the drive. It is arranged as follows:

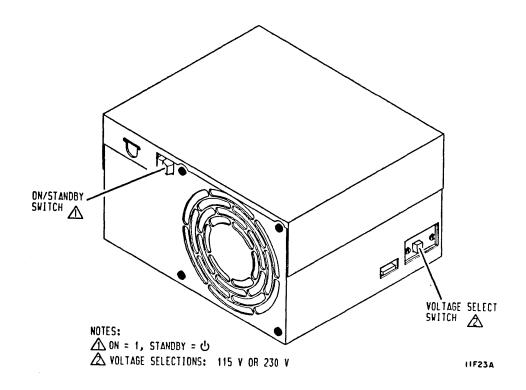
- Switches and Indicators -- Locates and describes the switches and indicators used for normal drive operation.
- Air Filter -- Describes filter maintenance for the drive operator.
- Operating Instructions -- Describes procedures for operating the drive.

SWITCHES AND INDICATORS

Switches and indicators used by the operator are on the power supply and the optional operator panel or status/control panel. Figure 2-1 shows these switches and indicators. They are explained in table 2-1.

As shown in figure 2-1, both the status/control panel and the operator panel provide identical sets of switches and indicators for the operator. In addition, the status/control panel has a diagnostic keyboard and liquid crystal display, intended for maintenance use. Refer to appendix A for a description of the maintenance features of the status/control panel.

Operation of a drive that does not have either type of panel is controlled by setting the appropriate switches on the control and I/O boards. LED indicators on the control board provide status on several drive conditions. Refer to section 3 for information on these switches and indicators. Normally the drive operator does not use them.



STATUS/CONTROL PANEL **OPERATOR** PANEL COVERED BY PANEL DIAGNOSTIC KEYBOARD LOGICAL ADDRESS INDICATORS 8 4 2 1 4 2 1 LOGICAL ADDRESS SWITCH LIQUID CRYSTAL DISPLAY UNIT SELECTED-ADORESS ADDRESS • SEL-● SEL READY INDICATOR , 0 2 3 START SWITCH-1 • FAULT INDICATOR-START START 5 7 4 6 FAULT CLEAR -STITCH 9 8 A В WRITE PROTECT-FAULT-FAULT INDICATOR F C D Ε WRITE PROTECT SVITCH BACK SPACE EXEC SPACE DIAGNOSTIC MODE SWITCH SPACE SWITCH DIAGNOSTIC EXECUTE BACKSPACE SWITCH SWITCH 11H72

Figure 2-1. Switches and Indicators

2-2 OPERATION 83325710 E

TABLE 2-1. DRIVE SWITCHES AND INDICATORS

Switch or Indicator	Function		
	POWER SUPPLY		
On/Standby Switch (1/也)	Applies dc operating voltages to the drive electronics and fan when placed in the On (1) position.		
OPERATOR	OPERATOR PANEL OR STATUS/CONTROL PANEL		
Logical Address Switch (ADDRESS)/ Indicators (8/4/2/1)	The Logical Address switch establishes the logical address of the drive. The switch must be pressed for 2 to 3 seconds to advance the logical address. Pressing it longer causes the address to increment continuously. The Logical Address Indicators display the logical address in binary. The logical address is stored in memory when dc power is removed.		
Unit Selected Indicator	Indicates that the drive is selected by a controller.		
START Switch/ Ready Indicator	The START switch has momentary action for Start and Stop, and it contains the Ready indicator. Pressing the START switch enables the power on sequence, and Ready indicator flashes (rapidly) until the discs are up to speed, the heads are loaded, and there are no fault conditions. The Ready indicator is on steady with power on complete.		
	Pressing the START switch to release it from the Start position causes the Ready indicator to flash (slowly) until disc rotation has stopped. The current condition of the START switch (Start/Stop) is stored in memory when dc power is removed.		
Table Continued on Page 2-5			



TABLE 2-1. DRIVE SWITCHES AND INDICATORS (Contd)

Switch or Indicator	Function			
OPERATOR PANEL OR STATUS/CONTROL PANEL (Contd)				
FAULT Indicator/ Fault Clear Switch	The FAULT indicator is inside the Fault Clear switch, and it lights if a fault exists within the drive. It is turned off by any of the following (provided that the error condition or conditions no longer exist):			
WRITE PROTECT Switch/Indicator	 Pressing the Fault Clear switch Fault Clear command from the controller A drive power on operation The operation of the WRITE PROTECT switch or the WRITE PROTECT maintenance switch on the control board places the drive in the write protected mode (preventing write operations) and lights the WRITE PROTECT indicator. The current position of the WRITE PROTECT switch is stored in memory when dc power is removed. 			

AIR FILTER

The air filter is located behind the operator panel or status/ control panel, as shown in figure 2-2. The air filter should be periodically inspected and either replaced or cleaned. Clean the filter only if a replacement filter is not available. The filter should be replaced about every six months in a computer room environment; replace it more often in a dirtier location.

CAUTION

Be careful not to damage system cabling when sliding drive in the drawer and drawer in and out of rack.

Remove front panel insert from each drive (see figure 2-2).

NOTE

Release latch on left-hand drive locks 2X drawer in rack. Release latch on right-hand drive has no function.

- 2. Push 2X drawer release latch to the right and extend 2X drawer to gain access to power supply.
- 3. Remove power from drive as follows:

For drives with operator panel or status/control panel:

- a. Press START switch to stop drive.
- b. Wait for Ready indicator (in START switch) to stop flashing; then, set On/Standby switch on power supply to Standby position.

For drives without either panel, set On/Standby switch on power supply to Standby position.

NOTE

If an operator panel is used, ignore steps 4 and 7. Filter may be replaced with operator panel in place.

4. Remove screws securing status/control panel to front panel and move status/control panel away from front panel to gain access to filter.

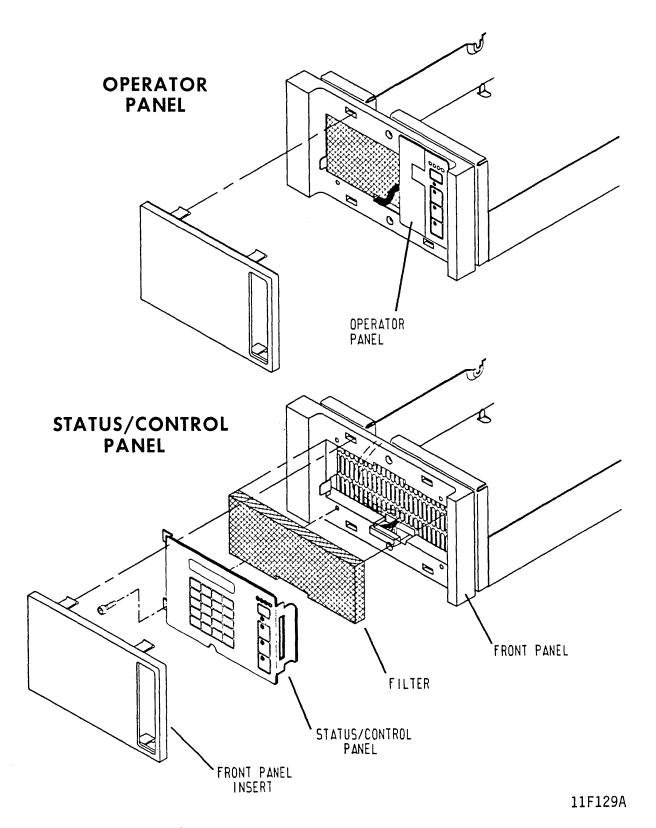


Figure 2-2. Air Filter Replacement

- 5. Remove dirty filter. If replacement filter is unavailable, clean dirty filter in solution of water and mild detergent. Rinse filter and allow it to dry.
- 6. Install clean filter.
- 7. Align status/control panel to front panel and secure with screws.
- 8. Set On/Standby switch on power supply to On (1) position.
- 9. Push 2X drawer back to closed position in rack.
- 10. Replace front panel insert.

2-8 OPERATION 83325710 B

OPERATING INSTRUCTIONS

For drives with no operator or status/control panel, there are no operating procedures. On these drives, when the R/L (Remote/Local) switch is placed in the Remote position, spindle power on/power off is handled by the controller. In Remote operation, the power on sequence is delayed. The length of delay is determined by the logical address number used, in increments of 5 seconds.

POWER ON PROCEDURE (DRIVES WITH OPERATOR PANEL OR STATUS/CONTROL PANEL)

This procedure describes how to turn on the drive. It is assumed that dc power is available to the drive because the power supply On/Standby switch is normally left in the On (1) position.

- 1. Press START switch to engage it in Start position.
 - If R/L (Remote/Local) switch on I/O board was set in Local position, power on sequence begins immediately.
 - If R/L (Remote/Local) switch on I/O board was set in Remote position, power on sequence continues when power sequence Pick or Hold (ground) signal is available from controller. With R/L (Remote/ Local) switch in Remote position, power on sequence to each drive is delayed. Length of delay is determined by logical address number used, in increments of 5 seconds.

For example: Logical Address 0 = 0 second delay Logical Address 7 = 35 second delay

- 2. After delay is completed, observe that Ready indicator (located in START switch) flashes rapidly, indicating that power on is in progress.
- 3. Observe that Ready indicator lights steadily within 90 seconds, indicating that discs are up to speed and heads are loaded.
- 4. Ensure that FAULT indicator is off.

The power on sequence is now complete, and the drive is ready to receive commands from the controller.

POWER OFF PROCEDURE (DRIVES WITH OPERATOR PANEL OR STATUS/CONTROL PANEL)

This procedure describes how to turn off the drive.

- 1. Press START switch to release it from Start position to start power off sequence. If R/L (Remote/Local) switch is in Remote position, power down starts when sequence pick or hold is dropped by controller.
- 2. Observe that Ready indicator (located in START switch) flashes slowly, indicating that power off is in progress.
- 3. Observe that Ready indicator goes off within 60 seconds, indicating that power off is complete.

With power off complete, the heads are positioned in the landing zone and the discs are not rotating. Normally, the power supply On/Standby switch is left On (1) to continue supplying dc power to the drive.

2-10 OPERATION 83325710 G

Section 3 Installation and Checkout

INTRODUCTION

The information contained in this section describes installation and initial checkout of the drive.

SITE REQUIREMENTS

The site requirements considered are electrostatic discharge protection, environment, space, power, grounding, and interface.

ELECTROSTATIC DISCHARGE PROTECTION

All drive electronic assemblies are sensitive to static electricity, due to the electrostatically sensitive devices used within the drive circuitry. Although some of these devices such as metal-oxide semiconductors are extremely sensitive, all semiconductors as well as some resistors and capacitors may be damaged or degraded by exposure to static electricity.

Electrostatic damage to electronic devices may be caused by a direct discharge of a charged conductor, or by exposure to the static fields surrounding charged objects. To avoid damage to drive electronic assemblies, service personnel must observe the following precautions when servicing the drive:

- Ground yourself to the drive whenever the drive electronics are or will be exposed. Connect yourself to ground with a wrist strap (refer to accessories in parts data manual for part numbers). Connection may be made to any metal assembly. As a general rule, remember that you, the drive, and the circuit boards must all be at ground potential to avoid potentially damaging static discharges.
- Keep boards in conductive bags when circuit boards are not installed in the drive, keep them in conductive static shielding bags (refer to accessories in parts data manual for part numbers). These bags provide absolute protection from direct static discharge and from static fields surrounding charged objects. Remember that these bags are conductive and should not be placed where they might cause an electrical short circuit.

- Remove boards from bags only when you are grounded all boards received from the factory are in static shielding bags, and should not be removed unless you are grounded.
- Turn off power to drive before removing or installing any circuit boards.
- Do not touch pins on power supply connector J15. Power supply circuitry is sensitive to electrostatic discharge.
- Never use an ohmmeter on any circuit boards.

ENVIRONMENTAL REQUIREMENTS

All environmental requirements for the drive are listed in table 3-1.

TABLE 3-1. ENVIRONMENTAL REQUIREMENTS

Conditions	Characteristics	Specifications		
TEMPERATURE				
Storage (Packaged)	Range	-10 to 50°C (14 to 122°F)		
	Maximum change per hour	15°C (27°F)		
Transit (Packaged)	Range	-40 to 60°C (-40 to 140°F)		
	Maximum change per hour	20°C (36°F)		
Table Continued on Next Page				

TABLE 3-1. ENVIRONMENTAL REQUIREMENTS (Contd)

Conditions	Characteristics	Specifications			
	TEMPERATURE (Contd)				
Operating	Range	10 to 45°C (50 to 113°F)			
	Maximum change per hour	15°C (27°F)			
	RELATIVE HUMIDITY				
Storage (Packaged)	Range	5% to 95%			
Transit (Packaged)	Range	5% to 95%			
Operating	Range	20% to 80% (no condensation allowed).			
BAROMETRIC PRESSURE (STANDARD DAY)					
Storage (Packaged)	Range	-305 m to 3000 m (-1000 ft to 10 000 ft) 104 kPa to 69 kPa (30 in to 20 in Hg)			
Transit (Packaged)	Range	-305 m to 12 192 m (-1000 ft to 40 000 ft) 104 kPa to 19 kPa (30 in Hg to 6 in Hg)			
Operating	Range	-305 m to 3000 m (-1000 ft to 10 000 ft) 104 kPa to 69 kPa (30 in Hg to 20 in Hg)			

SPACE REQUIREMENTS

The drive and power supply mount side-by-side with another drive and power supply into a 2X drawer, and the 2X drawer slide mounts into a 483 mm (19 in) standard rack. The slide action allows outward extension of the drawer for ease of maintenance. The space requirements are shown in figure 3-1.

The combined mass of the drive and power supply is 18.4 kg (40.8 lb).

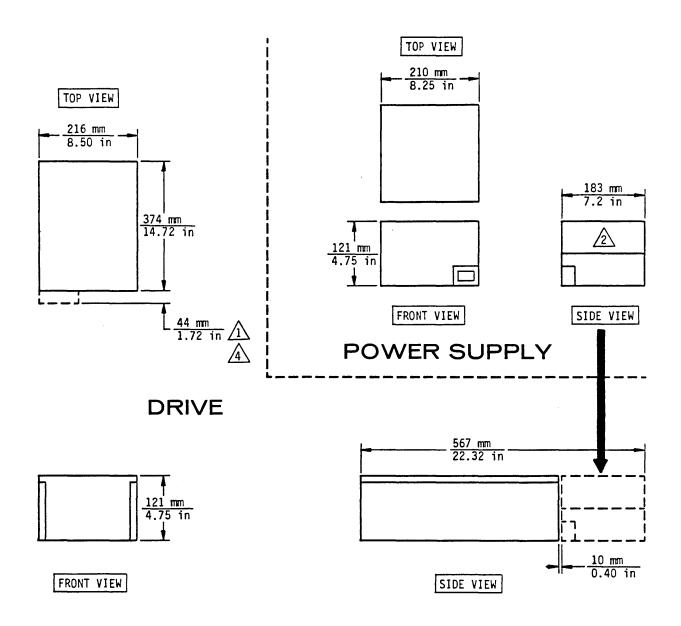
POWER REQUIREMENTS

An ac power cord used in the United States should be UL-listed. It should use Basic Cord Type SV (or UL-accepted equivalent). This cord must have copper conductors with a minimum gauge of 18 AWG. The following NEMA connector plugs are required:

- Type 5-15P for 120 volts
- Type 6-15P for 208 through 240 volts.

If you are installing the drive in a nation outside the United States, you should use a power cord that is acceptable in that nation and that is certified by authorities accepted in that nation.

• 3-4 INSTALLATION AND CHECKOUT 83325710 G



COOLING FAN MOUNTS ON REAR PANEL WHEN POWER SUPPLY IS NOT MOUNTED IN LINE WITH DRIVE.

2 COOLING FAN IS INSIDE POWER SUPPLY.

3. DIMENSIONS ARE NOMINAL.

DIMENSION IS 10 mm (0.37 in) LONGER ON DRIVES WITH VOLTAGE CONVERTER MOUNTED ON FAN BRACKET.

IIH95A

Figure 3-1. Drive Space Requirements

WARNING

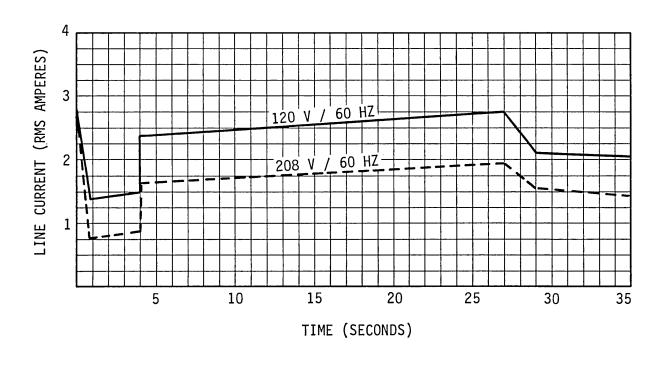
This unit has a single phase power supply with a capacitor input filter (sometimes called a switching type supply). If power comes from a 3-phase, 4-wire, wye branch or feeder circuit, ensure the circuit meets the latest requirements of the United States National Electrical Code. Failure to meet these requirements may cause hazardous conditions due to high currents and heating in the neutral conductors and transformers supplying the unit.

Drive ac power requirements are listed in table 3-2. Conversion to the different line voltages is explained in the installation procedures. Typical drive current versus start-up time is shown in figure 3-2 for 100-120 and 208-240 volt connections.

TABLE 3-2. POWER REQUIREMENTS

	Nominal Values		
Specifications	100 - 120 V ac	208 - 240 V ac	
Voltage Range	85 to 132 V	177 to 264 V	
Nominal Line Frequency	50/60 Hz	50/60 Hz	
Frequency Range	48.0 to 62.0 Hz	48.0 to 62.0 Hz	
Phase Requirements	Single Phase	Single Phase	
Power Consumed*	0.147 - 0.153 kW	0.149 - 0.144 kW	
Line Current*	2.6 - 2.3 A	1.6 - 1.4 A	
Power Factor*	0.57 - 0.56	0.46 - 0.43	
Start Up Current	See figure 3-2.	See figure 3-2.	

^{*}Measured at nominal values when discs are rotating and carriage is moving.



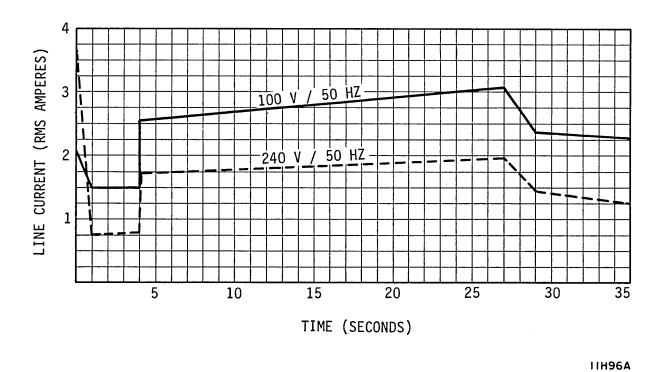


Figure 3-2. Typical Line Current Versus Start-up Time

GROUNDING REQUIREMENTS

Safety grounding (connecting the drive power cord to a grounded outlet) and system grounding (establishing a common ground between the drives, the power supplies, and the controller) are discussed in the following paragraphs.

Safety Grounding

A safety ground must be provided by the site ac power system. The green (or green and yellow striped) wire in the drive's power cord provides the safety ground connection between the power supply and the site power system. In turn, the site ac power system must tie this connection (safety ground) to earth ground. All site ac power connection points, including convenience outlets for test equipment, must be maintained at the same safety ground potential.

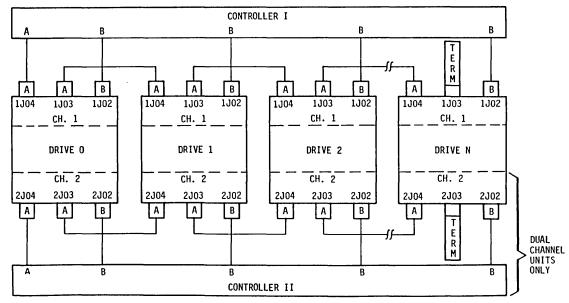
System Grounding

In addition to safety grounding, system ground connections are also required. System ground is established by a set of ground straps connected in a star or daisy chain configuration. The ground straps connect ground on the controller to earth ground and to each drive in the system. The installation procedures in this section provide detailed grounding instructions and a schematic diagram of the star and daisy chain configurations.

INTERFACE REQUIREMENTS

An important part of site preparation is planning the layout and routing of I/O cables. The I/O cables are designated as A and B cables. The A cables may be connected in either a daisy chain (figure 3-3) or star (figure 3-4) configuration. Each configuration calls for the use of terminators.

The following discussion of the I/O configurations applies to single channel installations where a set of drives are interfaced to one controller. Extending the discussion to dual channel installations (involving two controllers) requires doubling the quantities of cables and terminators because the two channels have independent cabling.



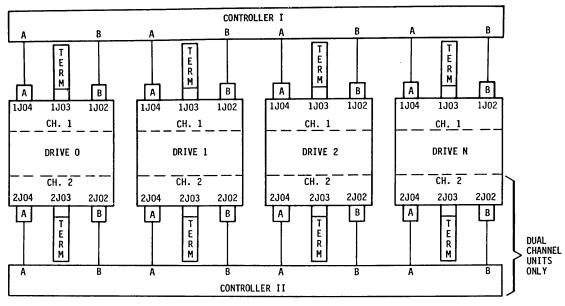
- 1. MAXIMUM CUMULATIVE A CABLE LENGTHS = 100 FEET
- 2. MAXIMUM INDIVIDUAL B CABLE LENGTHS = 50 FEET
 3. A SYSTEM MAY INCLUDE UP TO 16 DRIVES

11H82

Figure 3-3. Daisy Chain Configuration

The daisy chain configuration has individual B cables going from the controller to each drive. However, a single A cable connects the controller to the first drive. Other A cables go from drive to drive. The last drive in the string has a terminator installed on it. Use this configuration if the controller has only one A cable connector to serve the entire drive string.

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- 1. MAXIMUM INDIVIDUAL A CABLE LENGTHS = 100 FEET
 2. MAXIMUM INDIVIDUAL B CABLE LENGTHS = 50 FEET
 3. A SYSTEM MAY INCLUDE UP TO 16 DRIVES

11H83

Figure 3-4. Star Configuration

The star configuration has individual A and B cables going from the controller to each drive. Each drive has a terminator installed on it. Use this configuration if the controller has a separate A cable connector for each drive.

3-10 83325710 B INSTALLATION AND CHECKOUT

In estimating the I/O cables needed for an installation, decide on the configuration that will be used and allow sufficient length to permit extension of rack-mounted drives. Limitations on I/O cable lengths may influence system layout. The maximum length for each B cable is 15.3 m (50 ft). Each star system A cable or the cumulative A cabling in a daisy chain system cannot exceed 30.6 m (100 ft) in length. Refer to accessories in parts data manual for terminator and I/O cable part numbers.

Figure 3-5 shows the pin assignments and signal names for the A cable. Figure 3-6 shows the pin assignments and signal names for the B cable. Detailed information about interface lines is given in the theory manual.

UNPACKAGING, INSPECTION AND REPACKAGING

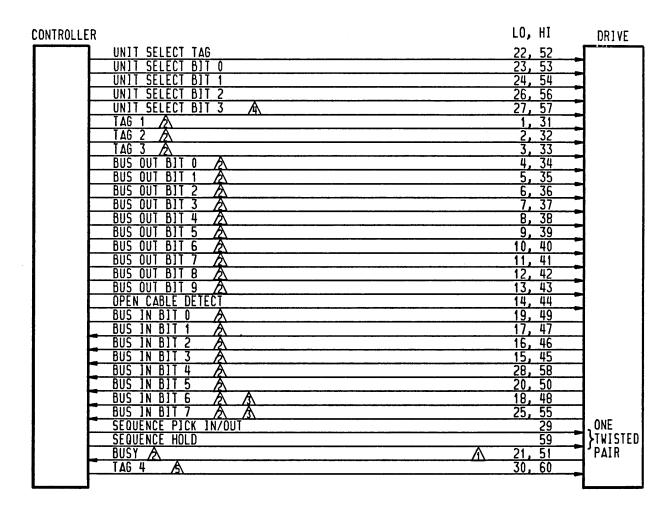
After removing packaging material according to the unpackaging instructions provided with the drive, inspection for shipping damage should be carried out and several final unpackaging procedures performed. Save all packaging materials for future use.

UNPACKAGING

- 1. After the drive has stablized to the ambient temperature of the room, open package (save all packaging materials).
- If drive has a 2X drawer with slide mount option, remove packages containing drawer, two slide mounts and slide mount hardware kit.
- 3. Remove package containing ac power cable and dc power cable.
- 4. Open sealed vapor barrier bag and remove drive and power supply.
- 5. Check all items against shipping bill for required equipment and hardware to complete installation. Discrepancies, missing items, damaged equipment, etc., should be reported to the Seagate account sales representative responsible for the equipment.

INSPECTION

Inspect the drive, power supply, and accessory items for possible shipping damage. All claims for shipping damage should be filed with the carrier involved.



1 DUAL CHANNEL UNITS ONLY

/2\ GATED BY UNIT SELECT

/3\ INDEX AND SECTOR MAY BE IN "A" CABLE, "B" CABLE, OR BOTH.

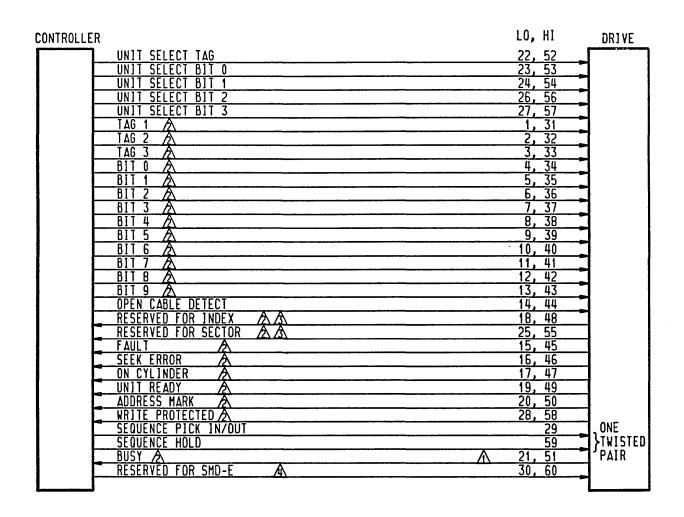
4 FUNCTIONS AS TAG 5 LINE FOLLOWING UNIT SELECTION.

FUNCTIONS AS CYLINDER ADDRESS BIT 210 LINE, WHEN OPERATING WITH TAG 1 EXTENDED ADDRESSING FEATURE.

SMD-E SIGNAL DEFINITIONS

11F43

Figure 3-5. A Cable (Sheet 1 of 2)



⚠ DUAL CHANNEL UNITS ONLY

⚠ GATED BY UNIT SELECT

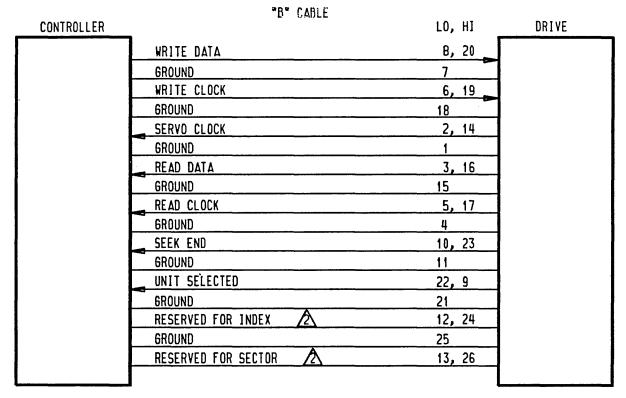
INDEX AND SECTOR MAY BE IN "A" CABLE, "B" CABLE, OR BOTH.

FUNCTIONS AS CYLINDER ADDRESS BIT 2¹⁰ LINE WHEN OPERATING WITH TAG 1 EXTENDED ADDRESSING FEATURE.

SMD-O SIGNAL DEFINITIONS

11F71

Figure 3-5. A Cable (Sheet 2)



NO SIGNALS GATED BY UNIT SELECTED

/2\ INDEX AND SECTOR MAY BE IN "A" CABLE, "B" CABLE, OR BOTH

11F44

Figure 3-6. B Cable

REPACKAGING

If it is necessary to ship the drive, repackage the drive with the original packaging materials (saved during installation). Comply with the manufacturer's packaging instructions to ensure that the drive will be undamaged in shipment. To obtain packaging instructions, contact:

> Seagate Technology, Inc. Customer Services 12701 Whitewater Drive Minnetonka, MN 55343

Phone: 1-800-382-6060 Fax: (612) 931-8817

When ordering packaging instructions, specify the exact equipment number and series code of the drive as shown on the equipment identification label.

INSTALLATION PROCEDURES

GENERAL

With the site requirements completed and the drive unpackaged, you are ready to begin the installation. Certain parts of the installation may vary -- how the drive and power supply are mounted, how dc power is supplied to the drive, and whether an operator panel or status/control panel is used. For those installations where the drive and power supply are mounted in a 2X drawer, the following procedures apply:

- Mounting 2X Drawer in Rack
- Mounting Front Panel
- Installing Operator Panel or Status/Control Panel Jumper Cable
- Mounting the Status/Control Panel
- Mounting the Operator Panel
- Mounting Drive and Power Supply in 2X Drawer

For some customers, the drive, power supply, and operator panel (if used) are mounted in an inner drawer prior to shipment. In this case, the following procedures apply:

- Mounting 2X Drawer in Rack
- Mounting Drive and Power Supply in 2X Drawer

For those installations where a 2X drawer is not used, the topic Alternate Methods for Installing the Drive provides some basic information. However, specific details of alternate mounting are beyond the scope of this manual.

There is another group of procedures that apply to all installations. They are:

- Power Supply Voltage Conversion
- System I/O Cabling
- System Grounding
- Circuit Board Switches and Indicators

In most cases, you will find it convenient to perform the procedures in the order they are presented in the manual. However, you might find it convenient to make switch settings on both the drive and the power supply before mounting them.

3 - 16

POWER SUPPLY VOLTAGE CONVERSION

The power supply is configured before shipment to operate in one of two ac input voltage ranges. The voltage select plate on the power supply (see figure 3-7) indicates the voltage range selected prior to shipment. The voltage range is determined by setting the voltage select switch to the desired range.

CAUTION

Power supply could fail if voltage select switch is not matched with input ac voltage.

- 1. Ensure that ac power cable is disconnected from power supply.
- 2. Remove screw securing voltage select plate to power supply and remove plate from power supply. Retain plate and hardware.

NOTES: LOCKS VOLTAGE SWITCH IN EITHER 115V POSITION OR 230V POSITION (OTHER SIDE) 115 V 230 V 0 **VOLTAGE SELECT** PLATE ON/STANDBY VOLTAGE SWITCH **SELECT** (ON = 1)SWITCH STANDBY = 也) (115V OR 230V) 11F47C

Figure 3-7. Power Supply Voltage Conversion

- 3. Set voltage select switch to desired range.
- 4. Reverse voltage select plate and install plate on power supply to lock switch in desired range.
- 5. Replace existing ac power cable with ac power cable specified for new operating voltage.

The ac power cord must be replaced if the voltage range is changed. Refer to figure 3-8 and to the parts data manual for information about ordering the replacement ac power cable.

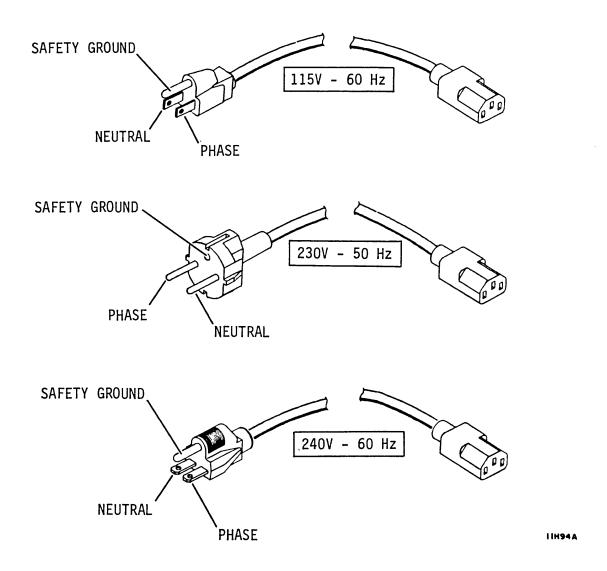


Figure 3-8. AC Power Cables

83325710 C INSTALLATION AND CHECKOUT 3-17

MOUNTING 2X DRAWER IN RACK

The slide assemblies permit inline mounting of the 2X drawer in a rack. The drawer may be extended out the front surface of the rack for maintenance. The following procedure provides instructions for attaching the 2X drawer to the rack.

- Remove screws from rack mounting kit. There are four smaller screws in kit. Two screws attach slide adjusting brackets to slides (step 2). Two others attach inner drawers to 2X drawer (see Mounting Drive and Power Supply in 2X Drawer).
- Loosely attach a slide adjusting bracket to each slide with a bracket clamp and screw (see figure 3-9).
- 3. Loosely attach screws and nut plates (for each slide) to rack as follows (see figure 3-9):
 - For rack front, screws go in top and third holes.
 - For rack rear, screws go in top and bottom holes.
- 4. Set slide adjusting brackets as required for proper rack depth. Mount right-hand and left-hand slides in rack in accordance with user requirements. Orient slides so that 2X drawer rests on flat edge of slides as shown in figure 3-9.
- 5. Secure adjusting brackets to slides and slides to rack.
- 6. Lift drawer and guide it into slide assemblies. Continue pushing until drawer is in rack. Ensure that there is no binding when sliding 2X drawer in and out of rack.

If it is necessary to remove drawer from rack, slide drawer out to full extension. Press drawer locking springs and remove drawer from rack.

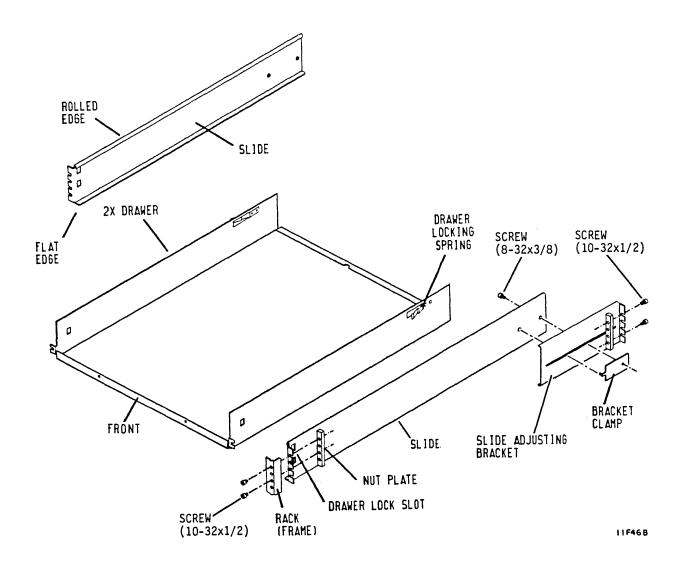


Figure 3-9. 2X Drawer Installation

83325710 E INSTALLATION AND CHECKOUT 3-19

MOUNTING FRONT PANEL

If an operator panel or status/control panel will be installed, skip this procedure and go on to the next procedures, which include front panel mounting instructions.

NOTE

When installing front panel, ensure that overhang (wide edge) covers rack frame (vertical support).

- Align top edge of front panel to top edge of inner drawer (see figure 3-10).
- 2. Secure front panel into place with four screws.
- 3. Install filter.
- 4. Remove adhesive backing and attach filler plate to front panel insert.
- 5. Install front panel insert.

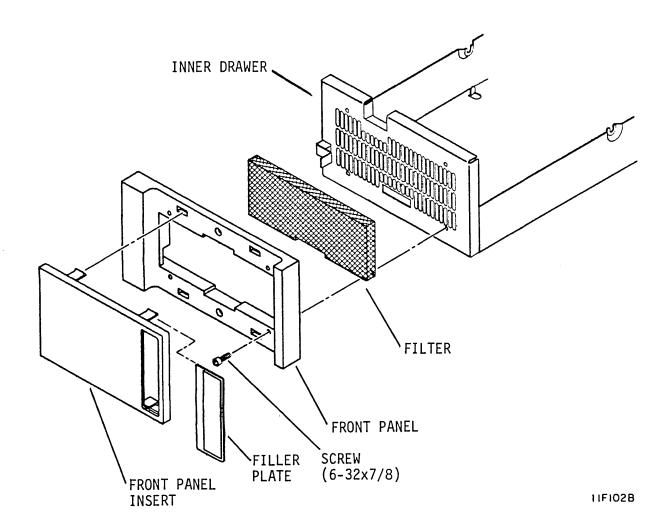


Figure 3-10. Mounting Front Panel

INSTALLING OPERATOR PANEL OR STATUS/CONTROL PANEL JUMPER CABLE

During a new installation, installing the jumper cable is quite simple as nothing has been mounted in the inner tray. However, to install the jumper cable in an existing installation, the drive and power supply must be removed from the inner drawer. This requires you to remove the inner drawer from the 2X drawer.

New Installation

- Attach two metal cable clamps (found in operator panel kit or status/control panel kit) to exposed shielding on jumper cable. One cable clamp is longer than the other. Attach longer cable clamp to J13 end of jumper cable and shorter cable clamp to P13 end of jumper cable.
- Attach cable clamp at P13 end of jumper cable to inner drawer using a screw and lockwasher. See figure 3-11.
- 3. Route jumper cable around left side of inner drawer.

Existing 2X Drawer Installation

1. Remove front panel insert from each drive.

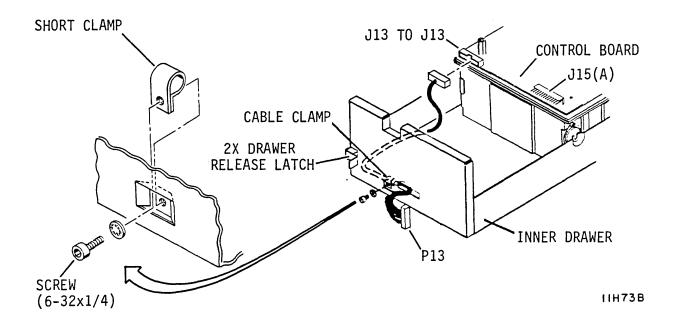


Figure 3-11. Installing Jumper Cable

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2. Remove filter.

NOTE

The release latch on left-hand drive locks 2X drawer in rack. The release latch on right-hand drive has no function.

- 3. Push 2X drawer latch to the right and extend 2X drawer to fully extended position. See figure 3-11.
- 4. Set On/Standby switch on power supply to Standby position.
- 5. Push 2X drawer back to closed position in rack.

NOTE

In the following steps, the I/O cables will remain clamped to cable bracket.

6. Remove I/O cover from I/O cable bracket. On older drives, which use a grounding clamp and a strain relief clamp, the grounding clamp is removed with the I/O cover.

CAUTION

Remove terminators by hand. They could be damaged if a pliers or other tool is used.

- 7. Disconnect I/O cables, terminators, and system ground strap from drive.
- 8. Remove I/O cable bracket (with I/O cables attached) from drive rear panel.
- 9. Disconnect ac power cable from site power.
- 10. Push 2X drawer release latch to the right and extend 2X drawer to fully extended position.
- 11. Remove inner drawer locking screw.
- 12. Push 2X drawer release latch to the right, lift inner drawer up and remove it from 2X drawer. Place inner drawer on work table.
- 13. Remove screws securing air baffle to top cover. See figure 3-14.
- 14. Loosen shock mount screws securing module to inner drawer.

- 15. Lift front end of drive up enough to disconnect dc power cable from J15(A) on control board.
- 16. Disconnect dc ground strap from module.
- 17. Carefully lift drive out of drawer and move to desired location.
- 18. Remove screws securing power supply to inner drawer.
- 19. Disconnect ac power cable from ac input connector Jl while removing power supply from inner drawer.
- 20. Remove front panel from inner drawer.
- 21. Attach two metal cable clamps (found in operator panel kit or status/control panel kit) to exposed shielding on jumper cable. One cable clamp is longer than the other. Attach longer cable clamp to J13 end of jumper cable and shorter cable clamp to P13 end of jumper cable.
- 22. Attach cable clamp at P13 end of jumper cable to inner drawer using a screw and lockwasher. See figure 3-11.
- 23. Route jumper cable around left side of inner drawer.
- 24. Perform procedure to mount either status/control panel or operator panel on front surface of inner drawer.

NOTE

Because this is not a new installation, some of the steps in the following procedure are already done.

- 25. Perform procedure for mounting drive and power supply in 2X drawer.
- 26. Attach I/O cable bracket to drive rear panel.
- 27. Connect I/O cables, terminators, and system ground strap to drive.
- 28. Attach I/O cover (and grounding clamp on older drives) to I/O cable bracket.
- 29. Push 2X drawer latch to the right and extend 2X drawer far enough to set On/Standby switch on power supply to On (1) position.
- 30. Push 2X drawer back to closed position in rack.

Mounting The Status/Control Panel

Refer to figure 3-12 and perform the following steps to install an operator panel:

NOTE

When installing front panel, ensure that overhang (wide edge) covers rack frame (vertical support).

- 1. Align front panel to inner drawer so that holes in front panel are centered on holes in inner drawer.
- 2. Secure front panel into place with two screws on right side of front panel.
- 3. Install filter.
- 4. Connect jumper cable to P13 on status/control panel.
- Align status/control panel to front panel and secure with screws.
- 6. Remove protective film from front surface of status/control panel.

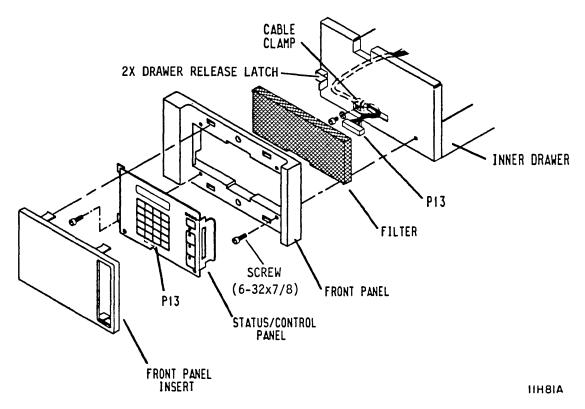


Figure 3-12. Mounting the Status/Control Panel

Mounting The Operator Panel

Refer to figure 3-12 and perform the following steps to install an operator panel:

NOTE

When installing front panel, ensure that overhang (wide edge) covers rack frame (vertical support).

- 1. Align front panel to inner drawer so that holes in front panel are centered on holes in inner drawer.
- 2. Secure front panel into place with two screws on left side of front panel.
- 3. Install filter.
- 4. Remove screws attaching operator panel to shield.
- 5. Align shield to front panel and secure with screws.
- 6. Connect jumper cable to P13 on operator panel.
- 7. Align operator panel to shield and secure with screws.

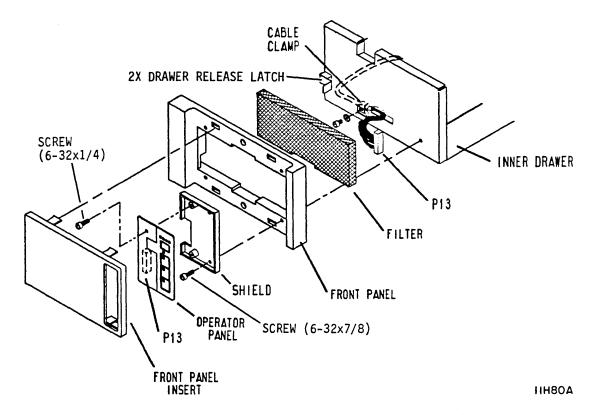


Figure 3-13. Mounting the Operator Panel

MOUNTING DRIVE AND POWER SUPPLY IN 2X DRAWER

You may find it convenient to make normal switch settings on the drive and power supply (described later in this section) before mounting them in the 2X drawer. See figure 3-14.

If your drive and power supply were mounted in the inner drawer prior to shipment, skip to step 11 of this procedure.

NOTE

For drives with an operator panel or status/control panel, ensure that P13 jumper cable does not interfere with power supply mounting. Cable is routed around left side of power supply (see figure 3-11).

- Place power supply into position in inner drawer and secure with screws.
- Connect dc power cable to J15 on power supply.
- 3. Orient ground strap as shown in figure 3-14, and connect ground strap to ac ground terminal on power supply. Ensure that lock washer is placed between ground strap and power supply case.

NOTE

If short ac power cable is not used, ignore steps about clamping cable shield to inner drawer (steps 4a and 4e).

- 4. Connect short ac power cable as follows:
 - a. Remove heat shrink from short ac power cable to expose shield in area shown in figure 3-14.
 - b. Connect cable to ac input connector J1 on power supply.
 - c. Route cable inside inner drawer as shown in figure 3-14.
 - d. Place cable behind tabs in inner drawer. Exposed shield must be behind tab as shown.
 - e. Place clamp over tab and shield. Bend tab to firmly secure clamp.
- 5. Loosely attach shock mount screws to drive shock mounts.

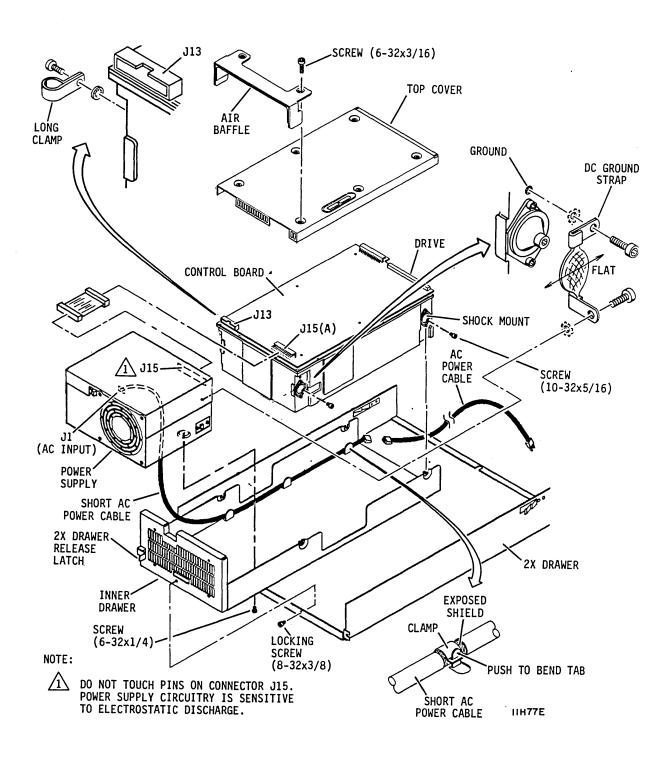


Figure 3-14. Mounting Drive and Power Supply in 2X Drawer

3-28

- 6. Mount drive to inner drawer so that drive is supported on shock mount screws.
- 7. Lift front end of drive up enough to connect dc power cable to J15(A) on control board.
- 8. To complete installation of status/control panel or operator panel jumper cable, perform the following steps:
 - a. Attach cable clamp at J13 end of jumper cable with screw in hole just above left front shock mount. See figure 3-14. Ensure that lock washer is placed between clamp and module.
 - b. Lift front end of drive up enough to connect jumper cable end marked J13 to J13 on control board.
- 9. Attach ground strap to drive module. See figure 3-14.
- 10. Tighten shock mount screws to secure drive in place.
- 11. Slide inner drawer into position in 2X drawer and secure with locking screw.
- 12. Push 2X drawer back to closed position in rack.
- 13. Replace front panel insert.
- 14. Connect one end of ac power cable to short ac power cable and other end to site power.
- 15. Remove two top cover screws and install air baffle as shown in figure 3-14.

ALTERNATE METHODS FOR INSTALLING THE DRIVE

Any alternate method of mounting the drive in an enclosure must satisfy certain requirements regarding mechanical isolation and air flow. The mounting design must support the drive by its shock mounts, either horizontally or vertically. It must allow adequate clearance between the drive and any surfaces near the drive. The mounting design must also ensure adequate ventilation of the drive and power supply. Detailed mounting requirements are given in the drive's product specification. Before actually mounting the drive and power supply in the enclosure, you may find it convenient to set their switches for normal operation.

When the power supply mounting is remote from the drive, there are special requirements for the dc power cable, the ac power cable, and airflow.

3 - 29

With a remote power supply, a shielded dc power cable is recommended. Figure 3-15 shows how the cable connects to the power supply and the drive. One end of the cable connects to J15 on the power supply, and the trailing ground lead on that end connects to the ac ground terminal on the power supply. The other end of the cable goes to J15(B) on the drive. The trailing ground lead on that end connects to the ground screw below the I/O connectors.

When installing a remote power supply for 60 Hz operation, we recommend that you use a shielded ac power cable and connect the shield to the power supply case. As shown in figure 3-15, you must remove heat shrink from the short ac power cable to expose the shield. Then install a cable clamp on the shield and attach it to the ac ground screw on the power supply. Finally, connect one end of the ac power cable to the short ac power cable and the other end to site power.

When the power supply is mounted remotely, an optional auxiliary fan and rear panel may be installed to provide air circulation for the drive. See figure 3-15. The fan kit has the fan mounted so the airflow arrow (on the body of the fan) will point toward the drive. This blows cooling air through the drive and is the preferred method. If, however, the installation requires it, the fan may be reversed so that the airflow arrow points away from the drive. This draws warm air out of the drive.

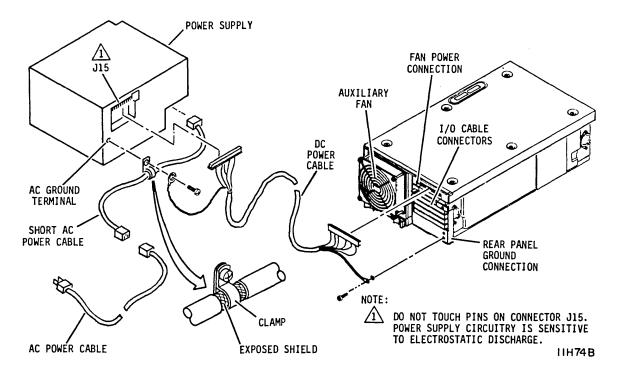


Figure 3-15. Cabling for a Remote Power Supply

Some drives operate from a power source supplying -12 V, +5V, and +24 V. They contain a voltage coverter that develops an additional supply voltage of -5 V from the -12 V input. As shown in figure 3-16, these drives have two dc power connectors, CN9 and CN10, located side-by-side above the I/O connectors. Following the directions provided with the power supply, connect the two dc power cables from the supply to CN9 and CN10. The following list provides pin assignments for these connectors:

Connector	Pin Number	<u>Signal</u>	
CN9	1 2	+5V +5V	
	3	+24 V return	
	4	+24 V return	
·	5	+24 V	
	6 7	Key	
	,	+24 V	
CN10	1	-12 return	
	2	Key	
	3 4	-12 V return -12 V	
	5	-12 V -12 V	
	6	+5 V return	
	7	+5 V return	
	REAR PANEL	CN9 CN10 PIN 1 PIN 1	
		THE PINE	
DC POWER CABLES			
	I/O CAB	N F	
	CONNECT	ORS	
6			11H160

Figure 3-16. Cabling for a Drive with a Voltage Converter

SYSTEM I/O CABLING

It assumed that the site has been prepared in accordance with the site requirements information provided earlier in this section. The following procedures describe how to cable the system in either a daisy chain or star configuration. These configurations were discussed earlier in this section under Interface Requirements.

The following procedures make the connections for one I/O channel. For dual channel installations, part of the procedure must be repeated. On dual channel drives there are two sets of I/O connectors: 1J02, 1J03, and 1J04 for channel 1, and 2J02, 2J03, and 2J04 for channel 2. The recommended connections are A cable to J04 and terminator to J03. These connections may be reversed without affecting drive operation. Figure 3-17 shows typical I/O cable connections.

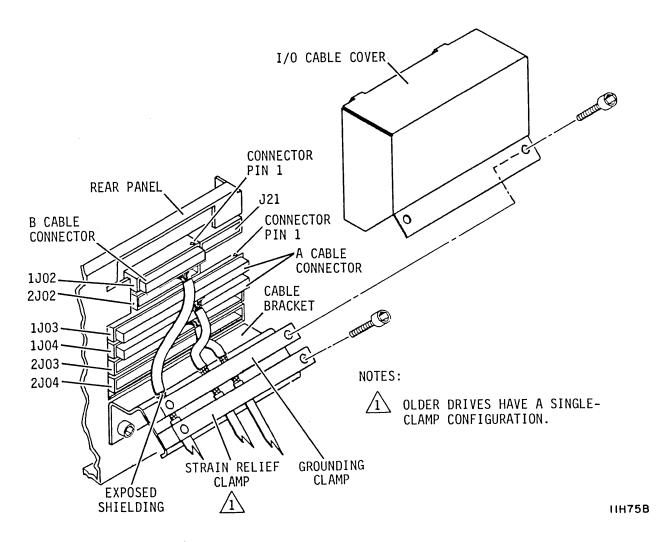


Figure 3-17. I/O Cable Attachment

Daisy Chain I/O Cabling Procedure

1. Attach cable bracket to rear panel, using two shorter screws found in hardware kit.

NOTE

Steps 2 through 5 apply to single channel drives and must be repeated for dual channel drives.

- Connect B cables from controller to connector JO2 on each drive.
- 3. Connect A cable from controller to connector J04 on first drive in daisy chain.

NOTE

If drive is not last drive in daisy chain, repeat step 4 until last drive is connected. When last drive is connected in daisy chain, perform step 5.

- 4. Connect another A cable from connector J03 to connector J04 on next drive in daisy chain.
- 5. Install terminator on drive connector J03 and make terminator ground connection (see figure 3-18). A terminator is required on last drive in a daisy chain system.
- Perform Cable Clamping Procedure (which appears several pages later in this topic) to ensure proper shielding of I/O cables.

Star I/O Cabling Procedure

1. Attach cable bracket to rear panel, using two shorter screws found in hardware kit.

NOTE

Steps 2 through 4 apply to single channel drives and must be repeated for dual channel drives.

Connect B cables from controller to connector JO2 on each drive.

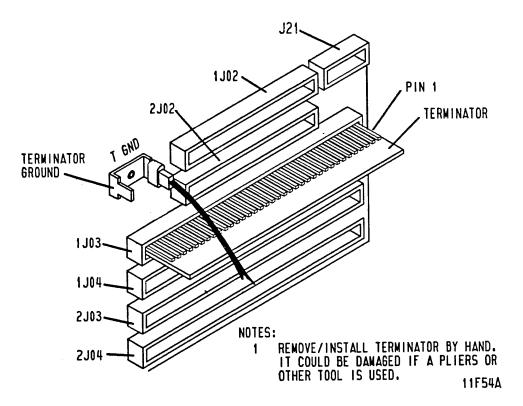


Figure 3-18. Terminator Installation (Typical)

NOTE

Repeat steps 3 and 4 for each drive in star system.

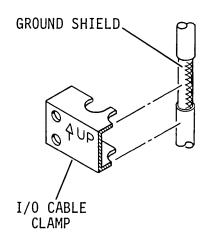
- 3. Connect an A cable from controller to drive connector J04.
- Install terminator on drive connector JO3 and make terminator ground connection (see figure 3-18).
 Terminators are required on all drives in a star system.
- 5. Perform Cable Clamping Procedure (which follows) to ensure proper shielding of I/O cables.

Cable Clamping Procedure

NOTE

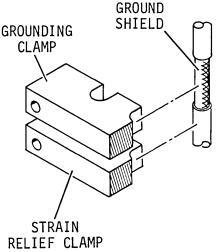
There are several locations on each I/O cable where heat shrink tubing can be removed to expose the ground shield. The ground shield should be exposed only where necessary to establish a proper ground.

- On drives with single-clamp (older) configuration, install I/O cable clamp as follows:
 - a. Strip heat shrink tubing from all cables so that bare shielding will be in contact with the top of the I/O cable clamp and will not be in contact with the bottom of the clamp.
 - b. Place I/O cable clamp onto cable bracket with cables and clamp positioned as shown. Loosely install two lower screws. Clamp must be installed with arrow pointing up.



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- c. Install I/O cable cover onto cable bracket with remaining two screws. Tighten two lower screws.
- 2. On drives with dual-clamp (newer) configuration, install I/O cable clamps as follows:
 - a. Strip heat shrink tubing from all cables so that bare shielding will be in contact with grounding clamp and not in contact with strain relief clamp.
 - b. Loosely install strain relief clamp (strain relief clamp has larger diameter openings than grounding clamp) onto cable bracket with cables positioned as shown.



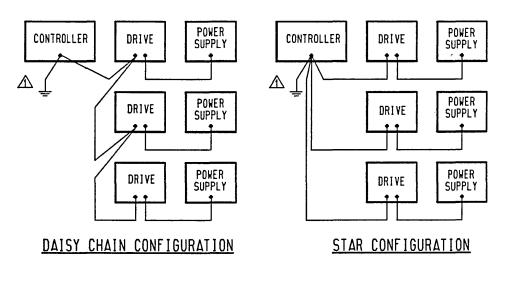
11D686

- c. Position cabling so that bare shielding begins just above strain relief clamp; then secure strain relief clamp into place with screws. This will ensure that grounding clamp (installed in following step) is in contact with bare shielding of cabling.
- d. Install grounding clamp and I/O cable cover onto cable bracket with cables positioned as shown in figure 3-16 and secure with screws. Ensure that bare shielding on each cable is in contact with grounding clamp.

SYSTEM GROUNDING

This section contains instructions on grounding the system to the drive. It is assumed that the site has been prepared in accordance with the site requirements information provided earlier in this section. The following procedures describe how to ground the system in a star or daisy chain configuration as shown in figure 3-19. Refer to accessories in parts data manual for part numbers of grounding accessories.

Interconnect cabling is supplied with each drive and installed on site, between case ground on each drive and case ground on its power supply. Refer to mounting drive and power supply procedure in this section for instructions on attaching ground cable between drive and power supply.



NOTES:

EARTH GROUND CONNECTION

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Figure 3-19. System Grounding Diagram

Star Grounding Procedure

In this configuration, ground straps connect the controller ground to each drive in the system as shown in figure 3-19.

- 1. Prepare ground straps as follows:
 - a. Allowing sufficient length for drive extension, cut ground straps to length needed for following connections:
 - Controller to earth ground
 - Controller to each drive
 - b. Crimp and solder terminal lugs to both ends of each ground strap.
- Referring to figure 3-19, connect ground straps to controller as follows:
 - a. Connect one end of each ground strap to controller ground terminal.
 - b. Connect one of the ground straps to earth ground.
 - c. Route remaining ground straps to drives.
- 3. Connect a ground strap from controller to each drive as follows:

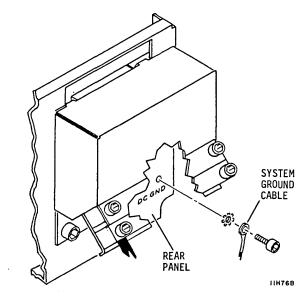


Figure 3-20. System Grounding

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- a. Remove screw and lockwasher from system ground terminal (DC GND) on each drive. See figure 3-20.
- b. Attach system ground strap to drive ground terminal. Ensure that lockwasher is between ground strap and ground terminal.

Daisy Chain Grounding Procedure

In this configuration, a ground strap connects the controller ground to the first drive in the system. The remainder of the drives are connected by grounding straps going from the first drive to the second, the second to the third, and so on. See figure 3-19.

- 1. Prepare ground straps as follows:
 - a. Allowing sufficient length for drive extension, cut ground straps to length needed for following connections:
 - Controller to earth ground
 - Controller to nearest drive
 - Each drive to next drive in daisy chain
 - b. Crimp and solder terminal lugs to both ends of each ground strap.
- 2. Referring to figure 3-19, connect ground straps to controller as follows:
 - a. Connect two ground straps to controller ground terminal.
 - b. Connect one ground strap to earth ground.
 - c. Route other ground strap to first drive in daisy chain. Route remaining ground straps (prepared in step 1) from drive to drive.
- 3. Make daisy chain ground connections at each drive as follows:
 - a. Remove screw and lockwasher from system ground terminal (DC GND) on each drive. See figure 3-20.
 - b. Attach system ground strap to drive ground terminal. Ensure that lockwasher is between ground strap and ground terminal.

CIRCUIT BOARD SWITCHES AND INDICATORS

The circuit boards inside the drive contain a number of switches that must be set correctly for normal operation of the drive. The following pictures and tables contain information about switch settings:

- Figure 3-21 -- shows pictures of actual DIP switches with instructions on setting them to either the open (off) or the closed (on) position.
- Figure 3-22 -- identifies switches and gives their locations on the I/O board.
- Table 3-3 -- lists the possible settings for normal drive operation for all switches on the I/O board.
- Figure 3-23 -- identifies various types of control boards.
- Figure 3-24 -- identifies switches and indicators on the control board.
- Table 3-4 -- lists the possible settings for normal drive operation for switches on the control board. However, setting the sector switches and logical address is covered later in this section.
- Table 3-5 -- describes the maintenance indicators.
- Figure 3-25 -- identifies jumpers on the control board.
- Table 3-6 -- lists the possible settings for normal drive operation for jumpers on the control board.

You may encounter two types of switches. Rocker switches are actuated by pressing one end of the actuator or the other (rocking it) to turn the switch on (closed) or off (open). Slide switches are actuated by sliding the actuator one way or the other to turn the switch on or off. Use a slender ball point pen, a straightened paper clip, or any similar object to change switch settings. Do not use a lead pencil point as it may break off and lodge in the switch, or cause the switch to malfunction.

To determine the type of control board in your drive, examine the arrangement of DIP switches on the board. By uncovering slots in the drive top cover, you can see the DIP switches. Figure 3-23 shows Type 1, Type 2, and Type 3 control boards.

Type 1 control boards have 3 groups of switches with 6 switches in each group. Type 2 and Type 3 control boards have 2 groups of switches, with 10 switches in each group. The main

difference between Type 2 and Type 3 boards is the location and function of jumpers that select certain features.

Setting the circuit board switches or seeing the maintenance indicators does not require removing the top cover. Both the top cover and the rear panel have openings to allow access to the DIP switches. On Type 3 control boards, the top cover opening also allows access to the jumpers. However, for Type 2 control boards, it is necessary to remove the top cover to examine or change the jumpers.

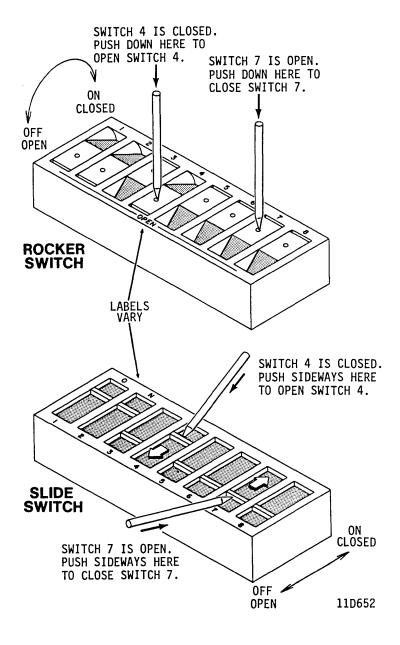


Figure 3-21. How to Set DIP Switches

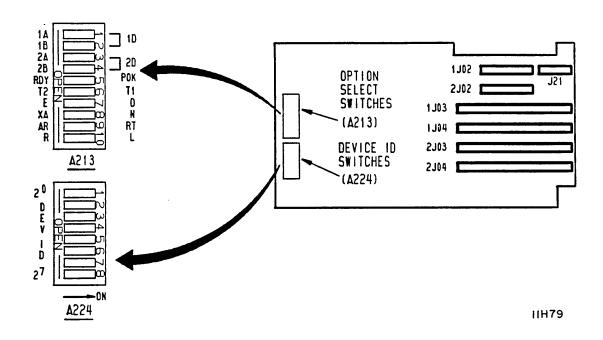


Figure 3-22. I/O Board Switches

TABLE 3-3. I/O BOARD SWITCHES

Switch	Setting	Description			
1A/1D, 1B/1D		Enable/Disable CH l and select an option for sending Index & Sector to CH l controller. The four combinations of switch settings are as follows:			
1A/1D 1B/1D	1A 1D	This pair of switch settings enables CH l & sends I & S on A cable only			
1A/1D 1B/1D	1D 1B	This pair of switch settings enables CH l & sends I & S on B cable only			
1A/1D 1B/1D	1A 1B	This pair of switch settings enables CH l & sends I & S on A and B cables			
1A/1D 1B/1D	1D 1D	This pair of switch settings disables CH l			
2A/2D, 2B/2D		Enable/Disable CH 2 and select an option for sending Index & Sector to CH 2 controller (see description of CH 1 switches)			
RDY/POK	RDY	Normal I/O Ready status (Up to speed, heads loaded, and no fault exists)			
	POK	Ready status with Power OK. In addition to above conditions (when switch is in RDY), Ready status also indicates that the optional power supply we provide has an acceptable ac input voltage. If the ac input voltage drops, Ready status goes inactive at least 5 milliseconds before a voltage fault is registered.			
	Table Continued on Next Page				

TABLE 3-3. I/O BOARD SWITCHES (Contd)

	r	
Switch	Setting	Description
T2/T1		Selects type of extended cylinder addressing, provided that XA/N switch is set in XA position.
	T2	Enables Extended Cylinder Address Bits 2^{10} and 2^{11} , via Tag 2 (Head Select). Can be selected in either SMD-0 or SMD-E mode.
	Tl	Enables Extended Cylinder Address Bit 2 ¹⁰ , via Tag 1 (Cylinder Select). Can be selected in either SMD-0 or SMD-E mode, but Tags 4 and 6 are unusable with this selection.
E/0	E	SMD-E mode (Tags l - 6)
	0	SMD-0 mode (Tags l - 3)
XA/N	XA	Extended Cylinder Address (cylinders 0-1634 on 741 MB, 1123 MB, and 1236 MB drives; 0-1380 on 851 MB drives); method set by T2/T1 switch
	N	Normal Cylinder Address (cylinders 0-1023): T2/T1 switch has no effect
AR/RT	AR	Absolute Reserve (Dual Channel)
	RT	Reserve Timer (Dual Channel)
R/L	R	Remote power on
	L	Local power on
DEV ID 20-27	(Closed)	Set according to customer requirements.

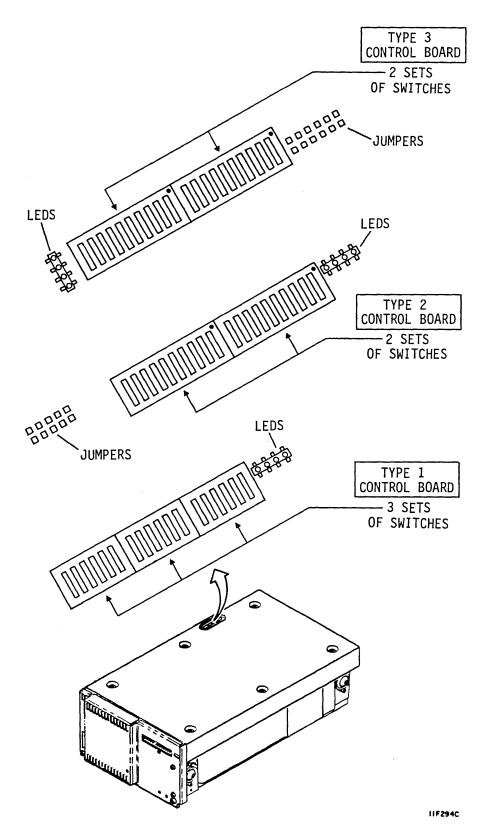


Figure 3-23. Control Board Types

TABLE 3-4. CONTROL BOARD INDICATORS

Indicator	Function
Maintenance LEDs	Provide indications of drive status when no operator panel or status/control panel is available.
+5 V	Indicates presence of +5 V.
SEL	Indicates that drive is selected by a controller.
RDY	Indicates that the drive is ready (spindle power on is complete, heads are loaded, and no fault exists).
FLT	Indicates that a fault exists within the drive.

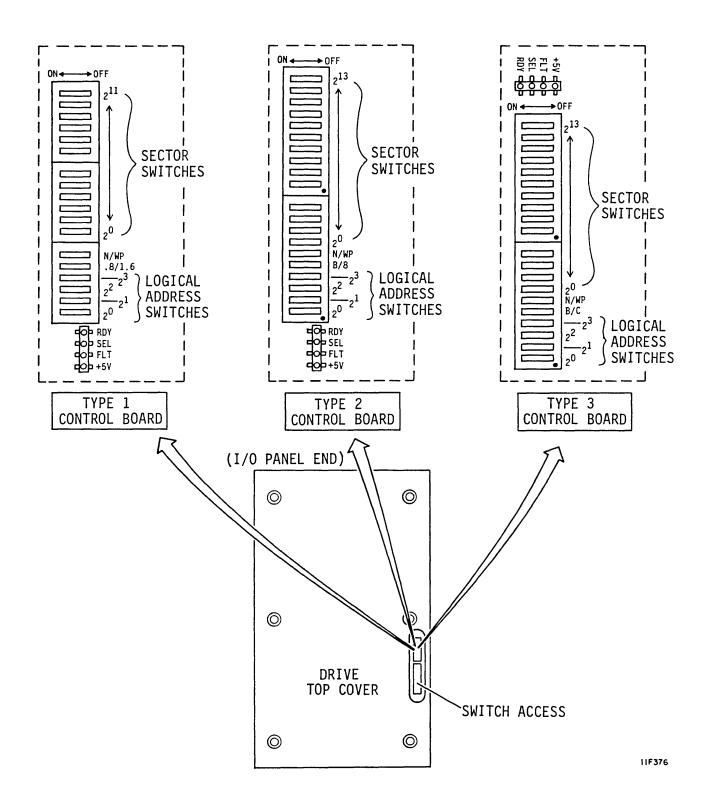


Figure 3-24. Control Board Switches and Indicators

TABLE 3-5. CONTROL BOARD SWITCHES

	Switches 20 through 23 are used to set the logical address for drives without the operator panel or status/control panel. See discussion on Setting Logical Address Switches.
	Allow the dividing of the disc into specific segments or sectors. The switch settings determine the number of sectors per track. See discussion on Setting Sector Switches (Types 2 and 3) or appendix B (Type 1).
.8 1.6	Sector clock frequency = .8 MHz Sector clock frequency = 1.6 MHz
8 or C (Off)	Clock frequency Drive capacity 1.6 MHz 741 MB drives 1.6 MHz 851 MB drives 1.83 MHz 1123 MB drives 2.016 MHz 1236 MB drives
B (On)	Byte frequency sector clock.
WP (Off) N (On)	Write Protect Normal Placing the switch in the WP position prevents the drive from performing write operations. The switch must be in the N position to enable write operations.
	1.6 B or C (Off) B (On) WP (Off)

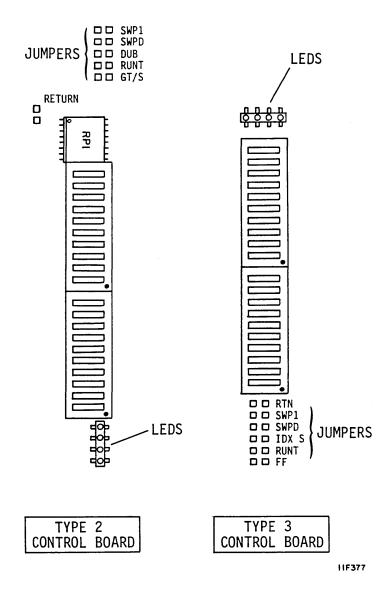


Figure 3-25. Control Board Jumpers

TABLE 3-6. CONTROL BOARD JUMPERS

Jumper [Board Types]	Setting	Description
RUNT Jumper* [2 & 3]	Connected	Runt sector pulses are suppressed.
	Discon- nected	Runt sector pulses may appear.
SWP1 Jumper** [2 & 3]	Connected	Disables the option for sweep only on seeks.
	Discon- nected	Enables the option for sweep only on seeks.
SWPD Jumper** [2 & 3]	Connected	Disables sweep cycles.
[2 & 3]	Discon- nected	Enables sweep cycles.
DUB Jumper [2 only]	Connected	Jumper is properly set at the factory and must not be changed.
GT/S Jumper [2 only]	Discon- nected	Jumper is properly set at the factory and must not be changed.
RTN (Return) Jumper** (Not labeled	Connected	Enables the option to return the heads to their original position following a sweep segment.
on Type 2 boards) [2 & 3]	Discon- nected	Disables that option.
IDX S Jumper [3 only]	Discon- nected	Jumper is properly set at the factory and must not be changed.
FF [3 only]	N/A	Reserved for future use.

^{*} For explanation, see Setting Sector Switches.

^{**} Read Programming the Sweep Cycle Function before selecting sweep cycle options. Some controllers will not function with sweep cycle enabled.

Setting Logical Address Switches

The control board logical address switch settings are shown in table 3-7. For drives with an operator panel or status/control panel, the Logical Address switch settings on the control board are ignored. Set the logical address on either panel by pressing the Logical Address switch and observing the indicators, displayed in binary (see figure 2-1).

TABLE 3-7.	LOGICAL	ADDRESS	SWITCHES	ON	CONTROL	BOARD

Logical	Switch (Binary)* Logical	Switch (Binary)*		Sw	itch	(Binar	(A)*		
Address	20	21	22	23	Address	20	21	22	2 ³
0	С	С	C	C	8	C	С	С	0
1	0	С	С	С	9	0	С	C	0
2	С	0	С	С	10	С	0	C	0
3	0	0	С	С	11	0	0	С	0
4	С	С	0	С	12	С	С	0	0
5	0	С	0	С	13	0	С	0	0
6	С	0	0	С	14	С	0	0	0
7	0	0	0	С	15	0	0	0	0
* O - Open (Off) C - Closed (Op)									

* O = Open (Off), C = Closed (On)

Programming the Sweep Cycle Function

The sweep cycle is a feature that periodically moves the heads to different locations on the discs during intervals when the drive is idle. The following are highlights of the sweep cycle function:

- Using the sweep cycle enhances drive reliability. We encourage you either to enable the drive sweep cycle or to use a sweep cycle driven at the system or subsystem level. Consult with an analyst in making this choice.
- There are several sweep cycle options available at the drive level. Make sure that the selected option is compatible with system operation.

- Our testing has verified that sweep activity results in lower particle count in the module.
- The sweep routine consumes approximately 11 seconds of a 13-hour period. Thus, the drive is available to the system more than 99% of the time.
- You can disable the sweep cycle (described below) without affecting the specified Mean Time Between Failures (MTBF) or warranty agreements.

Status codes associated with the sweep cycle are discussed in Appendix A.

The drive is preset during manufacturing with a set of sweep cycle options selected. Three jumpers on the control board control the selection, as shown in figure 3-24.

The three sweep cycle jumpers are identified as SWPD, SWP1, and RTN. The following paragraphs discuss the functions controlled by each jumper and tell different ways to position the jumpers:

SWPD -- enables or disables the sweep cycle function. The jumper can be positioned as follows:

- Connecting the two pins -- disables the sweep cycle function. The other jumpers then have no effect.
- Mounted on one pin (two pins not connected) -- enables the sweep cycle function.

SWP1 -- enables or disables the option to sweep only on seeks. When enabled, sweep movements can occur only in conjunction with seeks required by the controller. Each time the drive performs a sweep cycle, it starts a 12-minute timeout. When the timeout has elapsed, the drive performs another sweep cycle only when it receives a Seek command (Tag 1). When combining a sweep cycle with a seek, the drive performs the sweep cycle first and then executes the Seek command. The jumper can be positioned as follows:

- Connecting the two pins -- disables the option to sweep only on seeks (sweep cycles can be initiated within the drive).
- Mounted on one pin (two pins not connected) -- enables the option to sweep only on seeks.

RTN (not labeled on Type 2 control boards) -- enables or disables the option to return the heads to their original cylinder following a sweep segment. The jumper can be positioned as follows:

- Connecting the two pins -- enables the option. The drive performs the sweep and, when selected, returns to the original cylinder (where it was before the sweep occurred) with the following exceptions:
 - If the sweep was initiated by a Seek command, the drive performs the sweep function and then moves the heads to the cylinder requested by the controller.
 - If the drive had not been selected during the 12 minutes prior to a sweep, the heads stay on a cylinder accessed during the sweep segment.
- Mounted on one pin (two pins not connected) -- disables the option. The heads always stay on a cylinder accessed during the sweep segment and On Cylinder status remains inactive with the following exception: If the sweep was initiated by a Seek command, the drive performs the sweep function and then moves the heads to the cylinder requested by the controller.

Setting Sector Switches

Setting the sector select switches is discussed in the following paragraphs. Refer to the subsystem or controller reference manual to determine the number of sectors (or the bytes/sector) required by the controller. To achieve that requirement, the following settings are required:

- Sector switches -- See figure 3-24.
- Sector Clock switch (B/C or B/8) -- See figure 3-24.
- RUNT jumper -- See figure 3-25.

The information in this topic applies to drives with Type 2 and Type 3 control boards (see figure 3-24). If your drive has a Type 1 control board, refer to appendix B for instructions on setting its sector switches.

Your task of setting the sector switches on the drive is relatively easy if you already understand the various choices available. There are a number of selection methods, each described by a separate table of selections. If you know how many sectors are needed or the number of bytes in each sector, refer to the switch selection tables, make the suggested

settings, and proceed with the drive installation. However, if you need more information about the different methods before you choose one of them, read on. The following material will step you through the selection process.

Additional background information about sector selection appears in appendix B. For most installations it will not be necessary to refer to appendix B. However, some systems designers, when matching a disc drive to a specific controller, choose a sector length different from any given here in the tables. In cases like this, the material in the appendix relates arbitrary sector lengths to switch settings.

Sixteen tables are needed to cover all the possibilities for sector selection on the drives described in this manual. The following variables make it necessary to use multiple tables:

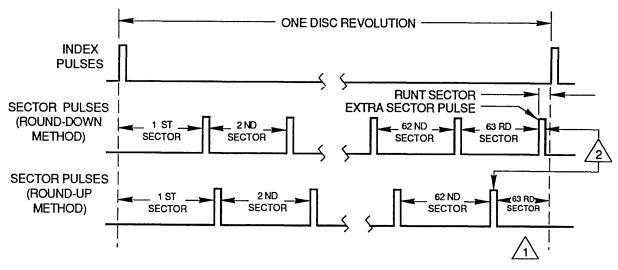
- Drive Capacity -- the 741 MB drive, the 851 MB drive, the 1123 MB drive, and the 1236 MB drive have different numbers of bytes per track.
- Sector Clock Frequency -- selecting a different sector clock frequency (via the B/C switch on Type 3 control boards [B/8 switch on Type 2]) affects the switch settings and in some cases the number of bytes per sector.
- Round-up and Round-down Methods -- produce differing results when the number of sector clock pulses per revolution is not evenly divisible by the number of sectors. The problem of an uneven division can be solved by methods of either rounding up or rounding down the result.

Round-down and Round-up Methods

Figure 3-26 uses an example of 63 sectors to show how the two methods differ both in terms of sector lengths and in the presence or absence of an extra sector pulse. This illustration also lists and describes the various tables.

For the round-down method, the sector tables list the bytes in usable sectors and the bytes in the runt sector. A runt sector is a short interval following the last usable sector. When a runt sector occurs, it is preceded by an extra sector pulse. The topic Runt Suppression describes when this extra sector pulse can be eliminated.

For the round-up method, the sector tables list the bytes in early sectors and the bytes in the last sector. The last sector can be somewhat shorter than the other sectors. There is no runt sector (or extra sector pulse), however.



NOTES:

 Λ

WITH ROUND-UP METHOD, LAST SECTOR MAY BE SHORTER THAN EARLY SECTORS.

 $\sqrt{2}$

CONNECTING "RUNT" JUMPER SUPPRESSES SECTOR PULSE PRECEDING SHORT LAST SECTOR.

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 G A TABLE MB DRIVES:	B/C [B/8] Switch	Round-down Method	Round-up Method
1.6 MHz Clock Byte Clock	C [8] (Off or Open) B [B] (On or Closed)	Table 3-9 Table 3-11	Table 3-10 Table 3-12

SELECTING A TABLE FOR 851 MB DRIVES:		B/C [B/8] Switch	Round-down Method	Round-up Method	
	1.6 MHz Clock Byte Clock	C [8] (Off or Open) B [B] (On or Closed)	Table 3-13 Table 3-15	Table 3-14 Table 3-16	

SELECTING A TABLE FOR 1123 MB DRIVES:				
		B/C [B/8] Switch	Round-down Method	Round-up Method
	1.83 MHz Clock Byte Clock	C [8] (Off or Open) B [B] (On or Closed)	Table 3-17 Table 3-19	Table 3-18 Table 3-20

 G A TABLE MB DRIVES:	B/C [B/8] Switch	Round-down Method	Round-up Method
2.016 MHz Clock Byte Clock	C [8] (Off or Open) B [B] (On or Closed)	Table 3-21 Table 3-23	Table 3–22 Table 3–24

Figure 3-26. Choosing a Sector Selection Method

The round-up and round-down methods yield the same results for many sector counts. In these cases, the two methods call for identical switch settings.

The choice of sector clock frequencies (via the B/C switch [B/8 switch on Type 2 boards]) can affect the lengths of the usable sectors (round-down and round-up methods), the runt sector (round-down method), or the last sector (round-up method).

Exact Sector Lengths

The sector selection tables provide idealized values for sector lengths. Deriving exact values from these idealized values requires an adjustment. This adjustment must be considered if the chosen sector length allows minimal overhead or if runt sector suppression is planned.

The adjustment must be made to calculate the actual lengths of the first sector (logical sector 0) and the last sector. For each drive type and sector clock combination, table 3-8 lists the number of bytes of adjustment that must be added to the first sector and subtracted from the last sector.

TABLE 3-8. SECTOR LENGTH ADJUSTMENT

Drive Type	Sector Clock	Adjustment (Bytes)
741 MB	1.6 MHz	4.50
741 MB	Byte Clock	5.00
851 MB	1.6 MHz	6.00
851 MB	Byte Clock	6.00
1123 MB	1.83 MHz	6.00
1123 MB	Byte Clock	7.00
1236 MB	2.016 MHz	7.50
1236 MB	Byte Clock	8.00

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The following examples show how to calculate exact sector lengths for the round-down method and then the round-up method. These examples are based on selecting 63 sectors on a 741 MB drive with the 1.6 MHz sector clock. Table 3-8 indicates for this case that the adjustment is 4.50 bytes.

Example 1: Round-down method (see table 3-9)

 Values from table
 Adjusted values

 Sectors 0 thru 62 = 479.25 bytes
 Sector 0 = 479.25 + 4.50 = 483.75 bytes

 Runt Sector = 47.25 bytes
 Runt Sector = 47.25 - 4.50 = 42.75 bytes

Example 2: Round-up method (see table 3-10)

 Values from table
 Adjusted values

 Sectors 1 thru 61 = 480.37 bytes
 Sector 0 = 480.37 + 4.50 = 484.87 bytes

 Sector 62 = 456.75 bytes
 Sector 62 = 456.75 - 4.50 = 452.25 bytes

If a round-down sector table indicates either no runt or a very short runt, in actuality the last usable sector is shortened. Example 3 is based on selecting 89 sectors on a 741 MB drive with the 1.6 MHz sector clock:

Example 3: Round-down method (see table 3-9)

Values from tableAdjusted valuesSectors 0 thru 88 = 339.75 bytesSector 0 = 339.75 + 4.50 = 344.25 bytesRunt Sector = 2.25 bytesRunt Sector = 2.25 - 4.50 = -2.25 bytes

A negative runt sector length doesn't make sense, however. The numbers indicate that there really is no runt and that sector 88 is shortened by 2.25 bytes. Expressing these results more sensibly:

Sector 0 = 339.75 + 4.50 = 344.25 bytes Sectors 1 thru 87 = 339.75 bytes Sector 88 = 339.75 - 2.25 = 337.50 bytes

Runt Suppression

Runt suppression may or may not be desirable. Runt sector pulses are suppressed when the RUNT jumper is connected (see figure 3-25 and table 3-6). Consider the three examples given above in the discussion of exact sector lengths.

Selecting runt suppression defeats the purpose of using the round-up method. Referring to example 2, the 452.25-byte (shorter) last sector would no longer be preceded by a sector pulse and would not be available for use.

Depending on the controller requirements, runt suppression may improve operation with the round-down method. If runt suppression had been selected in example 1, the runt sector would no longer exist. With no extra sector pulse, sector 62 would be extended by 42.75 bytes.

If runt suppression had been selected in example 3, the sector pulse preceding sector 88 would be suppressed. This illustrates an important point about runt suppression:

If the length of the runt shown in the sector table is shorter than the length of the adjustment shown in table 3-8, DO NOT use runt sector suppression.

Making the Selection

You must decide now which sector selection table you will use for switch setting information. If you don't know which table is best, compare the sector lengths listed in each of the tables to the requirements of the controller. The choice of tables may be arbitrary, or one of the tables may offer a real advantage over the others.

The Sector Select switch assembly has 14 independent switches used for selecting sectors. These switches and the B/C [or B/8] switch are located on the control board as shown in figure 3-24. Across from the number of sectors listed in each table is a row of Cs and Os. C represents the Closed (On) position of the sector switch. O represents the Open (Off) position of the sector switch. Set the switches to the positions designated in the table while referring to figure 3-21 for an illustration of the switch positions. Verify also that the B/C [or B/8] switch is set correctly.

For future reference, you may want to record the switch settings you made. Use the following worksheet:

Table Used:
Number of Sectors:
Switch Settings: $ \begin{bmatrix} 2^0 & 2^1 & 2^2 & 2^3 & 2^4 & \\ 2^5 & 2^6 & 2^7 & 2^8 & 2^9 & \\ 2^{10} & 2^{11} & 2^{12} & 2^{13} \end{bmatrix} $
B/C [or B/8] Switch: On Off RUNT jumper: Connected Disconnected

TABLE 3-9. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD

Number of		1	2	3			tor				10	1	12	213	Bytes in Usable	Bytes in Runt
Sectors	2 ⁰	21	2	2 ³	24	2 ⁵	26	2 ⁷	2	2	2	2	2 2 2	213	Sectors	Sector
4	0	0	0	0	0	0	С	С	С	0	С	0	0	С	7560	0
5	0	0	0	0	0	0	0	0	С	С	0	С	0	C	6048	0
6	0	0	0	0	0	0	0	С	0	C	С	С	0_	С	5040	0
7	0	0	0	0	0	0	0	0	С	0	О	0	С	C ·	4320	0
8	0	0	0	0	0	С	C	С	0	С	0	0	С	C	3780	0
9	0	C	<u>_C</u>	0	С	0	C	0	0	0	С	0	С	С	3359.25	6.75
10	0	0	0	0	0	0	0	С	С	0	С	0	С	С	3024	0
11	С	0	С	0	С	С	C	0	0	С	С	0	С	С	2748.37	7.87
12	0	0	0	0	0	0	C	0	<u>C</u>	C	С	0	С	С	2520	0
13	C	0	С	C	0	C	C	С	C	C	C	0	C	C	2325.37	10.12
14	0	0	0	0	0	0	0	С	0	0	0	С	C	C	2160	0
15	0	0	0_	0	0	0	0	0	C	0	0	C	C	C	2016	0
16	0	0	0	0	C	С	C	0	C	0	0	С	C	C	1890	0
17	C	С	0	0	C	0	C	C	C	0	0	C	C	C	1778.62	3.37
18	C	C	0	C	0	<u></u>	0	_0_	0	C	0	С	C	C	1679.62	6.75
19	0	С	0	C	C	C	C	0	0	C	0	С	C	C	1590.75	15.75
20	0	0	0	0	0	0	C	C	0	C	0	C	C	C	1512	0
21 22	0	<u>O</u>	<u> </u>	C	0	0	0	0	<u>C</u>	C	<u> </u>	C	<u>C</u>	C	1440 1373.62	20.25
23	C 0	0	0	0	C	C C	0	0	C C	C	0	C	C C	C	13/3.62	20.25 18
23 24	0	-	_	_	0	С	0	С	C	C	0	C	C	C	1260	0
25	C	0	0 C	C	0	0	C	<u> </u>	C	C	0	C	C	c	1209.37	5.62
26	C	C	C	0	C	C	C	С	C	c	0	C	C	c	1162.12	24.75
27	C	0	C	c	С	0	0	0	0	0	C	C	C	c	1119.37	16.87
28	0	0	0	0	0	0	C	0	0	0	c	C	C	c	1080	0
29	0	C	0	0	0	C	C	0	0	0	C	c	C	c	1041.75	29.25
30	0	0	0	0	o	0	o	C	0	ő	C	C	C	c	1008	0
31	C	0	C	c	c	0	0	c	-	0	C	c	C	C	975.37	3.37
32	0	0	o '	c	C	C	0	C	Ö	٥	c	c	C	c	945	0
33	0	C	o	o	Ċ	o	C	C	ō	o	C	c	C	c	915.75	20.25
34	0	C	0	C	0	C	C	C	ō	0	C	C	C	c	888.75	22.50
35	0	0	Ō	0	0	o	ō	0	Ċ	o	C	С	C	c	864	0
36	0	С	С	0	С	0	0	0	С	0	С	С	C	c	839.25	27
37	0	С	0	С	0	С	0	0	С	0	С	С	С	С	816.75	20.25
38	С	0	C	C	C	C	0	0	С	0	С	С	С	c	795.37	15.75
39	С	С	С	С	0	0	С	0	С	0	С	С	C	С	775.12	10.12
40	0	0	0	0	0	С	С	0	С	0	С	С	С	С	756	0
41	С	0	0	0	С	С	С	0	С	0	С	С	С	С	736.87	28.12
42	0	0	0	0	0	0	0	С	С	0	С	С	С	C	720	0
43	С	С	С	С	0	0	0	С	С	0	С	С	С	С	703.12	5.62
44	0	С	С	С	С	0	0	С	С	0	С	С	С	С	686.25	45
45	С	С	0	С	0	С	0	С	С	0	С	С	С	c	671.62	16.87
* C = Clo	osed	or	On	; 0	= 0	pen	or	off		L						<u> </u>
		sed or On; O = Open or Off Continued -												ied —		

TABLE 3-9. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number of							tor								Bytes in Usable	Bytes in Runt
Sectors	20	21	22	23	2 ⁴	2 ⁵	26	2 ⁷	2 ⁸	29	2 ¹⁰	2 ^{1.}	212	2 ¹³	Sectors	Sector
46	0	0	0	С	С	С	0	С	С	0	С	С	С	С	657	18
47	C	0	С	0	0	0	C	С	С	0	С	С	С	С	642.37	48.37
48	0	0	0	0	C	0	С	С	C	0	С	С	С	С	630	0
49	0	0	С	C	С	0	C	С	С	0	С	С	С	c	616.50	31.50
50	C	С	С	0	0	С	C	С	С	0	С	С	С	С	604.12	33.75
51	C	0	0	0	<u>C</u>	C	C	C	C	0	C	<u>C</u>	С	C	592.87	3.37
52	0	О	С	C	С	С	C	С	С	0	С	С	С	C	580.50	54
53	С	0	С	0	0	0	0	0	0	С	С	С	С	С	570.37	10.12
54	C	C	_ <u>C</u> _	C	0	0	0	0	0	С	С	C	С	С	559.12	47.25
55	0	0	0	C	С	0	0	0	0	С	С	С	С	С	549	45
56	0	0	0	0	0	С	0	0	0	C	С	С	С	С	540	0
57	C	0	_0_	C	0	C	0	0	0_	C	С	_C	С	С	529.87	37.12
58	C	0	0	0	С	С	0	0	0	С	С	С	С	С	520.87	29.25
59	C	0	0	C	C	С	0	0	0	С	С	С	С	C	511.87	39.37
60	0	0	0	0	0	0	C	<u> </u>	0_	C	C	_ <u>C</u>	С	C	504	0
61	0	0	0	C	0	0	C	0	0	С	С	С	С	С	495	45
62	C	С	C	С	0	0	C	0	0	С	С	С	С	C	487.12	38.25
63	0	C	C	0	_ <u>C</u>	0	C	0	<u> </u>	С	С	C	С	С	479.25	47.25
64	0	0	С	C	С	0	C	0	0	С	С	С	С	C	472.50	0
65	С	С	0	0	0	С	C	0	0	С	С	С	C	C	464.62	39.37
66	C	0	<u> </u>	C	_0	C	C	0	0	C	C	_ <u>C</u> _	С	С	457.87	20.25
67	C	C	C	C	0	C	C	0	0	С	С	С	С	C	451.12	14.62
68	C	0	С	0	С	C	C	0	0	С	C	С	С	С	444.37	22.50
69	C	<u>C</u>	0	C	<u></u>	<u>C</u>	C	0	<u> </u>	C	C	C	C	C	437.62	43.87
70	0	0	0	0	0	0	0	C	0	C	C	C	C	С	432	0
71	0	C	C	0	0	0	0	C	0	C	C	C	C	C	425.25	47.25
72	C	<u> </u>	<u> </u>	C	<u> </u>	0	0	<u> </u>	<u> </u>	C	C	_ <u>C</u>	C	C	419.62	27
73	0	0	0	0	C	0	0	C	0	C	C	C	C	C	414	18
74	C	0	C	0	C	0	0	C	0	С	C	С	С	C	408.37	20.25
75	0	C	_0_	C	<u>C</u>	0	0	<u> </u>	<u> </u>	C	<u>C</u>	<u>C</u>	C	C	402.75	33.75
76	C	C	C	C	C	0	0	C	0	C	C	С	С	C	397.12	58.50
77	C	C	0	0	0	C	0	C	0	C	C	C	C	C	392.62	7.87
<u>78</u>	0	0	0	C	_0_	C	0	<u>C</u>	<u> </u>	C	<u>C</u>	C	C	C	387	54
79	0		C	C	0	C	0	C	0	C	C	C	С	C	382.50	22.50
80	0	0	0	0	C	C	0	С	0	С	C	C	С	C	378	0
81	C	0	<u>C</u>	0	C	C	0	C	0_	C	C	C	C	C	372.37	77.62
82 83	C	0	0	C	C C	C	0	C C	0	C	C	C	C	C C	367.87 363.37	74.25 79.87
83 84	0	0	C	0	0	C O	0	C	0	C	C	C C	C	C	360	0
85	0	0	C	0	-0	0	C	C	0	C	c	C	C	C	355.50	22.50
86	0	0	0	C	0	0	C	C	0	C	C	C	C	C	355.50	54
87	0	0	C	c	0	0	C	C	0	C	С	C	C	c	346.50	94.50
												<u> </u>			340.30	74.50
* C = Clo	osed	or	On	; 0	= 0	pen	or	off								
		Continued —														

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TABLE 3-9. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number		Sector Switches*													Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	212	2 ¹³	Usable Sectors	in Runt Sector
88	С	С	С	С	0	0	С	С	0	С	С	С	С	С	343.12	45
89	0	С	0	0	С	0	C	С	0	C	С	С	С	C	339.75	2.25
90	0	С	С	0	C	0	C	C	0	C	С	С	C	С	335.25	67.50
91	С	0	0	С	С	0	C	С	0	C	С	С	С	С	331.87	39.37
92	0	0	С	C	С	0	C	С	0	С	С	С	С	С	328.50	18
93	С	С	C	С	С	0	С	С	0	С	С	С	С	С	325.12	3.37
94	С	С	0	0	0	С	С	С	0	C	С	С	C	С	320.62	101.25
95	0	С	С	0	0	С	C	С	0	С	С	С	С	С	317.25	101.25
96	0	0	0	C	0	C	С	C	0	C	С	С	C	С	315	0
97	С	С	0	С	0	C	C	С	0	C	С	С	C	С	311.62	12.37
98	0	С	С	C	0	С	С	С	0	С	С	С	С	С	308.25	31.50
99	С	0	0	0	C	C	C	С	0	C	С	С	С	С	304.87	57.37
100	0	0	С	0	С	С	С	С	0	С	С	С	С	С	301.50	90
101	0	С	С	0	С	С	C	С	0	C	С	С	С	С	299.25	15.75
102	С	0	0	C	C	C	C	C	0	C	С	С	С	C	295.87	60.75
103	0	0	С	С	С	С	С	С	0	С	С	С	С	С	292.50	112.50
104	0	С	С	C	С	С	C.	С	0	С	С	С	C	С	290.25	54
105	0	0	0	0	<u> </u>	0	0	0	С	С	С	С	С	C	288	0
106	С	C	0	0	0	0	0	0	С	С	С	С	С	С	284.62	69.75
107	С	0	С	0	0	0	0	0	C	C	С	C	C	С	282.37	25.87
108	0	<u> </u>	0	C	<u> </u>	0	0	0	<u></u>	C	C	C	С	C	279	108
109	0	C	0	C	0	0	0	0	C	C	C	C	C	C	276.75	74.25
110	0	0	C	C	0	0	0	0	C	C	C	С	C	C	274.50	45
111	0	<u>C</u>	C	C	0	0	0	0	<u>C</u>	C	C	C	<u>C</u>	C	272.25	20.25
112	0	0	0	0	C	0	0	0	С	C	С	C	C	C	270	0
113	C	C	0	0	C	0	0	0	C	C	C	С	C	C	266.62	111.37
114	<u></u>	<u>0</u>	C	0	<u></u>	<u> </u>	0	0	<u>C</u>	C	C	C	C	C	264.37	101.25
115 116	C	C	C	0	C	0	0	0	C	C	C	C	C	C C	262.12 259.87	95.62
117	C	0	0	C	C	0	0	0	C	C	C C	C	C	c	257.62	94.50 97.87
118		-	C	c	C	0	0	0	C	C	c	c	C	c	255.37	105.75
119	C	C	C	c	C	0	0	0	C	C	C	c	C	c	253.37	118.12
120	0	_	0	0	_		0	0		C		C	_	1	252	0
121	0	C	0	0	0	C	0	0	C	C	C	C	C	C C	249.75	20.25
122	o	0	C	ő	0	C	ő	o	C	c	C	c	C	c	247.50	45
123	0	C	C	0	o	C	0	0	C	c	C	C	C	c	245.25	74.25
124	0	0	0	C	_ 0	c	ō	0	C	C	C	C	C	c	243	108
125	C	o	o	c	o	c	ő	0	C	c	C	c	C	c	241.87	5.62
126	C	C	0	C.		C	0	0	C	C	C	c	C	c	239.62	47.25
127	С	0	С	С	0	C	0	0	C	c	C	c	C	c	237.37	93.37
128	0	C	C	c	o	C	Ö	0	c	c	C	c	C	c	236.25	0
		-	-	ľ	_	_		-	_	ľ	-	Ĭ		_	200.20	_
* C = Clc	sed	or	On	; 0	= 0	pen	or	off		L						

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TABLE 3-10. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	0	0	0	С	С	С	0	С	0	0	С	7560	7560
5	0	О	0	0	0	0	0	0	С	C	0	C	0	c	6048	6048
6	0	0	0	0	0	0_	0	_ <u>C</u>	0	C	_ <u>C</u> _	C	0	C	5040	5040
7	0	0	0	0	0	0	0	0	С	0	0	0	С	C	4320	4320
8	0	0	0	0	0	С	C	С	0	C	0	0	C	C	3780	3780
9	C	0	C	0	<u></u>	0	C	_0	_0_	0	_ <u>C</u> _	0	С	<u> </u>	3360.37	3357
10	0	0	0	0	0	0	0	С	С	0	С	0	С	С	3024	3024
11	0	0	С	0	С	С	C	0	0	C	С	0	С	C	2749.50	2745
12	0	_0_	0	0	0	0	C	0	C	C	C	_	C	С	2520	2520
13	0	0	С	C	0	С	C	С	С	C	С	0	С	С	2326.50	2322
14	0	0	0	0	0	0	0	С	0	0	0	С	С	C	2160	2160
15	0	0	0	0	0	0	0	0	<u> </u>	0	0	C	С	С	2016	2016
16	0	0	0	0	С	С	С	0	С	0	0	С	C	C	1890	1890
17	0	С	0	0	С	0	C	С	С	0	0	C	С	C	1779.75	1764
18	0	C	0	C	0	C	0	0	0	C	0	C	С	C	1680.75	1667.25
19	С	0	0	C	С	С	С	0	0	C	0	С	С	C	1591.87	1586.25
20	0	0	0	0	0	0	C	С	О	C	0	C	С	С	1512	1512
21	0	0	0	0	0	0	0	0	<u> </u>	C	0	C	С	С	1440	1440
22	0	С	0	C	С	С	0	0	С	C	0	C	С	C	1374.75	1370.25
23	С	С	С	C	0	С	C	0	С	C	0	C	С	С	1315.12	1307.25
24	0	0	0	0	0	C	0	C	С	C	0	C	С	С	1260	1260
25	0	0	С	C	0	0	C	С	С	C	0	С	С	С	1210.50	1188
26	0	С	С	0	С	С	C	С	С	C	0	C	С	C	1163.25	1158.75
27	0	0	С	C	C	0	0	0	0	0	<u> </u>	С	C	C	1120.50	1107
28	0	0	0	0	0	0	C	0	0	0	С	c	С	C	1080	1080
29	С	0	0	0	0	C	C	0	0	0	С	C	С	С	1042.87	1039.50
30	0	0	0	0	0	_0_	0	_ <u>C</u>	0	0	_ <u>C</u>	С	С	С	1008	1008
31	0	0	С	C	С	0	0	С	0	0	С	С	C	С	976.50	945
32	0	0	0	C	С	С	0	С	0	0	С	C	С	C	945	945
33	С		0	0	<u>C</u>		C	_ <u>C</u>		10	_ <u>C</u>	C	С	C	916.87	900
34	С	0	0	С	0	С	С	С	0	0	С	С	С	С	889.87	874.12
35	0	0	0	0	0	0	0	0	C	0	С	С	С	С	864	864
36	С	0	C	0	C	0	0	0	C	0	C	С	С	C	840.37	826.87
37	С	0	0	C	0	С	0	0	С	0	С	С	С	С	817.87	796.50
38	0	0	С	C	С	С	0	0	С	0	С	С	С	С	796.50	769.50
39	Ó	C	С	C	0	0	C	0	С	0	_C_	С	С	С	776.25	742.50
40	0	0	0	0	0	С	C	0	С	0	С	С	С	С	756	756
41	0	0	0	0	С	С	C	0	С	0	С	С	С	С	738	720
42	0	0	0	0	0	0	0	C	C	0	С	С	С	С	720	720
43	0	С	С	C	0	0	0	С	С	0	С	С	C	С	704.25	661.50
44	С	0	С	C	С	0	0	С	С	0	С	С	С	C	687.37	682.87
45	0	С	0	C	0	С	0	С	С	0	С	С	C	С	672.75	639
* C = Clc	sed	or	On	; 0	= (pen	or	off				Cor	ntinu	ed —		

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TABLE 3-10. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number of							tor								Bytes in Early	Bytes in Last
Sectors	20	2 ¹	2 ²	2 ³	2 ⁴	2 ⁵	26	2 ⁷	2 ⁸	29	2 ¹⁰	2 ¹¹	212	2 ¹³	Sectors	Sector
46	С	С	С	0	С	С	0	С	С	0	С	C	С	С	658.12	624.37
47	0	0	С	0	0	0	C	С	С	0	С	С	С	c	643.50	639
48	0	0	0	0	С	0	C	C	С	0	C	С	С	С	630	630
49	С	С	0	C	С	0	C	С	С	0	С	С	С	c	617.62	594
50	0	С	С	0	0	С	C	С	С	0	С	С	С	С	605.25	582.75
51	0	0	0	0	C	C	C	C	C	0	C	С	С	С	594	540
52	С	С	0	C	С	С	C	С	С	0	С	С	C	С	581.62	577.12
53	0	0	С	0	0	0	0	0	0	C	C	С	С	С	571.50	522
54	0	<u>C</u>	<u> </u>	C	0	0	0	0	0	C	C	С	С	C	560.25	546.75
55	С	С	С	0	С	0	0	0	0	С	С	C	С	C	550.12	533.25
56	0	0	0	0	0	С	0	0	0	C	С	С	С	C	540	540
57	0	0	0	C	0	C	0	0	0	C	C	С	С	_C	531	504
58	0	0	0	0	С	С	0	0	0	С	С	С	С	C	522	486
59	0	0	0	C	С	С	0	0	0	С	С	С	С	С	513	486
60	0	0	_0_	0	0	0	C	0	0_	C	C	С	С	C	504	504
61	С	С	С	0	0	0	C	0	0	C	С	С	С	С	496.12	472.50
62	0	С	С	C	0	0	C	0	0	C	С	С	С	С	488.25	456.75
63	С	0	<u>C</u>	0	С	0	C	0	0	C	С	С	С	С	480.37	456.75
64	0	0	С	С	С	0	С	0	0	С	С	С	С	С	472.50	472.50
65	0	С	0	0	0	С	C	0	0	C	С	С	С	С	465.75	432
66	0	0		C	0	C	C	0	0	C	<u>C</u>	С	С	С	459	405
67	0	С	С	C	0	С	C	0	0	С	С	С	С	C	452.25	391.50
68	0	0	С	0	С	С	С	0	0	С	С	С	С	С	445.50	391.50
69	0	C	_0_	C	C	C	С	0	0	С	C	С	С	C	438.75	405
70	0	0	0	0	0	0	0	C	0	C	C	С	C	C	432	432
71	C	0	C	0	0	0	0	C	0	C	C	C	C	C	426.37	393.75
72	0	C	<u> </u>	C	0	0	0	<u></u>	0	C	<u>C</u>	C	C	C	420.75	366.75
73	C	C	C	C	0	0	0	С	0	C	C	С	C	C	415.12	351
74	0	0	C	0	С	0	0	С	0	C	C	С	С	C	409.50	346.50
75	C	<u> </u>	<u> </u>	C	C	<u> </u>	9	C	<u>。</u>	C	C	C	C	C	403.87	353.25
76 77	0	C	C O	C	C	O C	0	C	0	C	C	C	C	C C	398.25 393.75	371.25 315
77 78	_			0			_			1					393.75	
79	C	C C	<u>C</u>	C	0	C C	0	C	0	C	C	C	C	C C	383.62	354.37 317.25
80	0	0	0	0	C	С	0	C	0	C	C	C	C	C	378	378
81	0	0	С	0	C	С	0	C	0	C	С	C	C	C	378 373 . 50	360
82	0	- 0	0	C	c	c	0	C	0	C	C	C	C	C	369	351
83	0	0	C	C	C	С	0	C	0	c	C	С	C	C	364.50	351
84	0	0	0	0	0	0	C	C	0	C	C	C	C	C	360	360
85	C	C	0	0	0	0	C		0	c	C	C	C	С	356.62	283.50
86	C	C	C	0	0	0	c	C	0	C	C	C	C	C	352.12	309.37
87	C	С	0	C	0	0	c	C	0	C	C	C	C	C	347.62	344.25
	<u> </u>						<u> </u>								377.02	J77.2J
* C = Clo	osed	or	On	; 0	= 0	pen	or	Off	· 			CO	ntinu	ıed —		

TABLE 3-10. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	27	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
88	0	С	С	С	0	0	С	С	0	С	С	С	С	С	344.25	290.25
89	C	0	0	0	С	0	C	С	0	C	С	С	С	С	340.87	243
90	С	0	С	0	С	0	С	С	0	C	С	С	С	С	336.37	302.62
91	0	0	0	C	С	0	С	С	0	С	С	С	С	С	333	270
92	C	С	0	C	С	0	C	С	0	С	С	С	С	С	329.62	244.12
93	0	C	С	С	С	0	С	С	0	C	С	С	С	С	326.25	225
94	0	С	0	0	0	C	С	С	0	C	С	С	С	C	321.75	317.25
95	C	0	С	0	0	С	C	С	0	С	С	С	С	С	318.37	312.75
96	0	0	0	C	0	C	C	C	0	С	С	С	C	С	315	315
97	0	С	0	C	0	С	C	С	0	C	С	С	С	С	312.75	216
98	C	0	С	C	0	С	C	С	0	C	С	С	С	С	309.37	230.62
99	0	0	0	0	C	С	С	С	0_	C	С	С	С	С	306	252
100	С	С	0	0	С	C	С	С	0	С	С	O	С	O	302.62	280.12
101	C	0	С	0	С	С	C	С	0	C	С	С	С	С	300.37	202.50
102	0	0	0	C	С	С	C	С	0	C	C	С	С	С	297	243
103	C	С	0	C	С	C	C	С	0	C	С	С	С	C	293.62	290.25
104	C	0	С	C	С	С	C	С	0	C	С	С	С	С	291.37	228.37
105	0	0	0	0	0	0	0	0	С	C	C	С	С	С	288	288
106	0	С	0	0	0	0	0	0	С	C	С	C	С	С	285.75	236.25
107	0	0	C	0	0	0	0	0	С	C	С	С	С	С	283.50	189
108	C	<u>C</u>	C	0	0	0	0	0	C	C	С	С	С	С	280.12	266.62
109	C	0	0	C	О	0	0	0	С	C	С	C	С	С	277.87	229.50
110	С	C	0	C	0	0	0	0	С	C	С	С	С	С	275.62	196.87
111	С	0	C	C	0	0	0	0	C	C	С	С	С	С	273.37	168.75
112	0	0	0	0	C	0	0	0	С	C	С	С	С	C	270	270
113	0	С	0	0	C	0	0	0	С	C	С	С	С	С	267.75	252
114	0	0	C	0	C	0	0	0	С	C	С	С	С	С	265.50	238.50
115	0	С	С	0	С	0	0	0	С	C	C	С	С	С	263.25	229.50
116	0	0	0	C	С	0	0	0	С	C	С	С	С	C	261	225
117	0	C	0	C	C	0	0	0	С	C	С	С	C	С	258.75	225
118	0	0	С	C	С	0	0	0	С	C	С	С	С	С	256.50	229.50
119	0	С	С	C	С	0	0	0	С	С	С	С	С	С	254.25	238.50
120	0	0	0	0	0	С	0	0	С	C	С	С	C	С	252	252
121	C	0	0	0	0	С	0	0	С	С	С	С	С	С	250.87	135
122	C	С	0	0	0	С	0	0	С	С	С	С	C	С	248.62	156.37
123	С	0	C	0	0	С	0	0	С	C	С	С	С	С	246.37	182.25
124	C	С	С	0	0	С	0	0	С	C	C	С	С	С	244.12	212.62
125	0	0	0	C	0	С	0	0	С	C	С	С	С	С	243	108
126	0	С	0	С	0	C	0	0	С	C	С	С	С	С	240.75	146.25
127	0	0	С	С	0	С	0	0	С	С	С	С	С	С	238.50	189
128	0	С	С	С	0	С	0	0	С	C	С	С	С	С	236.25	236.25
* C = Clo	osed	l oı	On	; 0	= (pen	or	off	:	L						

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TABLE 3-11. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.814 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD

Number						Sec	tor			es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	2 12	2 ¹³	Usable Sectors	in Runt Sector
4	0	0	0	С	С	С	C	0	0	С	0	0	0	С	7560	0
5	0	0	0	0	0	С	C	0	0	0	0	С	0	С	6048	0
6	0	0	0	0	<u>C</u>	0	C	0	0	0	C	С	0_	С	5040	0
7	0	0	0	0	0	С	0	0	С	С	С	С	0	С	4320	0
8	0	0	С	С	С	C	0	0	С	0	0	0	С	С	3780	0
9	0	0	0	0	0	C	C	<u> </u>	0	C	<u> </u>	0	<u></u>	C	3360	0
10	0	0	0	0	C	C	0	0	0	0	C	0	C	C	3024	0
11	C	C	0	0	0	0	C	0	C	0	C	0	C	C	2749	1
12 13	0	0	<u> </u>	C	0	C	0	0	0_	C	<u>C</u>	0	C	C	2520 2326	0
13	0	C	0	C	0	C O	C	C	0	C	C C	0	C	C	2326	2 0
15	_	0	_	1	_	C	_			_		_	C	c	2016	0
16	0	C	<u> </u>	C	<u> </u>	0	0	<u>0</u> C	0	0	0	C	C	c	1890	0
17	0	C	C	C	0	0	0	0	C	0	0	C	C	c	1778	14
18	0	0	0	0	С	C	C	0	C	0	0	C	C	C	1680	0
19	C	0	0	C	0	0	C	C		0	0	C	C	c	1591	11
20	0	0	0	C	C	0	0	0	0	C	0	C	C	c	1512	0
21	0	0	0	0	0	C	C	0	0	C	0	C	C	c	1440	0
22	0	- C	0	0	0	C	0	c	- 0	C	0	C	C	c	1374	12
23	0	C	C	C	C	0	C	C	0	C	0	c	C	c	1314	18
24	0	0	C	0	C	0	0	0	C	C	0	C	C	C	1260	0
25	С	c	C	0	0	0	c	0	c	C	0	C	C	c	1209	15
26	C	0	C	0	C	C	C	0	C	C	0	c	C	c	1163	2
27	0	0	0	0	0	C	0	C	C	C	0	C	C	c	1120	0
28	0	0	0	C	0	0	c	c	C	c	0	c	C	c	1080	0
29	0	C	C	c	o	c	C	C	C	c	o	c	C	c	1042	22
30	0	0	0	o	C	o	0	o	Ö	0	C	c	C	c	1008	0
31	U	0	0	o	C	C	0	0	0	0	C	C	C	C	975	15
32	C	c	C	c	0	o	c	o	Ö	0	C	c	C	c	945	0
33	0	ō	C	c	ō	Ċ	C	ō	o	o	C	c	C	C	916	12
34	С	C	C	o	0	ō	0	C	ō	ō	C	c	С	c	889	14
35	0	0	0	o	0	C	ō	C	ō	0	C	c	C	c	864	0
36	0	0	0	С	С	С	0	c	0	0	Ċ	С	С	C	840	0
37	С	c	C	С	0	0	C	C	0	0	c	C	С	c	817	11
38	С	0	С	0	0	C	C	C	0	0	C	С	С	c	795	30
39	С	0	0	С	С	С	c	С	0	0	С	С	C	С	775	15
40	0	0	С	С	0	0	0	0	С	0	С	С	С	С	756	0
41	С	С	С	С	С	0	0	0	С	0	С	С	С	c	737	23
42	0	0	0	0	С	С	0	0	С	0	С	С	С	С	720	0
43	С	0	0	0	0	0	С	0	С	0	С	C	С	С	703	11
44	С	0	0	0	С	0	С	0	С	0	С	С	С	С	687	12
45	0	0	0	0	0	С	С	0	C	0	С	С	С	С	672	0
* C = Clo	sed	or	On	; 0	= 0	pen	or	off		<u> </u>			<u> </u>			
	Continued —															

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TABLE 3-11. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.814 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	212	2 ¹³	Usable Sectors	in Runt Sector
46	С	С	С	С	0	С	С	0	C	0	С	С	С	С	657	18
47	С	0	С	C	С	С	C	0	С	0	С	С	С	C	643	19
48	0	<u>C</u>	0	C	0	0	0	_ <u>C</u>	С	0	C	С	С	C	630	0
49	С	С	С	0	С	0	0	С	С	0	С	С	С	С	617	7
50	0	0	С	0	0	С	0	С	С	0	С	С	С	С	604	40
51	0	0	0	0	<u>C</u>	C	0	С	C	0	С	С	С	С	592	48
52	С	C	0	C	C	С	0	C	C	0	C	С	С	С	581	28
53	0	С	C	0	0	0	C	C	C	0	C	С	C	C	570	30
54	0	<u> </u>	0	0	<u></u>	0	C	C	<u></u>	0	_ <u>C</u>	C	C	C	560	0
55 56	C	C	0	C	C	0	C	C	С	0	C	С	C	С	549	45
56 57	0	0	C	0	0	C	C	C	С	0	C	С	С	С	540 530	0
<u>57</u>	0	C	C	C C	0	C	C	<u>C</u>	C	0	C	C	C	С	530	30
58	C	C	C	0	C	C	C	C	С	0	C	С	С	С	521	22
59 60	0	0	0	0	0	0	0	0	0	C	C	С	C	С	512 504	32 0
60 61	C	0	0	0	<u> </u>	0	0	0	0	C	C	C C	C	C C	495	45
62	C	0	0	C	C	0	0	0	0	C	C	C	C	C	487	46
63	0	0	0	0	0	C	0	0	0	C	C	C	C	C	480	0
64	0	0	-	l c	0	C	0	0	0	C	c	C	C	C	472	32
65	C	C	C	C	0	c	0	0	o	C	c	c	C	C	465	15
66	0	c	C	0	c	C	١٥	0	o	C	c	C	C	C	458	12
67	С	0	c	c	C	C	0	0	0	c	C	C	C	C	451	23
68	0	0	Ċ	0	0	ō	C	0	o	c	c	c	C	C	444	48
69	o	C	Ö	c	0	ō	C	0	0	С	C	C	С	С	438	18
70	0	0	0	0	C	0	С	0	0	С	С	С	С	С	432	0
71	С	С	С	0	C	0	С	0	0	c	С	С	С	С	425	65
72	0	0	Ç	C	С	0	С	О	0	С	С	С	C	C	420	0
73	0	С	0	0	0	С	С	0	0	С	С	С	С	С	414	18
74	0	0	0	C	0	С	C	0	0	C	С	С	С	С	408	48
75	С	0	С	С	0	С	С	0	0	С	С	С	С	C	403	15
76	С	С	0	0	С	С	С	0	0	С	С	С	C	С	397	68
77	0	0	0	C	С	С	С	0	0	С	С	С	С	С	392	56
78	С	0	C	C	C	С	C	0	0	С	С	C	С	С	387	54
79	0	С	0	0	0	0	0	С	0	С	С	С	С	С	382	62
80	0	С	С	0	0	0	0	С	0	C	С	С	С	С	378	0
81	C	C	0	C	0	0	0	С	0	C	C	C	С	C	373	27
82	0	0	0	0	С	0	0	C	0	С	С	С	С	С	368	64
83	0	0	С	0	С	0	0	С	0	C	C	С	C	С	364	28
84	0	0	0	C	C	0	0	С	0	C	C	С	С	C	360	0
85	C	0	C	C	С	0	0	C	0	C	С	C	C	C	355	65
86	С	0	0	0	0	С	0	С	0	C	С	С	С	C	351	54
87	С	0	С	0	0	С	0	С	0	C	С	С	С	С	347	51
* C = Clo	sed	lor	On	; 0	= (pen	or	off				Cor	ntinu	ed —		

TABLE 3-11. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES
USING 1.814 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	212	2 ¹³	Usable Sectors	in Runt Sector
88	С	0	0	С	0	С	0	С	0	С	С	С	С	C	343	56
89	C	0	С	C	О	С	0	С	0	C	С	С	С	C	339	69
90	0	0	0	0	C	С	0	С	0	С	С	С	С	С	336	0
91	0	0	С	0	С	С	0	С	0	C	С	С	С	С	332	28
92	0	0	0	C	С	С	0	С	0	C	С	С	С	С	328	64
93	C	C	0	C	C	C	0	C	0	C	C	С	С	С	325	15
94	C	С	С	C	С	С	0	С	0	C	С	С	С	C	321	66
95	0	С	0	0	0	0	С	С	0	С	С	С	С	C	318	30
96	C	0	С	0	0	0	C	C	0	C	С	С	С	С	315	0
97	C	0	0	C	0	0	C	С	0	C	С	С	С	С	311	73
98	0	0	С	C	0	0	С	С	0	C	С	С	С	С	308	56
99	C	<u>C</u>	C	C	0	0	C	C	0	C	С	С	С	С	305	45
100	0	С	0	0	С	0	C	С	0	C	С	С	С	С	302	40
101	С	0	С	0	С	0	С	С	0	C	С	С	С	С	299	41
102	0	0		C	C	0	С	С	0	C	С	С	С	С	296	48
103	C	C	0	С	С	0	C	С	0	C	С	С	С	С	293	61
104	0	С	С	С	С	0	C	С	0	С	С	С	С	С	290	80
105	0	0	0	0	0	C	C	C	_0_	C	С	С	С	С	288	0
106	C	С	0	0	0	С	С	С	0	С	С	С	С	С	285	30
107	0	С	С	0	О	С	С	С	0	C	С	С	С	С	282	66
108	0	<u> </u>	<u> </u>	C	0	C	C	<u></u>	0	C	C	C	С	C	280	0
109	C	C	0	C	0	C	C	C	0	C	C	C	C	С	277	47
110	0	С	С	С	0	C	С	С	0	С	С	С	С	С	274	100
111	0	<u> </u>	0	0	<u></u>	<u> </u>	C	<u>C</u>	<u> </u>	C	<u>C</u>	C	C	C	272	48
112	0	C	0	0	C	С	C	С	0	C	С	С	C	C	270	0
113	C	0	C	0	C	C	C	С	0	С	C	С	С	C	267	69
114	C	_ <u>C</u>	<u></u>	0	<u>C</u>	<u>C</u>	C	<u></u>	0	C	<u></u>	C	C	C	265	30
115	0	C	0	C	C	C	C	C	0	C	C	С	С	C	262	110
116	0	0	С	C	C	C	C	С	0	C	C	С	C	C	260	80
117	<u> </u>	<u>C</u> _	C	C	<u>C</u>	<u>C</u>	C	<u>C</u>	<u> </u>	C	<u>C</u>	C	C	C	258	54
118	0	0	0	0	0	0	0	0	C	C	C	С	С	C	256	32
119	0	C	0	0	0	0	0	0	C	C	С	C	C	С	254	14
120	0	0	C	0	0	0	0	0	<u>C</u>	C	C	C	C	C	252	0
121	C	C	C	0	0	0	0	0	C	C	C	C		C	249	111
122	C	0	0	C	0	0	0	0	С	C	С	C	C	С	247	106
123	C	<u>C</u>	0	C	0	0	0	0	<u>C</u>	C	C	C	C	C	245	105
124	C	0	C	C	0	0	0	0	C	C	C	C	C	C	243 241	108 115
125	C	C	C	C	0	0	0	0	C	C	C C	C	C	C	241	1
126	0	0	0	0	_ <u>C</u>	0	0	0	<u>C</u>		C		C	C	238	0 14
127	0	C	0	0	C	0	0	0	C	C	C	C	C	C	238 236	32
128	0	0	С	0	U	0	O		С 			ر		C	230	32
* C = Clo	osed	l or	On	; 0	= C	pen	or	off								

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TABLE 3-12. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.814 MHz (BYTE) CLOCK AND ROUND-UP METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	2 ⁰	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	211	212	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	С	С	С	С	0	0	С	0	0	0	С	7560	7560
5	0	0	0	0	0	С	C	0	0	0	0	С	0	С	6048	6048
6	0	0	0	0	С	0	C	0	0	0	C	С	0	С	5040	5040
7	0	0	0	0	0	C	0	0	С	C	С	С	0	С	4320	4320
8	0	0	C	C	С	C	0	0	С	0	0	0	С	С	3780	3780
9	0	0	0	0	0	C	C	С	0	C	0	0	С	С	3360	3360
10	0	0	0	0	С	С	0	0	0	0	С	0	С	C	3024	3024
11	0	С	0	0	0	0	C	0	С	0	С	0	С	С	2750	2740
12	0	0	0	C	0	С	0	0	0	С	C	0	С	С	2520	2520
13	C	0	0	C	0	С	С	С	0	С	С	0	С	С	2327	2316
14	0	0	0	0	С	0	0	С	С	С	С	0	С	С	2160	2160
15	0	0	0	0	0	С	0	0	0	0	0	С	С	С	2016	2016
16	0	C	С	С	С	0	0	С	0	0	0	С	С	С	1890	1890
17	l c	0	С	l c	0	0	lo	0	С	0	0	С	С	С	1779	1776
18	0	0	0	0	С	С	c	0	С	lo	0	С	С	С	1680	1680
19	0	0	0	С	0	0	C	C	С	0	0	С	С	С	1592	1584
20	0	0	Ō	c	C	ō	0	ō	ō	c	ō	С	c	C	1512	1512
21	0	o	ō	0	o	C	c	o	ō	C	o	C	C	C	1440	1440
22	c	0	0	0	0	C	0	c	0	c	0	C	C	C	1375	1365
23	١c	ō	c	c	C	ō	l c	C	Ö	l c	o	C	c	c	1315	1310
24	0	0	С	0	C	ō	0	0	С	C	o	C	C	C	1260	1260
25	ō	C	c	o	0	ō	c	0	C	c	ō	C	C	C	1210	1200
26	0	Ō	Ċ	o	C	Ċ	c	ō	C	c	Ō	С	C	C	1164	1140
27	0	0	0	0	0	c	0	C	c	C	o	С	c	C	1120	1120
28	o	0	0	c	0	0	C	C	C	c	0	C	C	C	1080	1080
29	l c	o	Ċ	c	0	·C	C	Č	Ċ	c	ō	С	C	С	1043	1036
30	lo	0	Ō	0	C	ō	o	0	o	0	C	С	c	С	1008	1008
31	0	0	0	ō	C	C	0	0	0	0	C	C	C	C	976	960
32	C	C	Ċ	c	0	ō	c	0	Ö	0	C	C	c	c	945	945
33	c	c	o	C	o	C	c	0	o	0	C	C	c	C	917	896
34	0	c	C	0	0	0	0	c	0	0	c	C	C	C	890	870
35	0	0	o	0	0	C	0	C	o	0	C	C	C	c	864	864
36	0	0	0	C	C	C	0	C	0	0	C	C	C	C	840	840
37	0	c	c	c	0	0	c	c	0	0	c	C	C	C	818	792
38	0	0	C	o	0	C	c	C	o	0	C	C	c	C	796	788
39	0	0	0	C	C	C	C	C	0	0	C	C	C	C	776	752
40	0	0	c	C	0	0	0	0	C	0	c	C	C	C	756	756
41	0	C	С	c	c	0	0	0	C	0	C	C	C	C	738	720
42	0	0	0	0	C	C	0	0	C	0	С	C	C	C	720	720
43	0	0	0	0	0	0	c	0	c	0	C	C	C	C	704	672
44	0	0	0	0	c	0	c	0	С	0	C	С	C	C	688	656
45	0	0	0	0	0	C	c	0	C	0	C	c	C	c	672	672
-3-3												<u> </u>		· ·	012	072
* C = Cl	osec	loi	On	; 0	= C	pen	or	off				<i>a</i>	ntinu	od		
			_							_		COI	ILTIIU	ea		

TABLE 3-12. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.814 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tche	es*					Bytes in	Bytes
of Sectors	2 ⁰	2 ¹	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 ¹³	Early Sectors	in Last Sector
46	0	С	С	С	0	С	С	0	С	0	С	С	С	С	658	630
47	0	0	С	C	С	С	C	0	С	0	С	С	С	C	644	616
48	0	C	0	C	0	0	0	C	С	0	С	С	С	С	630	630
49	0	С	С	0	С	0	0	С	С	0	С	С	С	С	618	576
50	С	С	0	0	0	С	0	С	С	0	С	С	С	C	605	595
51	С	<u>C</u>	C	C	0	C	0	C	C	0	C	С	С	С	593	590
52	0	С	0	C	С	C	0	С	C	0	С	C	С	C	582	558
53	С	0	С	0	0	0	C	С	С	0	С	C	C	C	571	548
54	0	0	0	0	С	0	C	С	С	0	С	C	С	С	560	560
55	0	С	0	C	С	0	C	С	С	0	C	С	С	C	550	540
56	0	0	C	0	0	С	C	С	С	0	С	С	C	C	540	540
57	С	0	C	C	0	С	C	С	C	0	C	С	С	С	531	504
58	0	С	C	0	С	С	С	С	С	0	С	С	С	С	522	486
59	C	С	С	C	С	С	C	С	С	0	С	С	С	C	513	486
60	0	0	0	C	0	0	0	0	0	C	С	С	C	C	504	504
61	0	0	0	0	С	0	0	0	0	C	С	С	С	C	496	480
62	0	0	0	C	С	0	0	0	0	С	С	С	С	C	488	472
63	0	0	0	0	0	<u> </u>	0	0	0	C	С	С	С	С	480	480
64	С	С	С	0	0	С	0	0	0	С	С	С	С	C	473	441
65	0	С	С	C	0	С	0	0	0	С	С	С	С	C	466	416
66	C	0	C	0	C	C	0	0	0	C	С	С	С	C	459	405
67	0	0	С	C	С	С	0	0	0	С	С	С	С	C	452	408
68	С	С	0	0	0	0	C	0	0	С	С	С	С	c	445	425
69	С	0	0	C	0	0	C	0	0	C	<u>C</u>	С	С	C	439	388
70	0	0	0	0	С	0	С	0	0	С	C	С	С	C	432	432
71	0	С	С	0	С	0	C	0	0	C	С	С	С	C	426	420
72	0	0	С	C	C	0	C	0	0	C	C	С	С		420	420
73	С	0	0	0	0	С	C	О	0	С	С	C	С	c	415	360
74	С	С	С	0	0	С	С	0	0	С	С	C	С	С	409	383
75	0	0	C	C	0	<u>C</u>	C	0	0	С	C	С	С	С	404	344
76	0	С	0	0	С	C	C	0	0	С	С	С	С	С	398	390
77	С	С	C	0	С	C	С	0	0	С	С	С	С	С	393	372
78	0	0	C	C	C	C	C	0	0	C	С	С	С	С	388	364
79	С	0	0	0	0	0	0	С	0	С	C	С	С	C	383	366
80	0	С	С	0	0	0	0	С	0	С	С	С	С	С	378	378
81	0	<u></u>	0	C	0	<u> </u>	0	C	0	C	С	С	С	С	374	320
82	С	С	С	С	0	0	0	С	0	С	С	С	С	С	369	351
83	С	С	0	0	С	0	0	С	0	С	С	C	С	C	365	310
84	0	0	0	C	C	0	0	C	0	C	<u>C</u>	C	С	С	360	360
85	0	0	C	С	С	0	0	С	0	С	С	С	С	C	356	336
86	0	0	0	0	0	С	0	С	0	C	С	С	С	С	352	320
87	0	0	С	0	0	С	0	С	0	С	С	С	С	C	348	312
* C = Clo	osed	or	On	; 0	= C	pen	or	off				. Co:	ntinu	red —		

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TABLE 3-12. SECTOR SELECT SWITCH SETTINGS -- 741 MB DRIVES USING 1.814 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*			<u></u>		Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	21.	212	2 2 13	Early Sectors	in Last Sector
88	0	0	0	С	0	С	0	С	0	С	С	С	С	С	344	312
89	0	0	С	C	0	C	0	С	0	C	С	С	С	С	340	320
90	0	0	0	0	C	_ <u>C</u> _	0	C	0	C	С	С	C	С	336	336
91	С	С	0	0	С	С	0	С	0	C	С	С	С	С	333	270
92	С	С	С	0	С	С	0	C	0	C	С	С	С	C	329	301
93	0	C	0	C	_ <u>C</u>	_ <u>C</u>	0	С	0	C	C	С	С	C	326	248
94	0	С	С	C	С	C	0	С	0	С	С	C	С	С	322	294
95	C	0	0	0	0	0	C	С	0	С	С	С	С	С	319	254
96	C	_0	С	0	0	0	C	С	0	C	<u>C</u>	С	С	С	315	315
97	0	0	0	C	0	0	С	С	0	C	С	С	С	С	312	288
98	C	С	0	C	0	0	С	С	0	С	С	С	С	С	309	267
99	0	<u>C</u>	С	C	0	0	C	C	0	C	C	С	С	С	306	252
100	C	0	0	0	С	0	C	С	0	C	С	С	С	С	303	243
101	0	0	С	0	С	0	С	С	0	С	С	С	С	С	300	240
102	C	C	<u> </u>	0	_ <u>C</u>	0	C	C	0	C	С	С	С	С	297	243
103	0	С	0	C	С	0	С	С	0	С	С	С	С	С	294	252
104	C	0	С	C	С	0	C	C	0	С	С	С	С	С	291	267
105	<u> </u>	0	0	0	0	C	C	С	0	C	С	С	С	С	288	288
106	0	С	0	0	0	С	С	С	0	C	С	С	С	С	286	210
107	C	0	C	0	0	C	C	C	0	С	С	C	С	С	283	242
108	0	0	0	C	0	C	С	C	0	С	С	C	С	С	280	280
109	0	C	0	C	0	C	С	C	0	С	С	С	С	С	278	216
110	C	0	С	C	0	C	C	C	0	C	С	С	С	С	275	265
111	C	C	C	C	0	C	C	C	0	C	C	C	C		273	210
112	0	C	0	0	C	C	C	C	0	C	C	C	C	С	270	270
113	0	0	C	0	C	C	C	C	0	C	С	C	C	C	268	224
114	0	_ <u>C</u>	C	0	C	<u> </u>	C	C	0	C	C	C	C	C	266	182
115	C	0	0	C	C	C	C	C	0	C	C	C	С	C	263	258
116	C	C	0	C	С	C	C	C	0	C	С	C	C	C	261	225
117	C	0	<u>C</u>	C	C	C	C	C	0	C	C	C	C	C	259	196
118 119	C	С 0	С О	C	C O	C O	C	C	C	C	C C	C C	C	C C	257 255	171 150
119	0		C	1			1			1			l .		255 252	252
121	0	<u> </u>	C	0	0	0	0	0	C	C	C C	C C	C	C C	252 250	252
121	0	0	0	C	0	0	0	0	C	C	C	C	c	C	250 248	232
123	1	C		C		0	l		C	i .	С		C	C	248 246	232
124	0	0	O C	C	0	0	0	0	C	C	C	C C	C	C	244	228
124	0	C	C	C	0	0	0	0	C	C	C	C	C	C	244	232
125	1	0	0	1	C	0	1	-	С	I	C		C	C	242 240	232
127	C	0	0	0		0	0	0	C	C	c	C	C	C	239	126
128	C	C		0	C	0	0	0	С	C	С	C	C	C	237	141
120	`	C	J	"	C	J		J	C	[C	C			231	177
* C = Clo	osed	l or	On	; 0	= C	pen	or	off		J			L			<u> </u>

TABLE 3-13. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	2 ²	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 2 13	Usable Sectors	in Runt Sector
4	0	0	0	0	0	0	С	0	С	0	С	0	0	С	10272	0
5	0	С	0	C	С	0	0	С	0	С	0	C	0	С	8217	3
6	C	C	0	C	0	C	0	0	0	C	C	С	0	С	6847.50	3
7	С	C	С	0	C	С	0	С	0	0	0	0	С	С	5869.50	1.50
8	0	0	0	0	0	C	0	С	0	С	0	0	С	С	5136	0
9	C	<u> </u>	_ <u>C</u> _	C	<u> </u>	0	0	0	<u> </u>	0	<u> </u>	0	C	C	4564.50	7.50
10	C	0	C	C	0	0	C	0	С	0	С	0	C	C	4108.50	3
11	0	C	C	0	0	0	C	0	0	C	C	0	С	С	3735	3
12	0	C	C	0	<u>C</u>	<u> </u>	0	0	<u></u>	C	<u>C</u>	0	C	C	3423	12
13	C	0	C	0	0	0	C	C	C	C	C	0	C	C	3160.50	1.50
14	0	0	С	C	C	0	C	0	0	0	0	С	C	C	2934	12
15	0	<u> </u>	C	C	C	<u> </u>	C	<u>_</u>	0	0	0	C	С	C	2739	3
16	0	0	0	0	C	0	C	0	С	0	0	C	C	C	2568	0
17	C	0	C	0	С	C	0	C	C	0	0	С	C	C	2416.50	7.50
18	C	<u>C</u>	<u>C</u>	C	0	0	0	0	0	C	0	C	C	C	2281.50	21
19 20	C	C	C C	C	C	0	C	0	0	C	0	C	C	C	2161.50	19.50
21	0		_	0	0	C	0	C	0	C	0	C	C	C	2053.50 1956	18
22	C	0 C	0	0	0	_C _C	<u>C</u>	<u>C</u>	<u>0</u>	C	0	C	C	C	1867.50	<u>12</u> 3
23	0	C	0	C	C	0	C	0	C	C	0	C	C	c	1785	33
24	C	С	0	C	0	0	0	C	C	c	0	C	C	c	1711.50	12
25	C	0	0	C		c	0	C	C	C	0	C	C	c	1642.50	25.50
26	C	C	o :	0	0	C	C	C	С	C	0	C	C	c	1579.50	23.30
27	0	C	0	C	0	o '	o	0	0	0	c	C	C	c	1521	21
28	0	c	C	C	0	C	0	0	0	0	C	c	C	c	1467	12
29	0	0	0	0	C	0	c	0	0	0	C	c	C	c	1416	24
30	C	C	C	C	0	C	C	0	0	0	C	c	C	c	1369.50	3
31	C	0	C	C	0	0	0	c	0	0	c	c	C	c	1324.50	28.50
32	0	0	o	c	0	C	0	C	0	0	C	c	C	c	1284	0
33	0	C	o	0	o	o	C	C	0	0	C	C	C	c	1245	3
34	C	C	0	C	C	0	c	c	0	0	c	C	C	C	1207.50	33
35	0	C	o	0	C	C	c	C	o	0	C	c	C	c	1173	33
36	0	0	O	c	o	0	o	0	C	0	C	C	C	C	1140	48
37	0	0	C	c	C	0	0	0	C	0	C	C	C	C	1110	18
38	o	ō	o	o	C	C	ō	o	C	o	C	c	C	c	1080	48
39	o	C	0	o	0	o	c	0	С	0	C	С	С	c	1053	21
40	0	0	c	ō	C	0	c	0	С	0	C	С	C	C	1026	48
41	0	0	С	0	Ō	C	c	0	С	0	C	C	С	C	1002	6
42	0	0	С	0	С	С	С	0	С	0	С	С	С	С	978	12
43	С	С	0	0	0	0	0	С	С	0	С	С	С	С	955.50	1.50
44	0	С	0	0	C	0	0	C	С	0	С	С	С	C	933	36
45	0	0	0	0	0	С	0	С	С	0	С	С	С	С	912	48
* C = Clc	sed	or	On	; 0			L									
												Cor	ntinu	ied —		

TABLE 3-13. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	.tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
46	С	0	С	С	0	С	0	С	С	0	C	С	С	С	892.50	33
47	0	С	0	C	С	С	0	C	С	0	С	С	С	C	873	57
48	0	C	C	0	0	0	C	C	С	0	С	С	С	С	855	48
49	С	0	0	0	С	0	C	С	С	0	С	С	С	C	838.50	1.50
50	С	0	С	C	С	0	C	С	С	0	С	С	С	С	820.50	63
51	C	C	С	0	0	<u> </u>	C	<u>C</u>	C	0	<u></u>	C	С	С	805.50	7.50
52	0	С	0	0	С	С	C	С	С	0	С	C	С	C	789	60
53	0	0	С	С	С	С	C	С	С	0	C	C	С	C	774	66
54	C	0	C	0	0	0	0	0	0	C	_ <u>C</u> _	C	C	C	760.50	21
55 56	0	C	С	C	0	0	0	0	0	C	C	С	C	C	747	3
56	C	C	С	0	С	0	0	0	0	C	C	C	C	C	733.50	12
57	0	0	0	0	0	<u> </u>	0	_0_	0	C	_ <u>c</u>	<u></u>	C	<u>C</u>	720	48
58	0	0	0	C	0	C	0	0	0	C	C	C	C	С	708	24
59	0	0	0	0	С	C	0	0	0	C	С	C	C	C	696	24
60	0	<u> </u>	_0_	C	C	<u> </u>	0	_0_	0	C	<u></u>	C	C	C	684	48
61 62	C	C	C	C	C	C	0	0	0	C	C	C	C	C	673.50	4.50
	C	C	C	0	0	0	C	0	0	C	C	C	С	С	661.50	75 75
63 64	9	C	<u>C</u>	C	0	<u> </u>	C	<u> </u>	<u> </u>	C	<u>C</u>	C	C	C	651	75
65	0	0 C	C	0	C	0	C	0	0	C	C	C	C	C	642	0
66	_	_	0	C	_	0	C	0	0	C	C	C	С	С	631.50	40.50
	C	<u> </u>	<u> </u>	0	<u> </u>	<u>C</u>	C	0	0	C	<u>C</u>	C	C	C	622.50	3
67 68	0	0	0	C	0	C	C	0	0	C	C	C	C	C	612	84
69	0	C	С	C	0	C	C	0	0	C	C	C	С	C	603	84
70	C	0	<u>C</u>	C	C	<u>C</u>	C	0	0	C		C	C	C	594 586.50	102 33
70 71	C	C	C	C	C	C C	C	0	0	C	C	C	C	C	577.50	85.50
72	0	0	C	0	0	0	0	C	0	C	C	c	C	C	577.50 570	48
73	C	0	-	c	- 0	0	0	C	0	c	C	c	c	c	562.50	25.50
74	0	C	C	c	0	0	0	C	0	C	C	c	C	c	555	18
75	C	C	o	0	C	o	0	C	o	C	C	c	C	C	547.50	25.50
76	0	0	0	C	C	0	0	_	- 0	c		c	c	C	540	48
77	C	0	C	c	C	o	0	C	o	c	C	c	C	c	532.50	85.50
78	С	Ö	o	0	0	c	0	c	0	c	c	C	c	c	526.50	21
79	0	C	C	0	0	C	O	C	0	C	C	c	C	c	519	87
80	o	C	ō	C	o	C	0	c	Ö	c	C	c	c	c	513	48
81	0	C	c	c	0	Ċ	o	C	o	C	C	c	C	С	507	21
82	0	C	0	0	C	C	ō	c	0	С	c	c	c	c	501	6
83	0	С	С	0	С	C	0	С	0	С	С	c	С	С	495	3
84	0	С	0	С	С	С	0	С	0	С	C	С	С	С	489	12
85	0	С	С	С	С	С	0	С	0	С	С	С	С	С	483	33
86	0	С	0	0	0	0	С	С	0	С	С	c	С	c	477	66
87	0	С	С	0	0	0	С	С	0	C	С	c	С	c	471	111
* C = Clo	sed	or	On	; 0	= C	pen	or	off		L						
												Con	itinu	ed —		

TABLE 3-13. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
88	С	0	0	С	0	0	С	С	0	С	С	С	С	С	466.50	36
89	С	0	С	C	0	0	С	С	0	C	С	c	С	C	460.50	103.50
90	0	0	0	0	C	0	C	_ <u>C</u>	0	<u>c</u>	<u> </u>	<u> </u>	C	C	456	48
91	C	C	0	0	C	0	C	C	0	C	C	C	C	C	451.50	1.50
92 93	C	C	C O	0	C	0	C	C C	0	C	C C	C	C C	C	445.50 441	102 75
94	C	0	c	c	- C	0	C	c	0	c	C	c	C	C	436.50	57
95	0	0	0	0	0	C	C	C	0	c	C	c	C	c	432	48
96	C	c	Ö	٥	0	C	C	C	o	C	C	c	C	c	427.50	48
97	0	C	C	o	_ <u>~</u>	C	c	C	0	c	C	c	c	C	423	57
98	С	0	0	С	0	C	c	C	ō	c	c	c	C	c	418.50	75
99	0	0	С	С	0	С	С	С	0	С	С	c	C	С	414	102
100	С	С	С	С	0	С	С	С	0	С	С	С	С	С	409.50	138
101	C	0	0	0	С	С	С	С	0	C	С	C	С	C	406.50	31.50
102	0	0	С	0	C	C	С	С	0	С	С	C	С	С	402	84
103	С	С	С	0	С	С	С	С	0	С	С	C	С	C	397.50	145.50
104	С	0	0	C	С	С	C	C	0	С	С	C	С	C	394.50	60
105	0	0	C	C	C	C	C	<u>C</u>	0	C	_ <u>C</u> _	C	С	C	390	138
106	0	C	C	C	C	C	С	C	0	C	C	C	C	C	387	66
107 108	0	0	0	0	0	0	0	0	C	C	C	C	C	C	384	0
109	C	0	<u> </u>	0	0	0	0	0	C	C	C	C	C	$\frac{c}{c}$	379.50 376.50	102 49.50
110	C	C	C	0	0	0	0	0	C	C	C	c	C	c	373.50	3
111	0	C	o	C	0	Ö	0	0	c	c	C	c	C	c	369	129
112	0	0	C	c	0	0	0	0	C	c	C	c	C	c	366	96
113	0	C	Ċ	C	0	ō	0	ō	Ċ	C	Ċ	c	C	c	363	69
114	0	0	0	0	С	0	0	0	С	С	С	c	С	c	360	48
115	0	С	0	0	С	0	0	0	С	С	С	C	С	С	357	33
116	0	0	C	0	С	0	0	0	С	С	С	C	C	C	354	24
117	0	С	С	0	С	0	0	0	C	С	С	С	С	С	351	21
118	0	0	0	С	С	0	0	0	С	С	C	C	С	C	348	24
119	0	С	0	С	С	0	0	0	C	С	С	C	С	C	345	33
120	0	0	С	С	С	0	0	0	С	С	C	C	С	<u>c</u>	342	48
121	0	C	C	C	C	0	0	0	C	С	C	C	C	C	339	69
122	0	0	0	0	0	C	0	0	C	C	C	C	C	C	336	96
123	<u> </u>	<u>C</u>	0	0	<u> </u>	C	0	0	C	C	C	C	C	<u>C</u>	333	129
124	0	0	C	0	0	C	0	0	C	C	C C	C	C	C	330	168 25.50
125 126	C	0	C	0	0	C	0	0	C C	C	C	C	C	CC	328.50 325.50	25.50 75
127	C	0	0	C	<u> </u>	C	0	0	C	C	C	c	C	c	323.50	130.50
128	0	C	0	C	0	C	0	0	C	C	C	c	C	c	322.50	0
		Č	-	Ĭ	•	~		J	_	`	_	~	•	~	721	J
* C = Clc	sed	or	On	; 0	= C	pen	or	off		<u> </u>				<u></u>	· · · · · · · · · · · · · · · · · · ·	

TABLE 3-14. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	211	212	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	0	0	0	С	0	С	0	С	0	0	С	10272	10272
5	С	0	0	C	С	0	0	С	0	C	0	С	0	c	8218.50	8214
6	0	С	0	C	0	C	0	0	0	C	С	С	0	C	6849	6843
7	0	С	С	0.	C	C	0	С	0	0	0	0	С	С	5871	5862
8	0	0	0	0	0	С	0	С	0	С	0	0	С	C	5136	5136
9	0	0	С	C	<u>C</u>	0	0	0	0	0	C	0	С	С	4566	4560
10	0	0	C	C	0	0	C	0	С	0	С	0	С	C	4110	4098
11	С	0	С	0	0	0	C	0	0	C	С	0	С	С	3736.50	3723
12	C	0	_C	0	С	0	0	0	С	C	С	0	С	С	3424.50	3418.50
13	0	0	C	0	0	0	C	С	С	C	С	0	С	C	3162	3144
14	C	С	0	C	С	0	C	0	0	0	0	С	С	С	2935.50	2926.50
15	C	0	С	C	C	0	C	С	0	0	0	С	С	С	2740.50	2721
16	0	0	0	0	С	0	С	0	С	0	0	C	С	С	2568	2568
17	0	0	С	0	С	С	0	C	С	0	0	С	С	C	2418	2400
18	0	С	С	C	0	0	0	0	0	С	0	С	С	С	2283	2277
19	0	С	С	С	С	0	C	0	0	С	0	C	С	С	2163	2154
20	0	С	С	0	0	С	0	С	0	C	0	С	С	c	2055	2043
21	С	С	С	0	0	С	C	С	0	C	0	С	С	С	1957.50	1938
22	0	С	0	0	0	С	0	0	С	С	0	C	С	С	1869	1839
23	C	0	0	C	С	0	C	0	С	C	0	С	С	C	1786.50	1785
24	0	С	0	С	0	0	0	С	С	С	0	С	С	С	1713	1689
25	0	0	0	C	С	С	0	С	С	С	0	С	С	С	1644	1632
26	0	С	0	0	0	C	C	С	С	c	0	С	С	c	1581	1563
27	С	0	0	C	0	0	0	0	0	0	С	c	С	С	1522.50	1503
28	С	0	С	С	0	С	0	0	0	0	С	С	С	С	1468.50	1438.50
29	С	С	С	C	0	0	C	0	0	0	С	С	С	c	1417.50	1398
30	0	С	C	C	0	С	С	0	0	0	С	С	С	c	1371	1329
31	0	0	С	С	0	0	0	С	0	0	С	С	С	С	1326	1308
32	0	0	0	c	0	С	0	С	0	0	С	С	С	c	1284	1284
33	C	0	0	0	0	0	C	С	0	0	С	c	С	c	1246.50	1200
34	0	C	0	С	С	0	С	С	0	0	С	С	С	С	1209	1191
35	С	0	0	0	С	С	С	С	0	0	С	С	С	c	1174.50	1155
36	С	_C	C	0	0	0	0	0	С	0	С	С	С	c	1141.50	1135.50
37	С	С	0	С	С	0	0	0	С	0	С	С	С	С	1111.50	1074
38	С	С	С	C	0	С	0	0	С	0	С	С	С	c	1081.50	1072.50
39	С	0	0	0	0	0	С	0	С	0	С	c	С	c	1054.50	1017
40	С	C	0	0	C	0	c	0	С	0	С	С	С	c	1027.50	1015.50
41	С	C	0	0	0	C	c	Ō	C	0	С	c	C	c	1003.50	948
42	С	С	0	0	С	С	С	0	С	0	С	С	С	С	979.50	928.50
43	0	C	0	0	0	0	0	C	С	0	С	С	С	С	957	894
44	С	0	0	0	C	0	0	C	C	0	c	c	С	c	934.50	904.50
45	c	C	c	C	C	ō	0	C	C	o	C	c	C	c	913.50	894
* C = Clo	Sed		. On	. 0	= ()nen		Of f		<u> </u>		i				
				, ,				<u> </u>				Cor	ntinu	ed —		

TABLE 3-14. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	25*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 2 13	Early Sectors	in Last Sector
46	0	0	С	С	0	С	0	С	С	0	С	С	С	С	894	858
47	C	0	0	C	С	С	0	С	С	0	С	С	С	C	874.50	861
48	C	0	C	0	0	0	C	C	C	0	_ <u>C</u>	_C_	C	С	856.50	832.50
49	0	О	0	0	С	0	C	С	С	0	С	C	С	C.	840	768
50	0	0	С	C	С	0	C	С	С	0	С	С	С	С	822	810
51	0	С	<u>C</u>	0	0	C	C	C	<u>C</u>	0	C	C	C	C	807	738
52	C	0	0	0	С	C	С	C	С	0	С	С	С	С	790.50	772.50
53	C	С	0	C	C	C	C	С	C	0	C	C	C	C	775.50	762
54	0	0	<u> </u>	0	<u> </u>	<u> </u>	0	0	<u> </u>	C	<u> </u>	<u> </u>	C	C	762	702
55 56	C	0	C	C	0	0	0	0	0	C	C	C	C	C	748.50	669
56	0	С	C	0	C	0	0	0	0	C	C	C	С	C	735	663
57	C	C	<u> </u>	L C	_ <u>c</u>	0	0	0	<u> </u>	C	C	C	C	<u>C</u>	721.50	684
58	C	C	C	0	0	C	0	0	0	C	С	С	C	С	709.50	646.50
59	C	С	С	C	0	C	0	0	0	C	C	С	С	C	697.50	633
60	C	<u></u>	<u></u>	0	C	C	0	0	0	C	<u></u>	C	C	_ <u>C</u>	685.50	643.50
61	0	C	C	C	C	C	0	0	0	C	C	C	C	С	675	588
62	0	C	C	0	0	0	C	0	0	C	C	C	С	C	663	645
63	C	0	<u></u>	C	0	<u> </u>	C	0	<u> </u>	C	<u></u>	C	C	C_	652.50	633
64	0	0	C	0	C	0	C	0	0	C	C	С	C	C	642	642
65	0	С	0	C	C	0	C	0	0	C	C	C	C	C	633	576
66	0	<u> </u>	<u> </u>	0		<u>_</u>	C	0	0	C	C	C	C	C	624	528
67	C	C	С	0	0	C	C	0	0	C	С	C	C	С	613.50	597 586.50
68	C	0	C	C	0	C	C	0	0	C	C	C	C	C	604.50	
69 70	<u>C</u>	<u>C</u>	0	0 C	C	C	C	0	0	C	C	C	C	C C	595.50 588	594 516
70 71	0	C	C	C	C	C	C	0	0	C	C	C	C	C	579	558
72	C	C	0	0	0	0	0	C	0	C	С	C	C	C	571.50	511.50
73	6	0	- 0	 	0	0	0	<u> </u>	-	c	-	C	C	c	564	480
74	c	0	C	C	0	0	0	C	0	c	C	C	C	C	556.50	463.50
75	0	C	0	0	C	o	0	C	0	C	С	C	C	C	549	462
76	c	<u> </u>	c	0	c	-	0	C	- 0	C	<u> </u>	C	C	C	541.50	475.50
77	0	0	C	C	C	0	0	C	0	c	С	C	C	c	534	504
78	0	0	0	0	0	C	0	C	0	C	C	C	C	C	528	432
79	c	0	c	0	0	c	0	C	-	c	C	С	C	C	520.50	489
80	c	0	Ö	C	0	C	0	C	0	C	C	C	C	C	514.50	442.50
81	C	0	c	C	0	C	0	C	0	C	C	C	C	C	508.50	408
82	C	0	-	ō	C	c	0	C	0	c	c	C	C	C	502.50	385.50
83	C	0	c	0	C	C	0	C	0	C	C	C	C	C	496.50	375
84	C	0	o	C	C	C	0	C	o	c	C	c	c	C	490.50	376.50
85	c	0	C	C	c	C	0	c	0	C	C	C	c	С	484.50	390
86	c	o	o	0	0	ō	c	C	Ö	c	C	Č	C	C	478.50	415.50
87	С	0	C	0	0	0	C	C	0	С	C	C	С	С	472.50	453
* C = Clo	osed	or	On	; 0	= C	pen	or	off		·			L			<u> </u>
												Co	ntinu	ied —		

TABLE 3-14. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 1.6 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	27	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
88	0	0	0	С	0	0	С	С	0	С	С	С	С	С	468	372
89	0	0	С	С	0	0	С	С	0	C	С	С	С	С	462	432
90	C	<u> </u>	C	C	0	0	C	<u>C</u>	0	C	C	<u> </u>	C	C	457.50	370.50
91	0	C	0	0	С	0	С	C	0	C	C	C	C	C	453	318
92	0	C	С	0	C	0	C	C	0	C	C	C	C	C	447	411
93 94	0	0	C	C	C	0	C	C	0	C	C	C	C C	C	442.50	378 354
94 95		0		_	C	0	C	C	0	ı	C	- 1	C		438 433.50	334
95 96	С	C C	C	C	0	O C	C	C	0	C	C	C	C	C	433.50	339
97	O C	0	0 C	0	-0	c	C	c	0	C	C	c	C	C	424.50	336
98	0	0	0	C	0	C	c	C	0	C	C	c	C	C	424.50	348
99	С	С	0	c	0	C	C	C	0	C	С	c	C	c	415.50	369
100	0	- C	C	C	0	C	c	c	0	C	C	c	C	C	413.50	399
101	0	0	0	0	c	C	c	C	0	c	C	c	C	C	408	288
102	C	C	0	0	C	c	c	С	0	C	C	c	C	c	403.50	334.50
103	ō	c	C	ō	c	C	c	c	0	c	C		c	Č	399	390
104	o	o	0	C	C	c	C	C	o	C	c	c	C	c	396	300
105	C	c	o	C	C	c	c	C	ō	c	C	c	C	c	391.50	372
106	C	0	c	C	C	C	C	C	0	C	Ċ	C	C	C	388.50	295.50
107	0	ō	0	0	ō	ō	o	o	Ċ	C	Ċ	c	Č	C	384	384
108	0	С	0	0	0	0	0	0	С	c	С	С	С	С	381	321
109	0	0	С	0	0	0	0	0	С	С	С	С	С	С	378	264
110	0	С	C	0	0	0	0	0	С	C	С	С	С	С	375	213
111	С	0	0	С	0	0_	0	0	C	C	С	C	С	С	370.50	333
112	С	С	0	С	0	0	0	0	С	С	С	С	С	O	367.50	295.50
113	С	0	С	С	0	0	0	0	С	C	С	С	С	С	364.50	264
114	С	С	С	С	0	0	0	0	С	С	С	С	С	С	361.50	238.50
115	С	0	0	0	С	0	0	0	С	C	С	С	С	C	358.50	219
116	С	С	0	0	С	0	0	0	С	C	C	C	С	С	355.50	205.50
117	С	0	С	0	C	0	0	0	С	C	С	С	C	С	352.50	198
118	С	С	С	0	С	0	0	0	С	C	С	С	С	С	349.50	196.50
119	С	0	0	C	С	0	0	0	С	C	C	C	С	C	346.50	201
120	С	С	0	С	C	0	0	0	С	C	C	С	<u> </u>	С	343.50	211.50
121	C	0	С	С	С	0	0	0	С	С	С	С	С	С	340.50	228
122	С	С	С	С	С	0	0	0	С	C	С	С	С	С	337.50	250.50
123	C	0	0_	0	0	C	0	0	C	C	С	C	C	С	334.50	279
124	C	С	0	0	0	С	0	0	С	C	C	С	С	C	331.50	313.50
125	0	0	C	0	0	С	0	0	C	C	C	С	C	С	330	168
126	9	<u>c</u>	C	0	<u> </u>	C	<u> </u>	0	<u>C</u>	드	C	C	C	C	327	213
127	0	0	0	C	0	C	0	0	C	C	C	C	C	С	324	264
128	0	С	0	С	0	С	0	0	С	C	Ċ	С	С	С	321	321
	L			L			i			Ь						
* C = Clo	osed	or	On	; 0	= (pen	or	off								

TABLE 3-15. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 2.465 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	2 ¹	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	l 2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
4	0	0	0	0	0	С	С	С	С	С	С	0	С	0	10272	0
5	C	С	С	0	0	С	C	С	C	С	С	С	С	0	8217	3
6	0	0	0	0	0	0	C	0	C	0	С	0	.0	С	6848	0
7	C	С	0	0	С	0	0	0	С	0	0	C	0	C	5869	5
8	0	0	0	0	С	C	C	С	С	C	0	C	0	C	5136	0
9	C	_ <u>C</u>	0	C	0	C	0	0	0	C	С	С	0	С	4565	3
10	0	0	С	0	С	С	C	С	C	C	С	C	0	C	4108	8
11	C	0	0	C	0	С	C	0	С	0	0	0	С	C	3735	3
12	0	0	0	0	0	C	0	C	0	C	0	0	С	C	3424	0
13	0	0	0	C	0	С	0	С	С	C	0	0	С	C	3160	8
14	0	С	0	C	0	0	0	С	0	0	С	0	С	C	2934	12
15	C	0	С	C	0	0	C	0	С	0	_C	0	C	С	2739	3
16	0	0	0	C	C	С	С	С	С	0	С	0	С	c	2568	0
17	0	0	0	0	С	0	0	С	0	С	С	0	С	С	2416	16
18	0	C	С	0	С	0	0	0	C	C	С	0	С	С	2282	12
19	0	С	С	С	0	0	0	С	С	С	C	0	С	c	2162	10
20	0	С	0	C	С	С	C	С	C	С	С	0	С	c	2054	8
21	0	0	С	С	С	0	С	0	0	0	0	С	С	С	1956	12
22	C	0	С	0	С	С	0	С	0	0	0	C	C	С	1867	14
23	0	С	С	0	0	0	0	0	С	0	0	С	С	c	1786	10
24	0	0	0	0	С	0	С	0	С	0	0	C	С	С	1712	0
25	C	0	C	0	C	0	0	С	C	0	0	С	С	c	1643	13
26	0	0	С	0	С	0	C	С	С	0	0	C	С	C	1580	8
27	С	С	С	C	0	0_	0	0	0	С	0	С	С	С	1521	21
28	C	0	С	0	0	0	C	0	0	C	0	С	С	C	1467	12
29	0	0	0	C	С	C	C	0	0	С	0	С	С	C	1416	24
30	C	C	C	0	0	C	0	<u>C</u>	0	C	0	С	C	_C	1369	18
31	C	С	0	0	C	0	C	С	0	C	О	С	С	C	1325	13
32	0	0	С	C	C	С	C	C	0	С	0	С	С	C	1284	0
33	C	С	0	0	0	C	0	0	C	C	0	С	С	_C	1245	3
34	0	0	0	C	0	0	C	0	С	С	0	С	С	C	1208	16
35	C	С	0	C	0	С	C	0	С	С	0	С	С	C	1173	33
36	C	С	0	C	0	0	0	C	С	C	0	С	С	С	1141	12
37	0	С	0	C	0	С	0	С	С	C	0	C	С	С	1110	18
38	C	С	С	0	0	0	C	С	C	C	0	С	С	С	1081	10
39	C	C	0	0	0	С	C	С	С	С	0	<u>_C</u>	С	С	1053	21
40	C	0	С	C	С	С	С	С	С	C	0	С	С	С	1027	8
41	0	С	С	0	С	0	0	0	0	0	С	С	С	С	1002	6
42	0	С	С	С	0	С	0	0	0	0	С	С	С	С	978	12
43	С	0	С	0	0	0	С	0	0	0	C	С	С	С	955	23
44	C	C	0	C	С	0	C	0	0	0	C	С	С	С	933	36
45	C	С	С	C	0	С	С	0	0	0	С	С	С	С	913	3
* C = Cl	osed	or	On	; 0	= C	pen	or	off		l			_ • •			<u> </u>
												CO	ntinu	rea		

TABLE 3-15. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 2.465 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*			.,		Bytes in	Bytes
of Sectors	2 ⁰	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 2 13	Usable Sectors	in Runt Sector
46	С	С	0	0	0	0	0	С	0	0	С	С	С	С	893	10
47	0	С	C	0	С	0	0	С	0	0	С	С	С	С	874	10
48	0	_0_	_0_	C	_0	_ <u>C</u>	0	C	_0_	0	C	_ <u>C</u> _	C	С	856	0
49	0	C	0	C	С	C	0	C	0	0	C	C	С	C	838	26
50	C	C	0	C	0	0	C	C	0	0	C	С	C	С	821	38
<u>51</u> 52	C	C	0	C	_ <u>C</u>	C	C	C	0	0	C	_ <u>C</u> _	C	C C	805 790	33 8
53	C	0	0	c	C	C	C	C	0	0	C	C	C	C	790 775	13
54	0	0	0	c	0	0	0	0	C	0	С	C	C	C	760	48
55	C	0	c	0	C	0	0	-	c	0	c	C	C	C	747	3
56	C	C	ō	0	0	C	0	0	c	0	c	c	C	C	733	40
57	0	0	Ö	0	C	C	0	0	c	o	C	C	C	C	720	48
58	0	0	C	C	C	C	0	0	C	0	C	C	C	C	708	24
59	0	Ō	Ō	c	ō	ō	c	ō	Ċ	0	C	С	c	С	696	24
60	0	Ō	C	0	C	Ō	C	ō	Ċ	o	C	С	C	c	684	48
61	С	С	С	С	С	0	С	ō	C	0	С	C	С	С	673	35
62	0	С	0	С	0	С	С	0	С	0	С	С	С	С	662	44
63	0	0	С	0	С	С	С	0	C_	0	С	C	С	С	652	12
64	0	С	С	С	С	С	С	0	С	0	С	С	С	С	642	0
65	0	0	0	С	0	0	0	С	С	0	С	C	С	С	632	8
66	0	C	0	0	C	0	0	C	C	0	С	С	С	С	622	36
67	С	С	0	C	С	0	0	С	С	0	С	С	С	С	613	17
68	0	0	С	0	0	С	0	С	С	0	С	С	С	C	604	16
69	C	0	C	C	_0_	С	0	C	<u> </u>	0	C	С	С	С	595	33
70	0	С	С	0	С	С	0	С	С	0	С	С	С	С	586	68
71	0	С	С	C	С	С	0	С	С	0	С	C	С	С	578	50
72	0	C	C	0	0	0	C	C	_ <u>C</u> _	0	С	C	С	С	570	48
73	0	С	C	С	0	0	C	С	C	0	C	С	C	С	562	62
74	C	0	C	0	C	0	C	C	C	0	C	С	C	C	555	18
75	C	0	<u>C</u>	C	<u>C</u>	0	<u></u>	<u> </u>	C	0	C	C	C	C	547	63
76	0	0	С	0	0	C	C	C	C	0	С	C	C	С	540	48
77	C	C	0	C	0	C	C	C	C	0	C	С	C	C	533	47
<u>78</u> 79	0	<u>с</u> о	0	0	C C	C	C	C	C	0	C C	C	C	C	526 520	60 8
80	C	C	C	C	C	C	C	C C	C C	0	C	C	C	C C	520 513	48
81	С	0	C	0	0	0	0	0	0	C	C	C	C	C	507	21
82	C	c	0	c	0	0	0	0	0	C	C	С	C	C	501	6
83	C	0	0	0	C	o	0	0	0	C	C	C	C	c	495	3
84	С	C	C	0	C	o	o	0	o	C	C	С	C	c	489	12
85	С	0	C	C	C	0	0	0	0	C	C	С	C	C	483	33
86	C	Ċ	0	o	o	C	ō	o	o	C	Ċ	C	c	c	477	66
87	0	0	0	С	0	C	0	0	0	c	C	c	C	c	472	24
* C = Clc	sed		·	<u> </u>]		
						P-11		<u> </u>				Cor	ntinu	ied —		

TABLE 3-15. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES
USING 2.465 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
88	0	С	С	С	0	С	0	0	0	С	С	С	С	С	466	80
89	С	С	0	0	С	С	0	0	0	C	С	С	С	С	461	59
90	0	0	0	C	С	С	0	0	0	C	С	С	C	С	456	48
91	C	0	C	C	C	С	0	0	0	C	С	С	С	С	451	47
92	0	C	0	0	0	0	С	0	0	C	C	С	C	C	446	56
93 94	O O	C C	<u>C</u>	C	0	0	C	0	0	C	C C	C C	C	C	441	75
94 95	0	0	0	0	0	0	C	0	0	C	C	C	C	C C	437 432	10 48
96	0	0	С	0	C	0	C	0	0	C	C	c	C	C	432 428	0
97	C	0	0	C	c	0	C	0	0	c	c	C	C	c	423	57
98	C	0	C	C	C	0	C	0	0	C	С	c	C	C	419	26
99	C	0	0	0	0	C	c	0	o	C	C	c	C	C	415	3
100	0	C	C	0	0	C	C	0	0	c	C	C	C	C	410	88
101	0	C	ō	c	ō	Č	c	ō	ō	c	C	c	C	Ċ	406	82
102	0	С	С	С	0	С	С	0	0	c	С	С	С	С	402	84
103	0	С	0	0	С	С	С	0	0	С	С	С	С	С	398	94
104	С	0	С	0	С	С	С	0	0	С	С	С	С	С	395	8
105	С	0	0	С	С	С	С	0	0	С	С	С	С	С	391	33
106	С	0	С	С	С	С	С	0	0	С	С	С	С	С	387	66
107	0	0	0	0	0	0	0	С	0	С	С	c	С	С	384	0
108	0	0	C	0	0	0	0	С	0	C	C	C	C	С	380	48
109	0	0	0	С	0	0	0	С	0	С	C	c	С	C -	376	104
110	С	С	0	С	0	0	0	С	0	С	С	c	С	C	373	58
111	0	C	C	С	0	0	0	C	0	C	C	С	C	С	370	18
112	0	C	0	0	С	0	0	С	0	С	С	С	С	С	366	96
113	C	0	C	0	C	0	0	C	0	C	C	C	C	C	363	69
114	0	0	0	C	C	0	0	C	0	C	C	C	<u>C</u>	C	360	48
116	C 0	C C	C	C	C	0	0	C C	0	C	C C	C	C C	C	357 354	33 24
117	C	0	0	0	0	C	0	C	0	c	C	c	C	c	351	21
118	0	0	c	0	0	C	0	C	0	c	c	c	C	c	348	24
119	C	C	C	o	o	C	o	C	Ö	c	C	c	c	c	345	33
120	O	С	0	c	0	С	0	С	0	С	С	С	С	С	342	48
121	C	0	C	C	0	С	0	C	0	c	C	c	C	c	339	69
122	0	0	0	0	C	С	0	С	0	c	C	С	C	c	336	96
123	0	С	0	0	С	С	0	С	0	С	С	С	С	С	334	6
124	С	0	С	0	С	С	0	С	0	С	С	С	С	С	331	44
125	0	0	0	С	С	С	0	С	0	С	С	С	С	C	328	88
126	0	С	0	С	С	С	0	С	0	С	С	С	С	С	326	12
127	C	0	C	С	С	С	0	С	0	С	C	С	С	С	323	67
128	С	С	С	С	С	С	0	С	0	С	С	C	С	С	321	0
* C = Clc	sed	l or	On	; 0	= 0	pen	or	off		L				1		

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TABLE 3-16. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 2.465 MHz (BYTE) CLOCK AND ROUND-UP METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	213	212	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	0	0	С	С	С	С	С	С	0	С	0	10272	10272
5	0	С	С	0	0	С	C	С	С	С	С	С	С	0	8218	8216
6	0	0	0	0	0	0	С	0	С	0	С	0	0	С	6848	6848
7	0	С	0	0	С	0	0	0	С	0	0	С	0	С	5870	5868
8	0	0	0	0	С	С	C	С	С	C	0	С	0	С	5136	5136
99	0	C	0	C	0	С	0	0	0	С	C	С	0	С	4566	4560
10	C	С	0	0	С	С	C	С	C	C	С	С	0	C	4109	4107
11	0	0	0	C	0	С	C	0	С	0	0	0	C	C	3736	3728
12	0	0	0	0	0	C	0	C	0_	C	0	0	С	C	3424	3424
13	C	С	С	0	0	С	0	С	С	C	0	0	С	c	3161	3156
14	C	0	0	C	0	0	0	С	0	0	С	0	С	C	2935	2933
15	0	0	С	C	0	0	C	0	С	0	С	0	С	С	2740	2728
16	0	0	0	C	C	С	C	С	С	0	С	0	С	C	2568	2568
17	C	С	С	C	0	0	0	С	0	С	С	0	С	C	2417	2416
18	C	_ 0	_C	0	С	0	0	0	С	С	С	0	С	С	2283	2277
19	C	0	С	С	0	0	0	С	С	С	С	0	С	С	2163	2154
20	C	0	0	c	С	С	C	С	С	С	С	0	С	С	2055	2043
21	C	C	0	C	C	0	c	0	0	0	0	С	С	С	1957	1948
22	0	0	С	0	С	С	0	С	0	0	0	С	С	С	1868	1860
23	C	0	С	0	0	0	0	0	С	0	0	С	С	С	1787	1774
24	0	0	0	0	С	0	С	0	С	0	0	С	С	С	1712	1712
25	0	0	С	0	С	0	0	С	С	0	0	С	С	С	1644	1632
26	С	С	0	0	С	0	C	С	С	0	0	С	С	c	1581	1563
27	0	_ C	C	С	0	0	0	0	0	С	0	С	С	С	1522	1516
28	0	0	С	0	0	0	C	0	0	С	0	С	C	С	1468	1452
29	C	С	С	0	C.	С	C	0	0	C	0	С	С	C	1417	1412
30	0	С	С	0	0	С	0	С	0	С	0	С	С	С	1370	1358
31	0	С	0	0	С	0	С	С	0	С	0	С	С	С	1326	1308
32	0	0	С	C	С	С	C	С	0	С	0	С	С	c	1284	1284
33	0	С	0	0	0	С	0	0	С	С	0	С	С	c	1246	1216
34	С	С	С	0	0	0	С	0	С	С	0	С	С	С	1209	1191
35	0	С	0	C	0	С	C	0	С	С	0	С	С	C	1174	1172
36	0	С	0	С	0	0	0	С	С	С	0	С	С	С	1142	1118
37	С	0	0	С	0	С	0	С	С	С	0	С	С	С	1111	1092
38	0	С	С	0	0	0	С	С	C	С	0	С	С	C	1082	1054
39	0	С	0	0	0	С	С	С	С	С	0	С	С	С	1054	1036
40	0	0	C	С	С	C	С	С	С	С	0	С	С	С	1028	996
41	C	0	С	0	С	0	0	0	0	0	C	С	С	С	1003	968
42	С	0	С	С	0	С	0	0	0	0	С	С	С	С	979	949
43	0	0	С	0	0	0	С	0	0	0	С	С	С	С	956	936
44	0	С	0	С	С	0	С	0	0	0	С	С	С	С	934	926
45	0	С	С	c	0	С	С	0	0	0	С	С	С	С	914	872
* C = Clo	osed	or	On	; 0	= 0	pen	or	off		L						
												Cor	ıtinu	ed —		

TABLE 3-16. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 2.465 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	2 ¹	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2	212	2 ¹³	Early Sectors	in Last Sector
46	0	С	0	0	0	0	0	С	0	0	С	С	С	С	894	858
47	C	0	С	0	С	0	0	С	0	0	С	С	С	C	875	838
48	0	0	0	C	0	<u>C</u>	0	C	0	0	_ <u>C</u>	<u></u>	С	С	856	856
49	C	0	0	C	С	С	0	С	0	0	С	С	С	C	839	816
50	0	С	0	C	0	0	C	С	0	0	С	С	С	С	822	810
51	0	<u>C</u>	0	C	C	0	C	<u>C</u>	0	0	С	C_	С	С	806	788
52	C	0	0	C	0	С	C	С	0	0	С	С	С	С	791	747
53	0	0	0	C	С	С	С	С	0	0	С	С	С	С	776	736
54	C	C	<u>C</u>	0	0	0	0	0	<u> </u>	0	C	С	С	С	761	755
55	0	0	С	0	С	0	0	0	С	0	С	С	С	C	748	696
56	0	С	0	0	0	С	0	0	С	0	С	С	С	C	734	718
57	C	С	C	C	0	С	0	0	С	0	С	С	С	C	721	712
58	C	С	0	C	С	C	0	0	С	0	С	С	С	c	709	675
59	C	С	С	0	0	0	С	0	С	0	С	С	С	c	697	662
60	C	C	0	0	C	0	С	0	C	0	С	С	С	С	685	673
61	0	С	С	C	С	0	C	0	С	0	С	С	С	c	674	648
62	C	0	0	C	0	C	C	0	С	0	С	С	С	c l	663	645
63	C	С	0	0	C	С	C	0	С	0	С	С	С	С	653	602
64	0	С	С	С	С	С	С	0	С	0	С	С	С	С	642	642
65	C	С	С	0	0	0	0	С	С	0	С	C	С	c	633	576
66	C	0	0	0	С	0	0	С	С	0	С	С	С	С	623	593
67	0	С	0	С	С	0	0	С	С	0	С	С	С	С	614	564
68	C	С	0	0	0	С	0	С	С	0	С	С	С	c	605	553
69	0	0	С	C	0	С	0	С	С	0	С	С	С	c	596	560
70	С	0	С	0	С	С	0	С	С	0	С	С	C	С	587	585
71	C	0	С	c	С	С	0	С	С	0	С	С	С	c	579	558
72	C	0	С	0	0	0	c	С	С	0	С	С	С	С	571	547
73	С	0	С	С	0	0	С	С	С	0	С	С	С	С	563	552
74	0	0	С	0	С	0	c	С	С	0	С	С	С	c	556	500
75	0	0	С	C	С	0	С	С	С	0	С	С	С	С	548	536
76	С	С	0	0	0	С	С	C	C	0	С	С	С	С	541	513
77	0	С	0	С	0	C	С	С	C	0	С	С	C	c	534	504
78	С	0	0	0	С	С	С	С	С	0	С	С	С	С	527	509
79	С	C	С	0	С	С	С	С	С	0	С	С	С	С	521	450
80	0	С	С	c	C	С	С	С	С	0	С	С	С	С	514	482
81	0	0	С	0	0	0	0	0	0	С	С	C.	С	С	508	448
82	0	С	0	С	0	0	0	0	0	С	С	С	С	С	502	426
83	0	0	0	0	C	0	0	0	O	С	С	С	С	c	496	416
84	0	C	C	0	C	0	0	0	0	c	C	С	С	С	490	418
85	0	0	C	C	C	0	0	0	ō	c	C	C	С	c	484	432
86	0	C	ō	o	o	Ċ	o	0	Ö	c	Ċ	C	C	c	478	458
87	C	C	C	o	o	C	0	o	0	c	c	C	C	c	473	410
	<u> </u>			<u> </u>			L									
* C = Cl	osed	or	On	; 0	= C	pen	or	Off				Cor	ntinu	ıed		····

TABLE 3-16. SECTOR SELECT SWITCH SETTINGS -- 851 MB DRIVES USING 2.465 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tche	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 ¹³	Early Sectors	in Last Sector
88	С	0	С	С	0	С	0	0	0	С	С	С	С	С	467	459
89	0	С	0	0	С	С	0	0	0	С	С	С	С	c	462	432
90	С	C	С	0	C	С	0	0	0	C	C	С	С	C	457	415
91	0	0	С	C	С	С	0	0	0	С	С	С	С	c	452	408
92	С	0	0	0	0	0	C	0	0	С	С	С	С	C	447	411
93	0	C	С	0	0	0	C	O	0	C	С	С	С	С	442	424
94	0	С	0	C	0	0	C	0	0	C	С	C	С	c	438	354
95	С	С	С	С	0	0	C	0	0	С	С	С	С	C	433	386
96	0	0	С	0	С	0	C	0	0	C	С	C	С	С	428	428
97	0	0	0	C	С	0	С	0	0	С	С	С	С	C	424	384
98	0	0	С	C	С	0	С	0	0	С	С	С	С	C	420	348
99	0	0	0	0	0	C	С	0	0	C	С	С	С	_C	416	320
100	С	0	С	0	0	С	C	0	0	С	С	С	С	C	411	399
101	С	0	0	С	0	С	C	0	0	С	С	С	С	C	407	388
102	C	0	C	C	0	С	С	0	0	С	С	С	С	С	403	385
103	С	0	0	0	С	С	С	0	0	С	С	С	С	С	399	390
104	0	0	C	0	С	C	С	0	0	С	С	С	С	С	396	300
105	0	0	0	C	<u></u>	С	C	0	0	C	С	С	С	С	392	320
106	0	0	C	C	С	C	С	0	0	C	C	С	С	C	388	348
107	0	О	0	0	0	0	0	С	0	C	С	C	C	С	384	384
108	С	C	0	0	0	0	0	C	0	С	С	С	С	C	381	321
109	С	С	С	0	0	0	0	С	0	C	С	С	С	С	377	372
110	0	С	0	C	0	0	0	С	0	С	С	С	С	C	374	322
111	C	0	<u>_C</u>	C	0	0	0	С	0	C	С	С	С	С	371	278
112	С	0	0	0	С	0	0	С	0	С	С	С	С	c	367	351
113	0	0	С	0	C	0	0	С	0	С	C	С	С	C	364	320
114	С	С	C	0	_ <u>C</u>	0	0	С	0	C	С	С	С	С	361	295
115	0	С	0	C	С	0	0	С	0	C	C	C	С	С	358	276
116	С	О	C	C	С	0	0	С	0	C	С	С	С	С	355	263
117	0	0	0	0	0	С	0	С	0	C	С	С	С	C	352	256
118	С	С	0	0	0	С	0	С	0	С	С	С	C.	С	349	255
119	0	С	С	0	0	С	0	С	0	С	С	С	С	С	346	260
120	C	0	0	C	0	C	0	C	0	C	c c	C	C	C	343	271
121	0	0	С	C	0	С	0	С	0	С	С	С	С		340	288
122	C	С	С	C	0	С	0	С	0	C	С	С	С	С	337	311
123	C	0	0	0	C	С	0	C	0	C	C	С	С	С	335	218
124	0	0	С	0	С	С	0	С	0	C	С	С	С	С	332	252
125	С	С	С	0	С	С	0	C	0	C	C	С	C	С	329	292
126	С	0	0	С	C	С	0	C	0	C	С	С	С	C	327	213
127	0	0	С	C	С	C	0	С	0	C	С	С	С	С	324	264
128	C	С	С	C	С	С	0	С	0	C	С	С	С	С	321	321
* C = Clo	osed	or	On	; 0	= C	pen	or	off		L						L

TABLE 3-17. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-DOWN METHOD

Number					1	Sec	tor	Swi	tch	es*			***************************************		Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 ¹³	Usable Sectors	in Runt Sector
4	0	0	0	0	С	С	0	0	0	С	0	0	0	С	11448	0
5	C	С	С	0	0	C	0	0	0	0	0	С	0	С	9157.50	4.50
6	0	0	0	0	0	<u>C</u>	0	0	0	0	<u>C</u>	C	0	С	7632	0
7	C	С	С	0	С	С	C	С	0	C	С	С	0	C	6541.50	1.50
8	0	0	0	C	С	0	0	0	С	0	0	0	С	С	5724	0
9	0		0	10	0	0	C	С	0	C	0	0	C	С	5088	0
10	0	0	С	0	С	0	0	0	0	0	С	0	С	С	4578	12
11	C	0	0	C	0	С	0	0	С	0	C	0	С	C	4162.50	4.50
12	0	0	0	10	C	0	0	0	0	C	C	0	С	C	3816	0
13	0	0	C	0	C	0	C	С	0	C	C	0	С	C	3522	6
14	0	0	С	C	C	С	С	0	C	C	C	0	С	С	3270	12
15	C	0	<u>C</u>	C	0	0	0	0	0	0	_0_	С	С	С	3052.50	4.50
16	0	0	С	C	0	0	0	С	0	0	0	С	С	C	2862	0
17	C	0	С	C	С	С	С	С	0	0	0	С	С	c	2692.50	19.50
18	0	_ 0	0	10	0	C	C	0	C	0	0	С	С	C	2544	0
19	0	С	0	C	С	С	0	C	С	0	О	С	С	C	2409	21
20	0	С	0	C	0	0	0	0	0	C	0	С	С	C	2289	12
21	C	С	0	0	C	0	C	0	0	C	_0_	С	С	С	2179.50	22.50
22	C	0	C	0	С	0	0	C	0	C	0	С	С	C	2080.50	21
23	C	0	0	0	С	0	С	С	0	С	0	С	С	С	1990.50	10.50
24	0	0	0	C	0	0	0	0	C	C	0	С	С	С	1908	0
25	C	C	0	C	C	C	0	0	C	C	0	C	C	C	1831.50	4.50
26	0	С	0	C	0	С	С	0	С	С	0	С	С	С	1761	6
27	0	<u>C</u>	<u> </u>	<u> </u>	<u>C</u>	0	0	<u>C</u>	<u> </u>	C	0	C.	C	C	1695	27
28	0	С	C	C	C	C	0	C	C	C	0	С	C	C	1635	12
29	0	0	C	0	0	С	C	C	C	С	0	С	C	C	1578	30
30	C	<u>C</u>	<u> </u>	<u> 0</u>	0	0_	0	0	0	0	<u>C</u>	C	C	C	1525.50	27
31	0	0	0	C	0	С	0	0	0	0	С	С	C	C	1476	36
32	0	С	С	0	0	0	C	0	0	0	C	С	C	C	1431	0
33	C	<u>C</u>	0	9	0	<u> </u>	C	<u> </u>	<u> </u>	9	<u>C</u>	C	C	C	1387.50	4.50
34	C	C	С	C	C	C	C	0	0	0	C	C	С	C	1345.50	45
35 36	0	0	0	C	С	0	0	C	0	0	C	С	C	C	1308	12
36	0	0	0_	0	<u>C</u>	<u>C</u>	0	<u>C</u>	0	0	<u>C</u>	C	C	C	1272	0 4.50
37 30	C	С	C	0	0	0	C	C	0	0	C	C	C	C	1237.50	
38	C	0	C	C	C	0	C	C	0	0	C	C	C	C	1204.50 1173	21
39 40	C	<u>C</u>	C	0	<u>C</u>	<u> </u>	0	0	C	0	C	C	С	C C	11/3	45 12
40 41	0	0	0	C	C	0	0	0	C	0	C	C	C	C	1114.50	36
42	0	C	0	C	0	C	0	0	C	1	C	С	C	c	1089	54
43	C		0	C	_ C	C	0	0	c	0	_ C	C	C	c	1063.50	61.50
43 44	C	C	0	C	0	0	C	0	C	0	C	C	C	c	1039.50	54
45	0	C	_	C	C		C		C	1	C	c	C	c	1039.50	27
45 		<u> </u>	0		<u> </u>	0	L	0	<u> </u>	0	<u> </u>	Ü	U U	١	TOT /	41
* C = Cl	osed	l or	On	; 0	= (pen	or	off								
												COI	ntinu	eu -		

TABLE 3-17. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
46	С	0	0	С	0	С	С	0	С	0	С	С	С	C	994.50	45
47	C	С	С	0	C	С	С	0	С	0	С	C	С	С	973.50	37.50
48	0	0	C	0	0	0	0	C	С	0	С	С	С	С	954	0
49	C	0	0	0	С	0	0	C	С	0	С	С	С	С	934.50	1.50
50	0	С	С	С	С	О	0	С	С	0	С	С	С	C	915	42
51	0	С	0	C	0	С	0	<u> </u>	С	0	С	С	С	С	897	45
52	C	0	С	0	С	С	0	С	С	0	С	С	С	С	880.50	6
53	0	0	0	0	0	0	C	С	С	0	С	С	С	С	864	0
54	C	C	_0_	C	0	0	C	<u>C</u>	C	0	C	С	<u>C</u>	C	847.50	27
55	C	0	C	0	С	0	C	С	С	0	С	С	C	C	832.50	4.50
56	C	С	C	C	C	0	C	C	C	0	C	С	C	C	817.50	12
57	C	_0	0	C	0	<u> </u>	C	<u> </u>	<u> </u>	0	_ <u>C</u>	C	<u> </u>	C	802.50	49.50
58	0	C	0	0	C	С	C	C	C	0	C	C	C	C	789	30
59 60	C	C	0	C	C	С	C	C	C	0	C	C	С	C	775.50	37.50
61	0	0	C	C	0	0	0	0	0	C	C	C	C C	C	762 750	72 42
62	0	0	C	0	C	0	0	0	0	C	C	c	C	c	738	36
63	0	0	C	C	C	0	0	0	0	c	c	c	C	c	736 726	54
64	C	c	0	0	0	c	0	-	-	C	C	ट	C	c	715.50	0
65	c	C	0	C	0	C	0	0	0	C	C	c	C	c	703.50	64.50
66	0	C	0	0	C	C	0	0	0	c	С	c	c	c	693	54
67	c	0	0	c	c	C	0	- 0	0	c	<u>C</u>	c	C	c	682.50	64.50
68	0	0	0	0	o	o	c	ō	0	C	C	c	C	c	672	96
69	0	C	c	١٥	o	o	c	o	0	C	C	c	C	c	663	45
70	0	0	c	c	0	0	c	0	0	c	c	c	C	c	654	12
71	C	C	0	o	C	Ō	c	ō	Ō	c	C	c	Ċ	c	643.50	103.50
72	0	0	0	c	С	0	С	0	0	C	C	c	C	С	636	0
73	0	С	С	С	С	0	С	0	0	С	С	С	С	С	627	21
74	0	0	С	0	0	С	С	0	0	С	С	c	С	c	618	60
75	C	0	0	C	0	С	С	0	0	С	С	C	С	С	610.50	4.50
76	С	С	С	С	0	С	С	0	0	С	С	С	С	С	601.50	78
77	0	0	С	0	C	С	С	0	0	С	С	c	С	c	594	54
78	С	0	0	С	С	С	С	0	0	C	C	С	С	С	586.50	45
79	0	С	С	C	С	С	С	0	0	С	С	С	С	С	579	51
80	С	С	0	0	0	0	0	С	0	С	С	C	С	C	571.50	72
81	0	0	0	C	0	0	0	<u>C</u>	0	С	С	c	С	С	564	108
82	0	0	С	C	0	0	0	С	0	C	С	C	С	C	558	36
83	С	0	0	0	С	0	0	С	0	С	С	С	С	C	550.50	100.50
84	С	0	С	0	С	0	0	С	0	C	C	C	С	C	544.50	54
85	С	0	0	C	С	0	0	С	0	С	С	C	С	C	538.50	19.50
86	0	С	С	C	С	0	0	С	0	C	С	C	С	C	531	126
87	0	С	0	0	0	С	0	С	0	С	С	C	С	c	525	117
* C = Clo	osed	or	On	; 0	= C	pen	or	off						ــــــــــــــــــــــــــــــــــــــ		
												Con	tinue	ea		

TABLE 3-17. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	27	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
88	0	С	С	0	0	С	0	С	0	С	С	С	С	С	519	120
89	С	0	0	C	0	С	0	С	0	C	C	c	С	С	514.50	1.50
90	C	0	<u> </u>	C	0	C	0	C	0	C	C	С	С	C	508.50	27
91	С	0	0	0	С	С	0	С	0	С	С	C	С	С	502.50	64.50
92	С	0	С	0	С	С	0	С	0	C	С	C	С	С	496.50	114
93	0	0	0	C	С	C	0	C	0	C	С	C	С	С	492	36
94	0	0	C	C	C	C	0	C	0	C	C	c	C	С	486	108
95	C	C	C	C	С	C	0	C	0	C	C	С	С	С	481.50	49.50
96	9	C	<u> </u>	0	0	0	C	<u>C</u>	0	<u> </u>	<u></u>	C	<u>C</u>	<u>C</u>	477	0
97	0	C	C	0	0	0	C	C	0	C	C	C	C	C	471	105
98	C	0	0	C	0	0	C	C	0	C	C	C	С	C	466.50	75 5.4
99	0	0	<u> </u>	C	0	<u> </u>	C	C	0	C	C	<u>c</u>	<u>C</u>	C	462	54
100 101	C	C	C 0	C	0	0	C	C	0	C	C C	C	C	C	457.50	42
	0	_	-	0		0		C	0	C		٠ ا	C	С	453	39
102 103	CO	0	 O	C	C	0	C	C	0	C	C	C	C	C	448.50	45
103	C	C	0	C	С	0	C	C	0	C	C	c	C	c	444 439.50	60
104			-	1		_	1		_	i i		- i				84
	0	C	<u>C</u>	C	<u>C</u>	0_	C	<u>C</u>	0	C	C	C	<u>C</u>	C	435	117
106 107	0	0 C	0	0	0	C	C	C	0	C	C	C	С	C C	432 427.50	0 49.50
107	0	C	C	0	0	C	C	C	0	C	C	c	C ·	c	427.50	108
109	0	0	0	C	0		C	C	0	C	C	$\frac{c}{c}$	C	c	423	12
110	C	C	0	C	0	C	C	C	0	C	C	c	C	c	415.50	87
111	C	0	C	C	0	C	C	C	0	c	C	c	C	c	412.50	4.50
112	0	0	0	5	c	c	c	C	0	c	c	č	- C	c	408	96
113	0	C	o	0	C	C	C	C	Ö	c	c	c	C	c	405	27
114	C	0	Ċ	0	C	C	c	C	o	c	C	c	C	c	400.50	135
115	C	C	C	0	C	C	C	C	0	C	C	c	C	C	397.50	79.50
116	C	0	0	C	C	C	l c	C	o	C	C	c	C	c	394.50	30
117	0	0	C	C	C	C	c	C	o	C	C	c	C	c	390	162
118	0	c	C	c	c	C	c	C	0	c	C	č	C	c	387	126
119	0	0	Ö	0	0	o	0	o	Č	c	C	c	C	c	384	96
120	0	C	0	0	0	0	0	0	C	C	c	c	Ċ	c	381	72
121	0	0	C	o	0	0	0	0	C	c	C	c	C	c	378	54
122	0	C	c	o	o	ō	o	ō	C	c	C	c	Ċ	c	375	42
123	0	0	0	С	0	0	0	o	C	C	C	c	C	C	372	36
124	0	C	0	c	0	0	ō	0	c	c	C	c	C	c	369	36
125	0	0	C	C	0	O	0	O	C	C	C	C	C	C	366	42
126	0	С	С	С	0	0	0	0	С	С	С	С	С	С	363	54
127	0	0	0	0	С	0	0	0	С	С	С	C	С	С	360	72
128	0	С	0	0	С	0	0	0	С	C	С	c	С	C	357	96
129	0	0	С	0	С	0	0	0	С	С	С	c	С	C	354	126
* C = Clc	sed	or	On	; 0	= c	pen	or	off		L		1	· · · · · ·			
				——								Con	tinu	ed —		

TABLE 3-17. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	twa	tch	es*					Bytes in	Bytes
of Sectors	2 ⁰	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	L ₂ 12	2 ¹³	Usable Sectors	in Runt Sector
130	0	С	С	0	С	0	0	0	С	С	С	С	С	С	351	162
131	С	С	С	0	С	0	0	0	С	C	С	С	С	C	349.50	7.50
132	C	_0	0	C	C	0	0	0	<u>C</u>	C	С	С	С	С	346.50	54
133	C	С	0	C	С	0	0	0	С	C	С	С	С	С	343.50	106.50
134	C	0	С	C	С	0	0	0	С	C	С	С	С	C	340.50	165
135	0	C	C	C	<u>C</u>	0	0	0	C	C	С	C	С	С	339	27
136	0	0	0	0	0	С	0	0	С	C	С	С	С	С	336	96
137	0	С	0	0	0	С	0	0	С	C	С	С	С	С	333	171
138	С	<u>C</u>	0	10	0	C	0	0	C	C	C	С	С	С	331.50	45
139	C	0	С	0	0	С	0	0	С	C	С	С	С	С	328.50	130.50
140	0	С	С	0	0	С	0	0	С	C	С	С	С	C	327	12
141	0	0	0_	c	0	С	0	0	C	C	С	С	С	С	324	108
142	0	С	0	C	0	С	0	0	С	C	С	С	С	С	321	210
143	C	С	0	C	0	С	0	0	С	C	С	С	С	С	319.50	103.50
144	0	0	<u>C</u>	C	0	C	0	_0	_ <u></u>	L C	C	С	С	С	318	0
145	0	C	С	C	0	С	0	0	С	C	C	С	С	С	315	117
146	C	С	С	C	0	С	0	0	С	C	С	С	С	С	313.50	21
147	C	_ 0	0	0	_ <u>C</u>	C	0	0	C	C	_ <u>C</u>	С	С	С	310.50	148.50
148	0	С	0	0	С	С	0	0	С	C	С	С	С	С	309	60
149	0	0	С	0	С	С	0	0	С	C	С	С	С	С	306	198
150	C	0	_ <u>C</u>	0	C	С	0	0	C	C	C	С	С	С	304.50	117
151	0	С	С	0	С	С	0	0	C	C	С	С	С	С	303	39
152	0	0	0	C	С	С	0	0	С	C	С	С	С	С	300	192
153	C	0	0	C	C	С	0	0	C	C	С	С	С	С	298.50	121.50
154	0	С	0	C	С	С	0	0	С	C	С	С	С	С	297	54
155	0	0	С	C	C	С	0	0	C	C	С	С	С	С	294	222
156	C	0	_ <u>C</u>	C	С	C	0	0	_ <u>C</u>	C	С	С	С	С	292.50	162
157	0	С	С	C	С	С	0	0	С	C	С	С	С	С	291	105
158	C	C	С	C	С	С	0	0	С	C	C	С	С	С	289.50	51
159	0	<u> </u>	<u> </u>	10	_0	0	C	0	<u> </u>	C	<u>C</u>	C	С	C	288	0
160	0	C	0	0	0	0	C	0	C	C	C	С	C	C	285	192
161	C	C	0	0	0	0	C	0	C	C	C	C	С	C	283.50	148.50
162	10	_0	_ <u>c</u> _	10		0	드	_0	<u> </u>	C	_ <u>c</u> _	C	<u>c</u>	_ <u>C</u>	282	108
163	C	0		0	0	0	C	0	C	C	C	С	C	C	280.50	70.50
164	0	C		0	0	0	C	0	C	C	C	С	С	C	279	36
165	C	<u>C</u>		10	0	0	C _	0	_ <u>C</u> _	C	C	C	С	C	277.50	4.50
166	C	0		C	0	0	C	0	C	C	C	C	C	C	274.50	225
167	0	C		C	0	0	C	0	C	C	C	С	С	C	273	201
168	C	С	0	C	0	0	C	0	С	C	С	С	С	С	271.50	180
* C = Clo	osed	l o	r On	; 0	= (pen	or	off	:	L						<u> </u>

TABLE 3-18. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-UP METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	21.	212	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	0	С	С	0	0	0	С	0	0	0	С	11448	11448
5	0	С	С	0	0	С	0	0	0	0	0	С	0	С	9159	9156
6	0	0	0	0	0	С	0	0	0	0	С	С	0	С	7632	7632
7	0	С	С	0	С	C	C	С	0	C	С	С	0	C	6543	6534
8	0	0	0	C	С	0	0	0	С	0	0	0	С	c	5724	5724
9	0	0	0	0	0	0	С	С	0	С	0	0	С	С	5088	5088
10	С	С	0	0	С	0	0	0	0	0	С	0	С	С	4579.50	4576.50
11	0	0	0	C	0	С	0	0	С	0	С	0	С	c	4164	4152
12	0	0	0	0	С	0	0	0	0	С	С	0	С	С	3816	3816
13	С	С	0	0	С	0	С	С	0	C	С	0	С	С	3523.50	3510
14	С	С	0	C	C	C	C	0	С	C	С	0	С	c	3271.50	3262.50
15	0	0	С	C	0	0	0	0	0	0	0	С	С	С	3054	3036
16	0	0	С	C	0	0	0	С	0	0	0	С	С	c	2862	2862
17	0	0	С	C	С	С	С	С	0	0	0	С	С	C	2694	2688
18	0	0	0	0	0	C	C	0	C	0	0	С	C	С	2544	2544
19	С	0	0	C	С	С	0	С	С	0	0	С	С	C	2410.50	2403
20	С	0	0	C	0	0	0	0	0	С	0	C	С	c	2290.50	2272.50
21	0	С	0	0	С	0	С	0	0	С	0	С	С	С	2181	2172
22	0	0	C	0	С	0	0	С	0	С	0	С	С	C	2082	2070
23	0	0	0	0	С	0	C	С	0	С	0	С	С	c	1992	1968
24	0	0	0	C	0	0	0	0	С	С	0	С	С	С	1908	1908
25	0	С	0	С	С	С	0	0	С	С	0	С	С	С	1833	1800
26	С	0	0	C	0	С	С	0	С	С	0	С	С	c	1762.50	1729.50
27	C	0	С	0	C	0	0	С	С	С	0	С	С	C	1696.50	1683
28	С	0	C	C	C	C	0	C	C	С	0	С	С	С	1636.50	1606.50
29	С	С	0	0	0	С	C	С	С	C	0	С	С	C	1579.50	1566
30	0	C	С	0	0	0	0	0	0	0	C	С	С	С	1527	1509
31	С	С	С	0	0	С	0	0	0	0	С	С	С	C	1477.50	1467
32	0	С	С	0	0	0	C	0	0	0	С	С	С	C	1431	1431
33	0	C	0	0	0	С	C	0	0	0	С	С	С	С	1389	1344
34	0	С	С	C	С	С	С	0	0	0	С	С	С	C	1347	1341
35	C	С	С	0	С	0	0	С	0	0	С	С	С	C	1309.50	1269
36	0	0	0	0	С	С	0	С	0	0	С	С	С	С	1272	1272
37	0	С	С	0	0	0	С	С	0	0	С	С	С	C	1239	1188
38	0	0	С	С	С	0	С	С	0	0	С	С	С	C	1206	1170
39	C	0	0	0	C	С	C	C	0	0	C	С	С	С	1174.50	1161
40	0	0	С	0	0	0	0	0	C	0	С	С	С	С	1146	1098
41	С	С	С	0	С	0	0	0	С	0	С	С	С	С	1117.50	1092
42	С	0	0	C	0	<u></u>	0	0	C	0	C	С	С	С	1090.50	1081.50
43	0	С	0	C	C	С	0	0	С	0	C	С	С	С	1065	1062
44	0	С	0	C	0	0	С	0	С	0	С	С	С	С	1041	1029
45	С	0	0	C	С	0	С	0	С	0	С	С	С	С	1018.50	978
* C = Clo	osed	l or	On	; 0	= C	pen	or	off	•	.			L			
												Cor	ntinu	ied —		

3-86 83325710 G

TABLE 3-18. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-UP METHOD (Contd)

of Sectors	20					566	LOI	2M1	tche	25 *					Bytes in	Bytes
46	2	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
	0	0	0	С	0	С	С	0	С	0	С	С	С	С	996	972
47	0	С	С	0	С	С	C	0	С	0	С	С	С	C	975	942
48	0	0	C	0	0	0	0	C	C	0	С	С	С	С	954	954
49	0	0	0	0	C	0	0	C	C	0	С	С	С	С	936	864
50	С	0	С	C	С	0	0	С	С	0	С	C	С	c	916.50	883.50
51	C	0	0	C	0	С	0	С	C	0	_ <u>C</u>	С	С	С	898.50	867
52	0	0	С	0	С	С	0	С	С	0	С	С	С	С	882	810
53	0	0	0	0	0	0	С	С	С	0	С	С	С	С	864	864
54	0	С	0	C	0	0	C	С	C	0	С	С	С	С	849	795
55	0	0	С	0	C	0	C	С	С	0	C	С	С	C	834	756
56	0	С	С	C	С	0	С	С	С	0	С	C	С	С	819	747
57	0	0	0	C	0	С	С	С	С	0	C	С	С	С	804	768
58	С	0	0	0	С	С	С	С	С	0	С	С	С	С	790.50	733.50
59	0	С	0	C	С	С	С	С	С	0	С	С	С	С	777	726
60	С	С	0	0	0	0	0	0	0	С	С	С	С	С	763.50	745.50
61	С	С	0	С	0	0	0	0	0	С	С	С	С	С	751.50	702
62	С	С	0	0	С	0	0	0	0	c	С	С	С	c	739.50	682.50
63	С	С	0	C	С	0	0	0	0	С	С	С	С	С	727.50	687
64	С	С	0	0	0	С	0	0	0	С	С	С	С	С	715.50	715.50
65	0	С	0	c	0	С	0	0	0	c	C	С	С	С	705	672
66	С	0	0	0	С	С	0	0	0	С	С	С	С	С	694.50	649.50
67	0	0	0	С	С	С	0	0	0	С	С	С	U	С	684	648
68	lc	С	С	c	С	С	0	0	0	c	С	С	С	С	673.50	667.50
69	С	0	С	0	0	0	c	0	0	С	С	С	С	С	664.50	606
70	С	С	0	С	0	0	С	0	0	С	С	C	С	С	655.50	562.50
71	0	С	0	0	С	0	С	0	0	С	С	С	С	С	645	642
72	0	0	0	C	С	0	С	0	0	С	С	С	С	С	636	636
73	C	0	C	C	C	ō	c	ō	0	c	C	С	С	С	628.50	540
74	c	c	0	١٥	o	Ċ	c	ō	0	c	c	C	C	С	619.50	568.50
75	0	0	0	c	0	C	С	O	0	С	C	С	С	С	612	504
76	0	C	C	C	ō	C	c	ō	0	C	C	С	C	C	603	567
77	c	C	0	١٥	C	C	c	Ō	Ö	C	C	С	С	С	595.50	534
78	0	0	Ō	C	C	C	c	0	Ō	C	C	C	C	С	588	516
79	C	0	C	C	c	Ċ	C	0	ō	C	c	C	С	C	580.50	513
80	o	C	ō	0	o	ō	0	Ċ	ō	c	c	C	C	C	573	525
81	c	C	C	0	o	ō	o	C	ō	c	C	C	C	С	565.50	552
82	c	C	0	c	0	ō	0	C	0	c	c	C	C	С	559.50	472.50
83	0	0	ō	0	C	ō	0	c	Ö	c	C	C	c	C	552	528
84	o	0	C	0	C	ō	0	C	o	C	C	C	C	C	546	474
85	ō	0	0	c	C	0	0	C	0	c	C	C	C	C	540	432
86	c	0	Ċ	C	C	ō	0	C	Ö	c	C	C	C	c	532.50	529.50
87	C	0	o	0	0	c	0	C	0	C	C	C	c	C	526.50	513
				<u> </u>												L
* C = Clo	osed	or	On	; 0	= 0	pen	or	Off				י רטי	ntinu	ied		

TABLE 3-18. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number				······		Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	211	212	2 ¹³	Early Sectors	in Last Sector
88	С	0	С	0	0	С	0	С	0	С	С	С	С	С	520.50	508.50
89	0	0	0	C	0	С	0	С	0	C	С	С	С	C	516	384
90	0	0	_C	C	0	_C	0	C	0	С	С	С	С	C	510	402
91	0	0	0	0	С	С	0	С	0	C	С	С	С	C	504	432
92	0	0	С	0	С	С	0	С	0	C	С	С	С	С	498	474
93	C	С	C	0	С		0	С	0	С	C	C	С	С	493.50	390
94	С	С	0	C	С	С	0	С	0	C	С	C	С	C	487.50	454.50
95	0	С	С	C	С	С	0	С	0	С	С	С	С	C [483	390
96	0	С	0	0	0	0	С	C	0	С	С	С	С	С	477	477
97	С	.0	С	0	0	0	C	С	0	C	С	С	С	C	472.50	432
98	0	0	0	C	0	0	C	С	0	С	С	С	С	C	468	396
99	C	С	0	C	0	0	С	С	0	C	C	C	С	С	463.50	369
100	0	C	С	C	0	0	C	С	0	С	С	С	С	C	459	351
101	С	0	0	0	С	0	С	С	0	C	С	С	С	C	454.50	342
102	0	0	C	0	C	0	С	C	0	C	С	С	С	C	450	342
103	С	С	С	0	C	0	C	С	0	С	С	С	C	С	445.50	351
104	0	С	0	C	С	0	C	С	0	C	С	С	С	C	441	369
105	C	0	С	C	C	0	C	С	0	C	С	С	С	C	436.50	396
106	0	0	0	0	0	С	C	С	0	С	С	С	С	c	432	432
107	0	С	0	0	0	С	C	С	0	C	С	С	С	C	429	318
108	С	0	<u>C</u>	0	0	<u>C</u>	C	C	0_	C	C	C	С	C	424.50	370.50
109	С	С	С	0	0	С	C	С	0	C	С	С	С	C	421.50	270
110	0	С	0	C	0	С	C	С	0	C	С	C	С	C	417	339
111	0	0	_ <u>C</u> _	C	0	С	C	C	0	C	С	С	С	C	414	252
112	С	С	С	C	0	С	С	С	0	C	С	С	С	c	409.50	337.50
113	С	0	0	0	С	С	C	С	0	C	С	С	С	C	406.50	264
114	0	0	C	0	С	C	C	C	0	C	C	С	С	С	402	366
115	0	С	С	0	C	С	C	С	0	C	С	С	С	C	399	306
116	0	0	0	C	С	С	C	С	0	C	С	C	С	C	396	252
117	C	<u>C</u>		L C	C	<u></u>	C	<u> </u>	0	C	C	С	U	<u>c</u>	391.50	378
118	С	0	С	C	С	С	С	С	0	C	C	C	С	C	388.50	337.50
119	С	С	С	С	С	С	C	С	0	С	С	С	С	C	385.50	303
120	С	0	0	0	0	0	0	0	C	С	С	С	С	C	382.50	274.50
121	С	С	0	0	0	0	0	0	C	С	С	С	С	C	379.50	252
122	С	0	С	0	0	0	0	0	С	С	С	С	С	С	376.50	235.50
123	C	C	С	0	0	0	0	0	С	C	C	C	С	C	373.50	225
124	С	0	0	С	0	0	0	0	С	С	С	С	С	C	370.50	220.50
125	С	С	0	C	0	0	0	0	С	С	С	С	С	C	367.50	222
126	С	0	C	C	0	0	0	0	_ <u>C</u> _	C	С	С	С	C	364.50	229.50
127	С	С	С	C	0	0	0	0	C	С	С	С	С	С	361.50	243
128	С	0	0	0	С	0	0	0	С	C	С	С	С	C	358.50	262.50
129	С	С	0	0	С	0	0	0	С	С	С	С	С	С	355.50	288
* C = Clo	osed	lor	On	; 0	= C	pen	or	off		•			. •		·	
						-						Cor	ntinu	ed —		

TABLE 3-18. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 1.83 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
130	С	0	С	0	С	0	0	0	С	С	С	С	С	С	352.50	319.50
131	0	С	С	0	С	0	0	0	С	C	С	C	С	С	351	162
132	0	0	0	C	С	0	0	0	С	С	С	C	C	С	348	204
133	0	С	0	C	С	0	0	0	C	С	С	С	С	С	345	252
134	0	0	C	c	С	0	0	0	С	C	С	C	C	С	342	306
135	С	0	С	C	С	0	0	0	С	C	С	С	C	С	340.50	165
136	С	С	С	С	С	0	0	0	С	С	С	С	С	C	337.50	229.50
137	C	0	0	0	0	C	0	0	С	C	С	С	С	С	334.50	300
138	0	С	0	0	0	C	0	0	С	С	С	С	C	С	333	171
139	0	0	С	0	0	C	0	0	C	C	С	C	С	С	330	252
140	C	0	С	0	0	С	0	0	С	C	С	С	С	С	328.50	130.50
141	C	С	С	0	0	С	0	0	С	С	С	С	С	С	325.50	222
142	С	0	0	С	0	С	0	0	С	С	С	С	С	С	322.50	319.50
143	0	С	0	C	0	С	0	0	С	C	С	c	С	С	321	210
144	0	0	С	C	0	С	0	0	C	С	С	С	_C	С	318	318
145	С	0	С	С	0	C	0	0	С	С	С	С	C	C	316.50	216
146	0	С	С	C	0	С	0	0	С	C	С	C	С	С	315	117
147	0	0	0	0	С	C	0	0	C	C	С	c.	C	С	312	240
148	С	0	0	0	С	С	0	0	С	С	С	С	С	С	310.50	148.50
149	С	С	0	0	С	С	0	0	С	С	С	c	С	С	307.50	282
150	0	0	С	0	С	С	0	0	С	С	С	С	С	С	306	198
151	С	0	С	0	С	С	0	0	С	С	С	С	С	O	304.50	117
152	c	С	С	0	С	C	0	0	С	C	С	c	С	С	301.50	265.50
153	0	0	0	C	С	С	0	0	С	С	С	C	С	С	300	192
154	С	0	0	С	С	С	0	0	С	С	С	С	С	C	298.50	121.50
155	C	С	0	C	С	С	0	0	С	C	С	C	С	C	295.50	285
156	0	0	С	С	С	С	0	0	С	С	С	C	_C	С	294	222
157	С	0	С	С	С	С	0	0	С	C	С	С	С	C	292.50	162
158	0	С	С	C	С	С	0	0	С	C	С	С	С	С	291	105
159	0	0	0	0	0	0	С	0	С	C	С	С	C	С	288	288
160	С	0	0	0	0	0	С	0	С	C	С	С	C	C	286.50	238.50
161	0	С	0	0	0	0	С	0	С	C	С	С	С	С	285	192
162	C	С	0	0	0	0	С	0	С	C	С	С	C	C	283.50	148.50
163	0	0	С	0	0	0	С	0	С	С	С	С	С	C	282	108
164	С	0	С	0	0	0	С	0	С	С	С	c	С	C	280.50	70.50
165	0	C	С	0	0	0	C	0	С	C	С	С	С	С	279	36
166	0	0	0	С	0	0	С	0	С	С	С	С	С	C	276	252
167	С	0	0	C	0	0	С	0	С	C	С	С	C	C	274.50	225
168	0	С	0	C	0	0	С	0	С	C	C	c	С	С	273	201
				<u> </u>			<u> </u>									
* C = Clo	osed	l o	r On	; 0	= (pen	or	off	•							

TABLE 3-19. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 2.75 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in Usable	Bytes
of Sectors	20	2 ¹	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	² 2 ¹³	Sectors	in Runt Sector
4	0	0	0	C	0	0	С	0	С	С	0	0	С	0	11448	0
5	0	С	0	C	С	С	0	0	0	0	С	С	С	0	9158	2
6	0	0	0	0	С	С	0	0	0	C	0	0	0	C	7632	0
7	C	С	0	0	С	С	C	0	0	C	С	0	0	C.	6541	5
8	0	0	С	0	0	С	0	С	С	0	0	С	0	С	5724	0
9	0	_0	0	0	0	C	0	0	0	0	<u>C</u>	С	0		5088	0
10	С	0	С	C	С	0	0	0	0	С	С	С	0	c	4579	2
11	0	С	С	C	C	С	0	С	C	C	С	С	0	C	4162	10
12	0	0	0	C	C	0	0	0	С	0	0	0	С	С	3816	0
13	0	С	С	C	С	С	0	0	0	C	0	0	С	C	3522	6
14	0	С	0	C	С	С	0	0	С	С	0	0	С	С	3270	12
15	0	0	С	0	C	0	0	0	0	0	C	0	С	С	3052	12
16	0	С	0	0	С	0	C	С	0	0	С	0	С	C	2862	0
17	С	С	0	C	С	С	C	0	С	0	С	0	С	C	2693	11
18	0	0	0	0	C	0	0	0	0	C	С	0	С	C	2544	0
19	0	С	С	0	С	0	0	С	0	C	С	0	С	C	2410	2
20	С	С	С	C	0	0	0	0	С	C	С	0	С	C	2289	12
21	0	_0	C	C	C	C	C	0	_ <u>C</u>	C	C	0	С	С	2180	12
22	C	С	С	С	С	0	C	С	С	C	С	0	С	С	2081	10
23	0	С	0	С	С	С	0	0	0	0	0	С	С	С	1990	22
24	0	_0_	<u></u>	C	0	<u> </u>	0	<u></u>	0	0	0	С	C	C	1908	0
25	C	0	0	C	C	0	C	C	0	0	0	C	C	C	1831	17
26	C	C	C	C	С	0	0	0	C	0	0	С	C	C	1761	6
27	-	_0_	<u> </u>	0	<u> </u>	<u> </u>	C	<u> </u>	<u> </u>	0	<u> </u>	C	C	C	1696	0
28	C	0	C	C	C	0	0	C	C	0	0	С	С	C	1635	12
29	С	0	С	0	C	0	C	C	C	0	0	С	C	C	1579	1
30	0	_ <u>c</u> _	<u> </u>	l c		<u> </u>	0	<u> </u>	_0_	C	0	С	C	C	1526	12
31	C	C	0	C	C	C	0	0	0	C	0	С	C	C	1477	5
32	C	0	0	C	0	C	C	0	0	C	0	С	C	C	1431	0
33	C		<u> </u>	10	<u>_c</u>	<u> </u>	0	_ <u>C</u> _	<u> </u>	C	<u> </u>	C	C	C	1387	21
34 35	0	C	C C	C	C	C C	0	C	0	C	0	C C	C	C C	1346 1308	28 12
		_		_	0		I		_	I						i _
36 37	0 C	<u>0</u> C	0	C	0	<u>0</u> C	0	0	C C	C	0	C C	C	C C	1272 1237	23
38	C	C	0	C	0	0	C		C	C	0	C	C	c	1205	23
39	0	_C	0	C	0	C_	C	0 0.	C	C	0	С	C	c	1174	6
40	0	-	0	C	0	0	0	C.	_ C _	C	0	c	C	C	1144	32
41	0	0	C	0		C	0	C	C	C		C	C	C	1114	36
42		C			0	C	•			C	0		טט	C	1090	12
42	0	0	<u> </u>	C	C	0	0	C C	C C	C	0	C C	C		1064	40
43	0			1	C		C	C	C	C	0		C	C	1064	32
	ł	0	0	0		C	i			1	0	C	C			27
45	С	С	С	0	0	0	0	0	0	0	С	С	U	С	1017	41
* C = Clo	osed	or	On	; 0	= C	pen	or	off				Cor	ntinu	ıed —		

3-90 83325710 G

TABLE 3-19. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES
USING 2.75 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
46	С	0	С	С	С	0	0	0	0	0	С	С	С	С	995	22
47	0	С	0	0	С	С	0	0	0	0	С	С	С	С	974	14
48	0	С	С	0	0	0	C	0	0	0	C	С	C	С	954	0
49	0	C	0	C	С	0	С	0	0	0	С	С	С	С	934	26
50	C	0	C	C	0	C	C	0	0	0	C	C	C	C	915	42
51	C	<u>C</u>	C	C	C	_ <u>C</u> _	C	0	0	0	C	드	<u>C</u>	C	897	45
52 53	0	0	0	0	C 0	0	0	C	0	0	C C	C	С	C	880 864	32 0
	0	0	0	0		С	0		0	0	C		C	C		0
<u>54</u> 55	0	0	0	0	0	<u>C</u>	0 C	C	0	0	c	C	C	C	848 832	32
56	C	C	C	C	0	0	c	C	0	0	C	c	C	C	817	40
57	c	0	С	C	c	0	C	C	0	0	С	c	C	c	803	21
58	c	c	0	c	- 0	C	c	c	- 0	0	C	C	C	C	789	30
59	0	ō	ō	c	C	Ċ	c	C	ō	0	Č	c	C	c	776	8
60	С	0	C	o	0	ō	0	o	C	0	C	c	C	С	763	12
61	0	С	0	0	С	0	0	0	С	0	С	С	С	С	750	42
62	0	С	С	С	С	0	0	0	С	0	С	С	С	С	738	36
63	0	C	0	С	0	С	0	0	С	0	С	С	С	С	726	54
64	С	0	С	0	С	С	0	0	С	0	С	n	С	C	715	32
65	0	0	0	0	0	0	C	0	С	0	С	С	C	С	704	32
66	С	С	0	С	0	0	C	0	С	0	С	С	С	С	693	54
67	C	0	С	0	С	0	C	0	С	0	С	С	С	Ç	683	31
68	С	С	С	C	С	0	C	0	С	0	С	С	С	С	673	28
69	C	0	0	C	0	С	C	0	С	0	С	_C	C	С	663	45
70	0	С	0	0	C	С	C	0	С	0	С	С	С	С	654	12
71	0	0	С	C	С	С	C	0	С	0	С	С	С	С	644	68
72	0	0	C	0	0	<u> </u>	0	<u> </u>	<u></u>	0	C	<u></u>	C	C	636	0
73	C	0	C	C	0	0	0	C	C	0	C	С	C	C	627	21
74 75	0	С	C	0	C	0	0	C C	C	0	C	C	C	C	618	60 42
76	0	C	_C _C	0	<u>C</u>	<u>0</u>	0	C	C C	0	C	C	C C	C	610 602	42
77	0	C	C	C	0	C	0	C	C	0	C	C	C	C	594	54
	c	0	<u> </u>	10	c	c	0		C	0		C	C	C	587	6
79	c	0	c	C	c	C	0	C C	c	0	C C	c	C	C	579	51
80	0	0	C	ő	o	Ö	c	C	C	0	C	c	C	c	572	32
81	c	C	o	C	o	0	C	c	c	0	c	C	c	c	565	27
82	0	C	0	0	C	0	c	C	C	ō	C	c	С	C	558	36
83	С	0	0	С	С	O	C	С	С	0	C	С	C	С	551	59
84	С	С	С	С	С	0	С	С	С	0	С	С	С	С	545	12
85	0	С	С	0	0	С	С	С	С	0	С	С	С	C	538	62
86	0	0	С	С	0	С	C	C	С	0	С	С	С	С	532	40
87	0	С	0	0	С	С	С	С	С	0	С	C	C	С	526	30
* C = Clo	osed	or	On	; 0	= (pen	or	off	:				<u> </u>			
												Cor	ıtinu	ed —		

TABLE 3-19. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES
USING 2.75 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	2 ¹	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 2 13	Usable Sectors	in Runt Sector
88	0	0	0	С	С	С	С	С	С	0	С	С	С	С	520	32
89	0	С	С	C	С	С	С	C	С	0	С	С	С	С	514	46
90	0	0	C	0	0	0	0	0	0	C	С	С	С	С	508	72
91	C	0	0	C	0	0	0	0	0	C	С	C	С	С	503	19
92	C	С	С	C	0	0	0	0	0	C	С	С	С	С	497	68
93	10	0	С	0	<u>C</u>	0	0	0	0	C	C	С	С	С	492	36
94	C	0	0	C	С	0	0	0	0	C	С	С	С	С	487	14
95	0	С	С	C	С	0	0	0	0	C	C	С	С	С	482	2
96	C	C	0	0	0	<u>C</u>	0	<u> </u>	0	C	C	С	С	С	477	0
97	0	0	0	C	0	C	0	0	0	С	С	С	С	С	472	8
98	C	0	С	C	0	C	0	0	0	C	С	С	С	С	467	26
99	0	C	0	0	C	C	0	0	0	C	C	С	С	C	462	54
100	C	С	С	0	С	С	0	0	0	C	С	С	С	С	457	92
101	C	C	0	C	С	С	0	0	0	C	С	С	С	С	453	39
102	0	0	0	0	0	0	С	0	0	C	С	С	С	С	448	96
103	0	0	С	0	0	0	C	0	0	C	С	С	С	С	444	60
104	0	0	0	C	0	0	С	0	0	C	С	С	С	С	440	32
105	0	0	С	C	0	0	C	0	0	С	С	С	С	С	436	12
106	0	О	0	0	С	0	С	0	0	C	С	С	С	C	432	0
107	C	0	С	0	С	0	С	0	0	C	С	С	С	С	427	103
108	0	0	0	C	C	0	C	0	0	C	С	С	С	С	424	0
109	0	0	С	C	С	0	С	0	0	C	С	С	С	C	420	12
110	0	0	0	0	0	С	С	0	0	C	С	С	С	С	416	32
111	0	0	<u>C</u>	0	0	C	C	0	0	C	C	С	C	C	412	60
112	0	0	0	C	0	С	С	0	0	С	С	С	С	С	408	96
113	C	C	0	C	0	C	C	0	0	С	С	С	С	С	405	27
114	C	C	C	C	0	C	C	0	0	C	C	С	С	C	401	78
115	0	С	0	0	С	С	С	0	0	С	С	C	С	C	398	22
116	0	С	С	0	C	С	C	0	0	C	С	С	С	C	394	88
117	C	0	0	C	С	С	C	0	0	C	С	С	С	С	391	45
118	0	0	С	C	С	С	С	0	0	С	C	С	С	С	388	8
119	0	0	0	0	0	0	0	C	0	С	С	С	С	С	384	96
120	C	C	0	0	0	0	0	С	0	С	C	С	С	C	381	72
121	0	С	С	0	0	0	0	C	0	C	C	С	С	С	378	54
122	C	0	0	С	0	0	0	С	0	C	С	С	С	С	375	42
123	0	0	C	C	0	0	0	С	0	С	С	С	С	C	372	36
124	C	C	С	С	0	0	0	С	0	С	С	С	С	С	369	36
125	0	C	0	0	C	0	0	С	0	C	С	С	С	С	366	42
126	C	0	С	0	С	0	0	С	0	С	С	С	С	С	363	54
127	0	0	0	C	С	0	0	С	0	С	С	С	С	С	360	72
128	C	С	0	С	С	0	0	С	0	С	С	С	С	С	357	96
129	0	С	С	C	С	0	0	С	0	С	С	С	С	С	354	126

Continued —

TABLE 3-19. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 2.75 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number of						Sec	tor	Swi	tch						Bytes in Usable	Bytes in Runt
Sectors	20	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Sectors	Sector
130	0	0	0	0	0	С	0	С	0	С	С	С	С	С	352	32
131	C	С	0	0	0	С	0	C	0	C	С	C	С	C	349	73
132	0	C	С	0	0	C	0	С	0	C	С	С	С	С	346	120
133	0	0	0	C	0	С	0	С	0	C	С	C	С	С	344	40
134	С	С	0	C	0	С	0	C	0	[C	С	C	С	С	341	98
135	C	0	C	C	0	_C	0	С	0	C	C	C	C	С	339	27
136	0	0	0	0	С	C	0	С	0	C	С	C	С	С	336	96
137	0	С	0	0	С	С	0	С	0	C	С	C	С	С	334	34
138	C	0	_ <u>c</u>	0	C	<u>C</u>	0	С	0	C	C	드	<u> </u>	С	331	114
139	C	С	С	0	С	С	0	С	0	C	C	C	С	С	329	61
140	C	0	0	C	С	C	0	С	0	C	С	C	С	С	327	12
141	0	0	<u>C</u>	C	C	<u> </u>	0	_ <u>C</u>	0	C	C	_C	С	С	324	108
142	0	С	С	C	С	С	0	С	0	C	С	C	С	С	322	68
143	0	0	0	0	0	0	C	С	0	C	С	C	С	C	320	32
144	0	C	0	0	0	_0	C	С	0	C	С	C.	С	С	318	0
145	C	0	С	0	0	0	С	С	0	C	С	C	С	С	315	117
146	С	С	С	0	0	0	С	С	0	C	С	C	С	C	313	94
147	С	_0	0	C	0	_0	С	C	0	C	С	C	С	С	311	75
148	C	С	0	C	0	0	С	С	0	C	С	C	С	С	309	60
149	C	0	С	C	0	0	С	С	0	C	С	C	С	С	307	49
150	C	<u></u>	<u>C</u>	C	0	_0_	C	C	0	C	C	C	C	С	305	42
151	C	0	0	0	С	0	С	С	0	C	С	C	С	С	303	39
152	С	С	0	0	С	0	C	С	0	C	С	C	С	C	301	40
153	С	0	C	0	С	0	С	C	_0_	C	<u>C</u>	C	C	С	299	45
154	C	С	С	0	С	0	C	С	0	C	С	C	С	С	297	54
155	C	0	0	C	С	0	С	С	0	C	С	C	С	С	295	67
156	C	С	0	C	C	0	С	С	0	C	C	C	С	С	293	84
157	C	0	С	C	С	0	C	С	0	C	С	C	С	С	291	105
158	С	С	С	C	С	0	С	С	0	C	С	С	С	C	289	130
159	0	0	0	0	0	C_	C	C	_0_	C	C	C	С	C	288	0
160	0	С	0	0	0	С	C	С	0	C	С	c	С	С	286	32
161	0	0	С	0	0	С	C	С	0	C	С	C	С	С	284	68
162	0	C	С	0	0	C	C	С	0	C	С	С	C	С	282	108
163	0	0	0	C	0	С	С	С	0	С	C	С	С	С	280	152
164	С	0	0	C	0	С	C	С	0	C	С	C	С	С	279	36
165	C	С	0	C	0	С	C	C	0	C	C	С	С	C	277	87
166	С	0	С	C	0		C	С	0	C	С	C	С	С	275	142
167	0	С	С	C	0		C		0	C	С	C	С	С	274	34
168	0	0	0	0	С	С	С	С	0	C	С	C	С	С	272	96
* C = Clo	osed	1 oı	on.	; 0	= (Open	or	off	•							<u> </u>

TABLE 3-20. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 2.75 MHz (BYTE) CLOCK AND ROUND-UP METHOD

10123456/8910112131 - 1	Number of						Sec	tor	Swi	tch						Bytes in Early	Bytes in Last
5 C O O C C C O O O C C O O D D D D D P156 6 D P0 C C C O O C C C O O C C C O O C C C O O C C C O O C C C O O C <td></td> <td>2⁰</td> <td>21</td> <td>22</td> <td>2³</td> <td>24</td> <td>2⁵</td> <td>26</td> <td>2⁷</td> <td>28</td> <td>29</td> <td>2¹⁰</td> <td>21:</td> <td>212</td> <td>2 2 13</td> <td></td> <td>Sector</td>		2 ⁰	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	21:	212	2 2 13		Sector
6 O O C C O O C O O C C O C C O C C O C C G		1	0	0				С	0	С	С				0		
7 0 C 0 C C 0 0 C C 0 0 C 5744 5724 4162		C	0	0	C	С		0	0	0	0	С	С	С			l .
8 0 0 C 0 C C 0 C C 5724 5724 5724 9 0 0 0 C C C C 5088 5088 5088 5088 5088 5088 5088 5088 5088 4572 11 C 0 C		0		0	0		С	0	0	0	C	0	0	0			
9 O O O C O O C C C D C S088 5088 10 O O C C C O C<		0	С	0	0	C		C	0	0	C	С	0	0			
10		0	0	С	0	0	С	0	С	С	0	0	С	0	C		
11 C O C C C C C C C A163 4162 3816 3623 3516 14 C 0 0 C 0		0	0		0		<u></u>	0	0	0	0	C	С	0			
12 0		1	0	C	C			0	0	0	C		С	0			
13 C O C C C O O C C C 3523 3516 14 C O O C C O O C C 3271 3269 15 C C O O C C O C C C 3053 3050 16 O C O O C C O C C C 2862 2862 17 O C O C C O C C 2694 2688 18 O O O C C O C C 2694 2888 18 O O C C O C C C 2694 2888 18 O C C C O C C C 2299 2282 21		C	0	С	C	С	С	0	С	С	C	С	С	0			
14 C O O C C O O C C 3271 3269 15 C C O O C O C C 3053 3050 16 O C O C C O C		0	0	0		<u>C</u>	0	0	0	С	0	0	0				
15 C C O O O O C C C 3053 3050 16 O C O C C O O C C 2862 2862 2862 17 O C O C C O C C C 2694 2688 18 O O O C O C C C C 2544 2544 19 C O C O C C C C 2411 2394 20 O C C C O C C C 2411 2394 20 O C C C C C C C 2290 2282 21 C C C C C C C C C 2299 2282 2070 22 </td <td></td> <td>C</td> <td>0</td> <td>С</td> <td>1</td> <td></td> <td></td> <td>0</td> <td>0</td> <td>0</td> <td>C</td> <td>0</td> <td>0</td> <td>1</td> <td></td> <td></td> <td></td>		C	0	С	1			0	0	0	C	0	0	1			
16 0 C 0 C L L L L	14	C	0	0	C	С	С	0	0	С	C	0	0	С	C		3269
17 0 C 0 C C 0 C 0 C 2 694 2688 18 0 0 0 0 0 0 C C 0 C 2544 2544 19 C 0 C 0 C C 0 C C 2411 2394 20 O C C O O C C C 2411 2394 20 O C C C O C C C 2290 2282 21 C C O C C C C C C C C C C 2290 2282 2270 23 C O O C C C C C C 1991 1991 1990 24 O O C C O O		C	C	. 0	0	C	0	0	0	0	0	С	0	С	С	3053	3050
18 O O O C C C C C C 2544 2544 19 C O C O O C O C C C C 2411 2394 20 O C C C O C D	16	0	С	0	0	С	0	C	C	0	0	С	0	С	С	2862	2862
19 C O C O C C C C C 2411 2394 20 O C C C O C C C C 2290 2282 21 C C O C C C C C C C C 2290 2282 21 C C O C C C C C C 2290 2282 21 C C C C C C C C C 2181 2172 22 O C C C O O C C C 1991 1990 24 O O C C O O O C C 1908 1908 25 O O O C C O O C C C <	17	0	С	0	C	С	С	C	0	С	0	С	0	С	С	2694	2688
20 O C D D C C C	18	0	0	0	0	С	0	0	0	0	C	С	0	С	С	2544	2544
21 C D	19	С	0	С	0	С	0	0	С	0	С	С	0	С	С	2411	2394
22 O C C C C C C C O O C C C C O C C C O C C C O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O O O O O O C C C C O	20	0	С	С	C	0	0	0	0	С	С	C	0	С	С	2290	2282
23 C O O C C C O O O O C O O O C C C C 1991 1990 24 O O C C C O O O O C O O O C C C C 1908 1908 25 O O O C C C C O O O C C O O C C C C C 1832 1824 26 O C C C C C C O O O C C O O C C C C 1762 1742 27 O O O O C C C C O O C C C C O C C C C 1696 1696 28 O O C C C C C O O C C C C C O C C C C C	21	C	C	0	C	C	С	C	0	С	C	С	0	С	С	2181	2172
24 O C C O O C C C C 1908 1908 25 O O C C O C C C C C C 1832 1824 26 O C C C O O C C C C 1742 1742 27 O O O C C O C C C 1696 1696 28 O O C C O C C C 1696 1696 29 O C C O C C C C 1527 1509 31 O C C C C O O C C C 1478 1452 32 C O O C O C C C 1431 1431	22	0	С	С	С	С	0	С	С	С	С	С	0	С	С	2082	2070
25 0 0 0 0 C C C 0 C 0 C 0 0 C C C 1832 1824 26 0 C C C C C C 0 0 0 C C 0 C C C 1762 1742 27 0 0 0 0 0 C C C C O C C C C C 1696 1696 28 0 0 C C C C O C C C C O C C C C C 1636 1620 29 0 0 C C C C C C C C C C C C C C C C C C	23	C	0	0	C	С	С	0	0	0	0	0	С	С	С	1991	1990
26 O C C C O O C	24	0	0	С	С	0	0	0	С	0	0	0	С	С	С	1908	1908
27 O O O O C C C O C O C O C C C C 1696 1696 28 O O C C C C O O C C C O O C C C 1636 1620 29 O O C O C O C O C C C C O O C C C C 1580 1552 30 C O O C C C C O O O O C C C C C 1580 1552 31 O C O C C C C O O O C C C C C 1478 1452 32 C O O C C C C C O O C C C C C C C C C C	25	0	0	0	С	С	0	С	С	0	0	0	С	С	С	1832	1824
28 0 0 0 C C C 0 0 0 C C 0 0 C C 0 0 0 C C C 1636 1620 29 0 0 C 0 C 0 C 0 C 0 C C C 0 0 C C C 1580 1552 30 C 0 0 C C 0 0 C 0 C C C 0 0 C C C C 1580 1552 31 0 C 0 C C C C 0 0 O C C C C C 1527 1509 31 0 C 0 C C C C O C C C C C C C C C C C C	26	0	С	С	C	С	0	0	0	С	0	0	С	C	C	1762	1742
29 O O C C O C O C C C C O O C C C C I580 1552 30 C O O C C O O O O O O C C C C I527 1509 31 O C O C C C C O O O C C C C I478 1452 32 C O O C C C C C O O C C C C C I431 1431 33 O O C O C O C O C O C C C C I388 1376 34 C O C C C C C C O C O C C C C C I347 1341 35 C C O O O C C C C C C C C C C C C C C C	27	0	0	0	0	0	С	С	0	С	0	0	С	С	С	1696	1696
30 C O C O O C O C C C L 1509 31 O C O C C C C C 1478 1452 32 C O O C C C C C C L 1431 1431 1431 33 O O C O C O C C C C 1388 1376 34 C O C C C O C 1347 1341 1341 1347 1341 1341 1366 36 O O O C C C C C C C C C C C	28	0	0	С	С	С	0	0	С	С	0	0	С	C	С	1636	1620
31 O C O C O C C C O O O C C C C C 1478 1452 32 C O O O C O C C C O O C C C C 1431 1431 33 O O C O C O C O C O C C C C 1388 1376 34 C O C C C C C O C O C O C C C C 1347 1341 35 C C O O O C C C C C C C C C C C C C C C	29	0	0	С	0	С	0	C	С	С	0	0	С	C	C	1580	1552
32 C O O C C O C C C O O C C C C C 1431 1431 33 O O C O C O C O C O C C C C 1388 1376 34 C O C C C C C O C O C O C C C C 1347 1341 35 C C O O O C C C C O C C C C C 1309 1286 36 O O O C C O C O C C C C C C C C C C C C	30	C	0	0	С	0	0	0	0	0	C	0	С	С	С	1527	1509
33 0 0 0 C 0 C 0 C 0 C 0 C 0 C 0 C C C 1388 1376 34 C 0 C C C C C C C O C C C C C C C C C C	31	0	C	0	С	С	С	0	0	0	С	0	С	С	С	1478	1452
34 C O C C C C C O C O C O C C C C I347 1341 35 C C O O O C C C C O C C C C I309 1286 36 O O O C C O C O C C C C C I272 1272 37 O C O C O C O C O C C C C C C I238 1224 38 O C O C O C O C C C C C C C I206 1170 39 C O O C C C C C C C C C I175 1142 40 C C C C O C C C C C C C C C C C I1175 1142 41 C C C C C C C C C C C C C C C C C C C	32	С	0	0	C	0	C	C	0	0	С	0	С	С	C	1431	1431
35 C C C O O O C C C C O C C C C 1309 1286 36 O O O C C O O O O C C C C C C 1272 1272 37 O C O C O C O C O C C C C C C 1238 1224 38 O C O C O C C C C C C C C C C 1206 1170 39 C O O C C C C C C C C C C C C C C C C C	33	0	0	С	0	С	0	0	С	0	С	0	С	С	С	1388	1376
36 O O O O C O O O C O C C O C C C C 1272 1272 37 O C O C O C O C O C C O C C C C C 1238 1224 38 O C O C O C O C C C C C C C C 1206 1170 39 C O O C C C C C C C C C C C C C C C C C	34	С	0	С	С	С	C	0	С	0	С	0	С	C	С	1347	1341
37 0 C 0 C 0 C 0 C 0 C 0 C C 0 C C 0 C C 1238 1224 38 0 C 0 C 0 C 0 C C 0 C C C C 1206 1170 39 C 0 0 C C C C C C C C C C C 1175 1142 40 C C C C 0 0 0 C C C C C C C C C C C 11175 1137 41 C C C 0 0 C C C C C C C C C C C C C C C	35	C	С	0	0	0	С	C	C	0	C	0	С	C	С	1309	1286
38 0 C O C O C O C C O C C C C C 1206 1170 39 C O O C C O C C C O C C C C 1175 1142 40 C C C C O O O O C C C C C C C C 1145 1137 41 C C O C C C C C C C C C C C C C C C C C	36	0	0	0	С	0	0	0	0	С	С	0	С	C	С	1272	1272
38 0 C O C O C O C C O C C C C C 1206 1170 39 C O O C C O C C C O C C C C 1175 1142 40 C C C C O O O O C C C C C C C C 1145 1137 41 C C O C C C C C C C C C C C C C C C C C					_			0									
39 C O O C C O C C O C C O C C O C C C D C C D D D D D D D D D D D D D D D D D D D					C			1	0		C	0		C			
40 C C C C O O O O O O C C C C C C 1145 1137 41 C C O C O C C C C C C C C C C 1117 1112 42 C O C C C C C C C C C C C C C C C C C C					С	0		I .	0		_c	0					
41 C C O O C C O C C C C C 1117 1112 42 C O C C C C C C C C C C C C C C C C C C					_												
42 C O C C C C O C C C C C C C C C C C C C C C C		4															1
43 C C C C O C O C C C C C C C C C C C C C		C	_0	C	_c	C		0	С	С	_c	<u>o</u> .	С	С		1091	1061
44 C C C C O C C C C C C C C 1041 1029																1065	
								1						1			
		ı						I			i .						1
* C = Closed or On; O = Open or Off		<u> </u>			<u> </u>			ł			<u></u> _						<u></u>

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TABLE 3-20. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES
USING 2.75 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	21.	212	2 ¹³	Early Sectors	in Last Sector
46	0	0	С	С	С	0	0	0	0	0	С	С	С	С	996	972
47	C	0	0	0	С	С	0	0	0	0	С	С	С	С	975	942
48	0	С	_ <u>C</u> _	0	0	0	C	0	0	0	<u>C</u>	C	С	С	954	954
49	C	0	0	C	C	0	С	0	0	0	С	С	С	С	935	912
50	0	0	С	С	0	С	C	0	0	0	С	С	С	С	916	908
51	0	C	<u> </u>	C	<u> </u>	C	C	0	0	0	C	<u> </u>	C	C	898	892
52 52	C	C	С	C	0	0	0	С	0	0	C	C	C	C	881	861
53	0	0	0	0	0	C	0	C	0	0	C	C	C	C	864	864
54	0	0	0_	0	<u>C</u>	<u>C</u>	0	C	0	0	C	C	С	С	848	848
55 56	C	C	C	C	C	C	0	C	0	0	C	C	C	C	833	810
56	0	C	C	C	0	0	C	C	0	0	C	С	С	C	818	802
57	0	0	_ <u>C</u> _	C	<u> </u>	0	C	C	0	0	C	<u></u>	C	C	804	768
58 59	0	С	0	C	0	C	C	С	0	0	С	С	C	C	790	762 726
60	C	C	C	0	C	C	C	C	0	0	C	С	C	C	777 764	726
61	C	0	_ <u>C</u>	0	<u>0</u> C	0	0	0	<u>C</u>	0	C	C C	C	C	751	732
62	C	0	C	C	C	0	0	0	C	0	C	C	C	C	739	713
63	C	0	0	C	0	C	0	0	С	0	C	C	C	C	727	718
64	0	0	C	0	c	c	0	0	C	1 0	c	c	C	C	716	684
65	C	C	C	C	C	C	١٥	o	c	۱ŏ	c	c	C	C	705	672
66	0	C	o	c	0	o	C	o	C	١٥	c	C	C	C	694	682
67	0	0	C	0	C	0	C	0	c	0	C	C	С	С	684	648
68	0	C	C	c	C	o	C	0	C	0	C	C	C	c	674	634
69	o	0	o	c	0	Ċ	c	o	C	١٥	C	C	C	c	664	640
70	c	0	ō	ō	C	C	c	0	C	0	C	C	C	C	655	597
71	C	C	ō	c	C	C	C	o	C	0	C	C	C	C	645	642
72	lo	o	C	0	0	0	0	C	c	0	C	Ċ	C	C	636	636
73	o	0	C	c	0	0	0	C	C	0	C	C	С	C	628	576
74	c	0	C	٥	C	Ō	٥	C	Ċ	٥	Ċ	Č	C	C	619	605
75	c	o	C	c	C	Ō	0	C	C	٥	Ċ	Ċ	С	c	611	578
76	С	0	С	0	0	C	ō	C	С	ō	С	С	С	С	603	567
77	C	0	C	c	0	C	0	C	C	o	C	С	C	С	595	572
78	0	0	C	0	С	С	0	C	С	0	С	С	С	С	588	516
79	0	0	С	С	С	С	0	С	С	0	С	С	С	С	580	552
80	С	С	0	0	0	0	С	С	С	0	С	С	С	С	573	525
81	0	С	0	С	0	0	С	С	С	0	С	С	С	С	566	512
82	С	0	0	0	С	0	С	С	С	0	С	С	С	С	559	513
83	0	0	0	С	С	0	C	С	С	0	С	С	С	С	552	528
84	0	С	С	С	С	0	С	C	С	0	С	С	С	С	546	474
85	С	0	С	0	0	С	С	С	С	0	С	С	С	С	539	516
86	С	С	0	С	0	С	С	С	С	0	С	С	С	С	533	487
87	С	0	0	0	С	С	С	С	С	0	С	C	С	С	527	470
* C = Clo	osed	or	On	; 0	= C	pen	or	off		1		Co	ntinu			
												COL		icu		

TABLE 3-20. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 2.75 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number of						Sec	tor	Swi	tche						Bytes in Early	Bytes in Last
Sectors	20	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 2 13	Sectors	Sector
88	С	С	С	0	С	С	С	С	С	0	С	С	С	С	521	465
89	С	0	С	C	С	С	C	С	С	0	С	С	C	C	515	472
90	C	C	0	0	0	0	0	0	0	С	С	C	С	С	509	491
91	0	0	0	C	0	0	0	0	0	C	С	С	С	C	504	432
92	0	С	С	C	0	0	0	0	0	С	С	С	С	С	498	474
93	C	<u>C</u>	0	0	C	0	0	0	0	С	С	С	С	С	493	436
94	0	0	0	C	С	0	0	0	0	С	С	С	С	С	488	408
95	C	0	С	C	С	0	0	0	0	С	С	С	С	С	483	390
96	С	С	0	0	0	С	0	0	0	C	С	С	С	С	477	477
97	С	С	С	0	0	С	0	0	0	С	С	С	С	С	473	384
98	0	0	С	C	0	C	0	0	0	С	С	С	С	С	468	396
99	С	0	0	0	С	С	0	0	0	С	С	С	С	С	463	418
100	0	С	С	0	С	С	0	0	0	С	С	С	C	С	458	450
101	0	С	0	C	С	С	0	0	0	С	С	С	С	c	454	392
102	С	С	С	С	С	С	0	0	0	С	С	С	C	С	449	443
103	С	С	0	0	0	0	С	0	0	С	С	С	С	С	445	402
104	С	С	С	0	0	0	С	0	0	С	С	C	С	c	441	369
105	С	С	0	С	0	0	С	0	0	С	С	C	С	С	437	344
106	0	0	0	0	С	0	С	0	0	С	С	С	С	С	432	432
107	0	0	С	0	C	0	С	0	0	С	С	С	С	С	428	424
108	0	0	0	С	С	0	С	0	0	С	С	С	С	c	424	424
109	U	С	0	С	С	0	U	0	0	С	С	С	С	С	421	324
110	С	С	С	C	С	0	С	0	0	С	С	С	С	c	417	339
111	С	С	0	0	0	С	С	0	0	С	С	С	С	С	413	362
112	С	С	C	0	0	С	С	0	0	С	С	С	C	С	409	393
113	0	С	0	C	0	С	С	0	0	С	С	C	С	c	406	320
114	0	С	С	С	0	С	С	0	0	С	С	С	С	С	402	366
115	C	0	0	0	С	С	С	0	0	С	С	С	С	С	399	306
116	С	0	С	0	С	С	С	0	0	С	С	C	С	c	395	367
117	0	0	0	C	С	С	С	0	0	С	С	С	С	С	392	320
118	С	С	0	C	С	С	С	0	0	C	С	С	С	С	389	279
119	С	С	C	C	С	C	С	0	0	С	С	С	С	C	385	362
120	0	C	0	0	.0	0	0	С	0	С	С	С	С	С	382	334
121	С	0	С	0	0	0	0	С	0	С	С	С	C	С	379	312
122	0	0	0	С	0	0	0	С	0	С	С	С	С	C	376	296
123	С	С	0	С	0	0	0	С	0	С	С	С	С	С	373	286
124	0	С	С	С	0	0	0	С	0	С	С	C	С	С	370	282
125	С	0	0	0	С	0	0	С	0	С	С	С	С	c	367	284
126	0	0	С	0	С	0	0	С	0	С	С	С	С	С	364	292
127	С	С	С	0	С	0	0	С	0	С	С	С	С	С	361	306
128	0	С	0	С	С	0	0	С	0	С	C	C	С	С	358	326
129	С	0	С	С	С	0	0	С	0	С	С	С	С	c	355	352
* C = Clc	sed	or	On	; 0	= O	pen	or	off		L				j		
	Closed or On; O = Open or Off Continued												ntinu	ied —	-	

3-96 83325710 G

TABLE 3-20. SECTOR SELECT SWITCH SETTINGS -- 1123 MB DRIVES USING 2.75 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	2 ²	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	l ₂ 12	2 ¹³	Early Sectors	in Last Sector
130	С	С	С	С	С	0	0	С	0	С	С	С	С	С	353	255
131	0	С	0	0	О	С	0	С	0	С	С	С	С	С	350	292
132	C	0	<u> </u>	0	0	С	0	C	0	C	С	С	С	С	347	335
133	C	С	С	0	0	C	0	С	0	C	С	С	С	C	345	252
134	0	С	0	C	0	С	0	С	0	C	С	С	С	C	342	306
135	0	0	<u>C</u>	C	0	C	0	_ <u>C</u>	0	C	C	С	С	С	340	232
136	C	С	С	C	0	С	0	С	0	C	С	С	С	С	337	297
137	C	0	0	0	С	С	0	С	0	С	C	C	С	C	335	232
138	0	0	C	0	С	С	0	C	0	C	С	С	С	С	332	308
139	0	С	С	0	С	С	0	С	0	C	С	С	С	С	330	252
140	0	0	0	C	·C	С	0	С	0	C	С	С	С	С	328	200
141	С	С	0	С	С	С	0	С	0	C	C	С	С	С	325	292
142	C	0	С	C	С	С	0	С	0	С	С	С	С	С	323	249
143	C	С	С	c	С	С	0	С	0	C	С	С	С	С	321	210
144	0	С	0	0	0	0	c	С	0	c	С	С	С	С	318	318
145	0	0	С	0	0	0	С	С	0	С	С	C	С	С	316	288
146	lo	С	С	0	0	0	c	С	0	С	С	С	С	С	314	262
147	0	0	ō	c	o	Ō	C	C	Ō	C	C	C	С	С	312	240
148	0	С	0	C	0	0	C	С	0	С	С	C	С	С	310	222
149	0	Ō	Č	١c	Ō	Ō	c	C	Ō	c	C	C	С	С	308	208
150	lo	C	Ċ	C	Ō	ō	C	C	ō	C	C	Ċ	C	C	306	198
151	o	0	0	ō	C	0	C	C	0	C	C	C	C	C	304	192
152	o	C	o	0	C	0	C	C	Ö	c	C	C	c	C	302	190
153	0	0	C	0	C	o	C	C	Ö	c	C	C	c	C	300	192
154	ō	c		0	c	0	C	c	0	C	c	c	C	C	298	198
155	0	0	o	C	C	0	C	C	0	c	С	C	c	C	296	208
156	_		_	1		_	1		_	1	С	_	1		294	222
	0	<u>C</u>	0	C	<u>C</u>	0	C	C	<u> </u>	C		C	C	C		
157	0	0	C	C	C	0	C	C	0	C	C	C	C	C	292	240 262
158	0	C	C	C	C	0	C	C	0	C	C.	С	C	С	290	I
159	10	<u> </u>	<u> </u>	10	0	<u>C</u>	C	<u>C</u>	<u> </u>	C	<u>C</u>	<u>C</u>	C	C	288	288
160	C	0	0	0	0	C	C	C	0	C	C	C	C	C	287	159
161	C	C	0	0	0	C	C	C	0	C	C	C	C	C	285	192
162	C		_ <u>C</u> _	0	0	<u>C</u>	C	C	0	C	<u></u>	_ <u>C</u> _	C	<u>C</u>	283	229
163	C	C	С	0	0	С	С	С	0	C	C	C	C	C	281	270
164	0	0	0	C	0	С	C	С	0	C	С	С	С	С	280	152
165	0	<u>C</u>	0	C	0	C	C	C	0	C	C	<u></u>	C	С	278	200
166	0	0	С	C	0	С	C	С	0	C	С	С	С	С	276	252
167	C	0	С	C	0	С	C	С	0	C	С	С	С	С	275	142
168	C	С	С	C	0	С	С	С	0	С	С	С	С	С	273	201
* C = Clo	ose	i or	On	; 0	= C	pen	or	off	:	<u>. </u>			I			<u> </u>

TABLE 3-21. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-DOWN METHOD

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
4	0	0	0	0	C	С	0	0	С	С	С	С	С	0	12600	0
5	0	0	0	0	0	0	C	С	С	0	С	0	0	C	10080	0
6	0	0	0	0	0	C	0	0	0	C	0	С	0	С	8400	0
7	0	0	0	0	0	0	C	0	С	0	С	C	0	C	7200	0
8	0	0	0	C	С	0	0	С	С	C	С	C	0	C	6300	0
99	С	C	0	C	0	C	C	0	С	0	0	0	<u>C</u>	C	5599.50	4.50
10	0	0	0	0	О	С	С	С	0	C	0	0	С	C	5040	0
11	0	С	0	0	С	0	0	0	0	0	С	0	С	C	4581	9
12	0	0	0	0	C	0	0	0	C	0	С	0	С	C	4200	0
13	0	0	0	C	0	С	C	С	С	0	С	0	С	C	3876	12
14	0	0	0	0	0	С	0	С	0	C	С	0	С	C	3600	0
15	0	0	0	0	0	0	C	0	_ <u>C</u>	С	С	0	<u>C</u>	C	3360	0
16	0	0	С	C	О	0	C	С	С	C	С	0	С	C	3150	0
17	0	0	0	C	0	0	С	0	0	0	0	C	С	C	2964	12
18	0	<u>C</u>	C	0	C	C	0	C	0	0	0	C	C	C	2799	18
19	0	0	0	C	С	0	0	0	С	0	0	C	C	C	2652	12
20	0	0	0	0	С	C	C	0	C	0	0	C	С	C	2520	0
21	0	0	0	0	0	0	C	C	C	0	0	C	С	C	2400	0
22	С	0	0	C	0	0	0	0	0	C	0	С	С	C	2290.50	9
23	0	0	С	C	0	0	С	0	0	C	0	C	С	C	2190	30
24	0	0	0	C	0	0	0	<u>C</u>	0	C	0	С	С	C	2100	0
25	0	0	0	0	0	0	C	С	0	C	0	C	С	C	2016	0
26	0	0	С	0	С	С	С	С	0	C	0	C	С	C	1938	12
27	0	_0_	C	0	0	С	0	0	_ <u>C</u> _	C	0	C	<u>C</u>	C	1866	18
28	0	0	0	0	С	0	С	0	С	C	0	C	С	C	1800	0
29	0	С	0	C	С	С	С	0	С	C	0	C	С	c	1737	27
30	0	_0_		0		<u>C</u>	0	C	C	C	0	C	<u>C</u>	C	1680	0
31	C	0	C	0	0	0	C	C	C	C	0	C	C	C	1624.50	40.50
32	0	C	C	0	0	C	С	C	C	С	0	C	C	C	1575	0
33	0	C	<u></u>	0	0	0	0	0	0	0	C	C	<u> </u>	C	1527	9
34	0	0	C	0	0	C	0	0	0	0	С	C	C	C	1482	12
35 36	0	0	0	0	0	0	C	0	0	0	C	C	С	C	1440	0
36	C	_ <u>C</u> _	_0_	드	<u></u>	0	C	<u> </u>	<u> </u>	0	<u></u>	c	_ <u>C</u>	$\frac{c}{c}$	1399.50	18
37	0	0	C	0	C	C	C	0	0	0	C	C	С	С	1362	6
38	0	0	С	C	0	0	0	С	0	0	C	C	С	C	1326	12
<u>39</u> 40	C	C	0	0	0	C	0	C	0	0	C	C	C C	C C	1291.50	31.50
	0	0	0	C	С	C	0	C	0	0		C			1260	0
41 42	C	0	С	C	0	0	C	C	0	0	C C	C	C	C	1228.50	31.50
42	0	0	0	9	0	C	C	C	0	0		C	C	<u>c</u>	1200	25 50
43	C	C	0	0	С	C	C	С	0	0	C	C	. C	C	1171.50	25.50
44 45	C	0	C	0	0	0	0	0	C	0	C C	C	C C	C C	1144.50	42
40	0	С	С	0	С	0	0	0	C	0	<u> </u>		<u> </u>	٢	1119	45
* C = Clo	osed	or	On	; 0	= 0	pen	or	off	:			Con	itinu	ed —		

TABLE 3-21. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number		· ·				Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	211	1 ₂ 12	2 ¹³	Usable Sectors	in Runt Sector
46	0	С	С	0	0	С	0	0	С	0	С	C	С	C	1095	30
47	0	С	С	0	С	С	0	0	С	0	С	С	С	С	1071	63
48	0	0	C	0	0	0	C	0	C	0	С	С	С	С	1050	0
49	C	С	0	0	С	0	C	0	С	0	С	С	С	С	1027.50	52.50
50	0	0	0	0	0	С	C	0	С	0	С	С	C	С	1008	0
51	0	C	C	C	0	С	C	0	C	0	С	С	С	С	987	63
52	0	С	0	C	С	С	С	0	С	0	С	С	С	С	969	12
53	C	С	С	0	0	0	0	С	С	0	С	С	С	С	949.50	76.50
54	0	<u>C</u>	0	0	<u>C</u>	_0_	0	C	_ <u>C</u> _	0	C	С	С	_C	933	18
55	0	С	С	C	С	0	0	С	С	0	С	С	С	С	915	75
56	0	0	0	C	О	С	0	С	С	0	С	С	С	С	900	0
57	C	_ <u>C</u>	0	10	C	C	0	_ <u>C</u>	С	0	<u> </u>	С	С	С	883.50	40.50
58	C	0	С	C	С	С	0	С	С	0	С	С	С	С	868.50	27
59	C	С	С	0	0	0	C	С	С	0	С	С	С	С	853.50	43.50
60	0	_0	0	0	C	0	C	С	C	0	С	С	С	С	840	0
61	0	С	0	C	С	0	C	С	С	0	С	C	С	С	825	75
62	C	С	0	0	0	С	C	С	С	0	С	С	С	С	811.50	87
63	C	C	_0_	C	0	<u></u>	C	C	C	0	C	C	С	С	799.50	31.50
64	C	С	0	0	С	С	C	С	С	0	С	С	С	С	787.50	0
65	0	0	С	C	С	С	C	С	С	0	С	С	С	С	774	90
66	C	C	_0_	10	0	0	0	0	0	C	C	C	C	С	763.50	9
67	C	C	0	C	0	0	0	0	0	C	C	С	С	С	751.50	49.50
68	0	C	0	0	C	0	0	0	0	C	C	C	C	С	741	12
69	0	<u>_c</u>	0	C	<u> </u>	<u> </u>	0	<u> </u>	<u> </u>	C	C	C	C	C	729	99
70	0	0	0	0	0	C	0	0	0	C	C	С	C	C	720	0
71	C	C	C	0	0	C	0	0	0	C	C	С	C	С	709.50	25.50
72	0	<u></u>	<u> </u>	L C	_0	<u></u>	0	0		C	C	<u></u>	C	<u>C</u>	699	72
73	0	0	С	0	C	C	0	0	0	C	C	C	C	C	690	30
74	0	C	0	C	C	C	0	0	0	C	C	С	C	С	681	6
75	0	_0	<u> </u>	0		0	L C	<u> </u>	<u> </u>	C	<u> </u>	<u></u>	C	C	672	0
76	0	C	C	0	0	0	C	0	0	C	C	C	C	С	663	12
77	0	0	C	C	0	0	C	0	0	C	С	C	C	С	654	42
78	0	_ <u>C</u>	0	0	<u>C</u>	0	C	0	<u> </u>	C	C	C	C	C	645	90
79	C	C	С	0	С	0	C	0	0	C	C	C	C	C	637.50	37.50
80	0	0	C	C	C	0	C	0	0	C	C	C	C	C	630	0
81	0	C	0	0	0	_ <u>C</u>	C	0	_0_	C	C	C	C	C	621	99
82	C	C	C	0	0	C	C	0	0	C	C	C	C	C	613.50	93
83 84	0	0	C	C	0	C	C	0	0	C	C	C	C	C	606	102
<u>84</u> 85	<u>0</u>	0	<u>0</u>	0	C C	C C	C	0	0	C	C C	C	C	C C	600 592.50	0 37.50
86	0	C	0	0	C	C	C	0	0	C	C	C	C	C	592.50	90
87	0	C	C	C	C		C	0	0	C	C	C	C	C	579	90 27
67		<u></u>	C		<u></u>	C		- 0	0	٢				L .	815	41
* C = Cl	osed	lor	On	; 0	= 0	pen	or	Off	;				ntinu	ed		
										_		COI	TLTIIU	ea _		

TABLE 3-21. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	212	2 ¹³	Usable Sectors	in Runt Sector
88	С	C	0	0	0	0	0	С	0	С	С	С	С	С	571.50	108
89	C	С	С	0	0	0	0	C	0	С	С	С	С	С	565.50	70.50
90	C	_ <u>C</u>	0	C	0	0	0	C	0	С	<u>C</u>	С	С	C	559.50	45
91	C	С	С	C	0	0	0	С	0	C	С	С	С	С	553.50	31.50
92	С	С	0	0	С	0	0	С	0	C	С	С	С	C	547.50	30
93	С	C	C	0	С	0	0	C	0	C	C	С	С	C	541.50	40.50
94	C	C	0	C	С	0	0	C	0	С	С	С	С	С	535.50	63
95	С	С	C	C	С	0	0	С	0	С	С	С	С	C	529.50	97.50
96	0	C	0	0		C	0	C	0	C	C	С	С	С	525	0
97	0	С	С	0	0	С	0	С	0	C	С	С	С	C	519	57
98	0	С	0	C	0	С	0	С	0	C	С	С	С	C	513	126
99	C	0	C	C	_0	С	0	<u>C</u>	0	C	С	C	С	С	508.50	58.50
100	0	0	0	0	С	С	0	С	0	C	С	С	С	C	504	0
101	0	0	С	0	С	C	0	С	0	C	С	С	С	С	498	102
102	C	C	<u></u>	0	С	C	0	C	_0_	C	C	C	С	С	493.50	63
103	0	С	0	C	С	С	0	С	0	C	С	С	С	С	489	33
104	С	0	С	C	С	С	0	С	0	С	С	С	С	C	484.50	12
105	0		0	0	0	0	C	<u>C</u>		C	C	С	С	C	480	0
106	0	0	С	0	0	0	С	С	0	C	C	С	С	С	474	156
107	0	C	С	0	0	0	C	C	0	C	C	С	C	C	471	3
108	C	0	<u> </u>	C	0	0	C	<u></u>	<u> </u>	C	C	<u> </u>	C	<u>c</u>	466.50	18
109	0	0	C	C	0	0	C	C	0	C	C	С	C	C	462	42
110	С	C	С	C	0	0	C	C	0	C	C	С	C	C	457.50	75
111	0	<u> </u>	<u>, o</u>	0	_ <u>C</u>	0	C	<u> </u>	<u> </u>	C	<u> </u>	<u> </u>	C		453	117
112	0	0	C	0	C	0	C	C	0	C	C	C	C	C	450	0
113	C	C	С	0	С	0	C	C	0	C	C	C	C	C	445.50	58.50
114	0	<u>_</u>	<u> </u>	C	<u> </u>		C	<u> </u>	<u> </u>	C	<u> </u>	C	C	C	441	126
115	0	0	C	C	C	0	C	С	0	C	C	С	C	C	438	30
116	C	C	C	C	C	0	C	С	0	C	C	С	C	C	433.50	114
117	C	<u>。</u>	<u> </u>	10	_0	C	C	C	<u>~</u>	C	_ <u>C</u> _	C	C	C	430.50 426	31.50 132
118 119	0 0	0 C	C	0	0	C	C	C	0	C	C	C	C	CC	426 423	63
				_	_		1		0	1						I _
120 121	0	<u> </u>	0	C	0	C_	C	C	0	C	C	C C	C C	C	420 415.50	0 124.50
121	C	0	C	C	0	C	C	C	0	C	C	C	C	C	413.50	75
123	C	c	С	C	0	C	C	C	0	C	C	C	ט	C	409.50	31.50
124	0	- <u>C</u>	0	0	- c	c	C	C	-	C	<u> </u>	c	C	C	409.50	180
125	0	0	C	0	C	C	C	C	0	C	C.	C	C	c	402	150
125	0	C	C	0	C	C	C	C	0	C	C	C	C	C	399	126
127	0	-	-	c	- C	- C	C	C	0	C	C	C	C	C	396	108
128	0	C	0	C	С	C	C	С	0	C	C	C	C	c	393	96
129	0	0	C	C	C	C	C	С	0	C	C	C	C	c	390	90
149			<u> </u>		<u> </u>	<u> </u>		<u> </u>				C	٠	ر	370	30
* C = Clo	sed	or	On	; 0	= (pen	or	off	•			Cor	ntinu	ed —		

TABLE 3-21. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec			tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
130	0	С	С	С	C	С	С	С	0	С	С	С	С	С	387	90
131	0	0	0	0	0	0	0	0	С	C	С	c	С	С	384	96
132	0	С	0	0	0	0	0	0	С	C	С	c	С	С	381	108
133	0	0	C	0	0	0	0	0	С	C	С	С	С	C	378	126
134	0	С	С	0	0	0	0	0	С	C	С	c	С	С	375	150
135	0	0	0	C	0	0	0	0	С	C	С	С	С	С	372	180
136	С	0	0	C	0	0	0	0	С	С	С	C	С	C	370.50	12
137	С	С	0	C	0	0	0	0	С	C	С	C	С	C	367.50	52.50
138	C	0	C	C	0	0	0	0	С	C	С	C	С	С	364.50	99
139	C	С	С	C	0	0	0	0	С	C	C	C	С	С	361.50	151.50
140	0	0	0	0	С	0	0	0	С	C	С	c	С	С	360	0
141	0	C	0	0	C	0	0	0	С	C	C	С	_C	С	357	63
142	0	0	С	0	С	0	0	0	С	C	С	c	С	С	354	132
143	0	С	С	0	С	0	0	0	С	C	С	C	С	С	351	207
144	С	<u>C</u>	C	0	<u>C</u>	0	0	0	С	C	С	С	С	С	349.50	72
145	C	0	0	C	С	0	0	0	С	C	С	c	С	C	346.50	157.50
146	0	С	0	C	С	0	0	0	С	C	С	c	С	C	345	30
147	0	0	<u>C</u>	C	С	0	0	0	С	C	С	C	С	С	342	126
148	С	0	С	C	С	0	0	0	С	C	С	c	С	c	340.50	6
149	C	С	C	C	С	0	0	0	С	C	С	c	С	С	337.50	112.50
<u>150</u>	0	0	0	0	0	C	0	0	C	C	С	С	С	С	336	0
151	0	С	0	0	0	С	0	0	С	C	С	C	С	C	333	117
152	С	С	0	0	0	С	0	0	С	C	С	С	С	C	331.50	12
<u>153</u>	С	0	C	0	0	С	0	0	С	C	С	С	С	С	328.50	139.50
154	0	С	С	0	0	C	0	0	C	C	С	C	C	С	327	42
155	0	0	0	C	0	С	0	0	С	C	С	C	С	C	324	180
156	C	0	0	C	0	С	0	0	C	С	С	С	С	С	322.50	90
157	0	С	0	C	0	С	0	0	С	C	С	C	С	C	321	3
158	0	0	С	C	0	С	0	0	С	C	С	С	С	C	318	156
159	С	0	C	C	0	С	0	0	С	C	С	С	С	С	316.50	76.50
160	0	C	С	С	0	С	0	0	С	C	С	C	C	С	315	0
161	0	0	0	0	С	С	0	0	C	С	С	c	С	С	312	168
162	С	0	0	0	C	С	0	0	C	C	С	С	С	С	310.50	99
163	0	C	0	0	С	С	0	0	С	С	С	С	C	С	309	33
164	0	0	С	0	С	С	0	0	С	C	С	c	C	C	306	216
165	С	0	С	0	С	C	0	0	С	С	С	С	С	С	304.50	157.50
166	0	С	C	0	С	С	0	0	С	С	С	C	С	С	303	102
167	С	C	С	0	С	С	0	0	С	C	С	c	C	c	301.50	49.50
168	0	0	0	С	С	С	0	0	С	С	С	c	С	С	300	0
	İ			<u> </u>			L			<u> </u>						
* C = Clo	osed	or	On	; 0	= 0	pen	or	off								

TABLE 3-22. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-UP METHOD

Sectors 20 21 22 23 24 25 26 27 28 29 210 211 212 213 Sectors Sectors Sectors 4 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Number of	l					Sec	tor	twa	tch	es*					Bytes in	Bytes
5 0		20	21	2 ²	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Las Sector
6		0	-	_	0		С	•					С	_			12600
7 0		1 -	_	_	1	_	_	1		_	} -		- 1				10080
8 0 0 C D D D D D D D C C C C C D		+			-						+						
9		1	_	_	_			ł			1 -			_			
10		1	_	_	_		_	1	_	_	1		- 1				1
11 C O O C O O O C C C 4582.50 4575 12 O O O C O C <t< td=""><td></td><td></td><td></td><td></td><td>-</td><td></td><td></td><td></td><td></td><td></td><td>+</td><td></td><td></td><td></td><td></td><td></td><td>·</td></t<>					-						+						·
12 O O O C O C C C C 4200 4200 13 C C C C C C O C		1	_	_	1 -			I			1						
13 C		1	_	_			_	I -		_					1		1
14 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		+			+						 						
15 0 0 0 0 C		1						1			_						
16 0 0 C		1	_	_			_			_	1		_		1		
17 C C C O O O C C C C 2965.50 2952 18 C O C C O O O O C C C 2800.50 2791 19 C C C C O O O C C C 2653.50 2637 20 O O O C C C C C 2520 2520 21 O O O O C C C C 2400 2400 22 O O C O O C C C 2292 2268 23 C C O O C O C C C C 2191.50 2187 24 O O O C O C C C C C <td></td> <td> </td> <td></td> <td></td> <td>-</td> <td></td>		 			-												
18 C O C C O C O C C C 2890.50 2791 19 C C C O O O O C <t< td=""><td></td><td>1 -</td><td></td><td></td><td>ı</td><td>_</td><td>_</td><td></td><td></td><td></td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td></t<>		1 -			ı	_	_				1						
19 C C C O O O C C C C 2653.50 2637.50 20 O O O C C O O C		i		_	-		_	1	_	_	1		_		1		
20 0 0 0 C C 0 C					+												2791.5
21 O O O O C					_		_	_	_		i -						
22 0 0 0 0 C 0 0 C 0 0 0 0 C 0 C C C C 2292 2268 23 C C 0 C 0 C 0 C 0 C 0 C C C C 2191.50 2187 24 0 0 0 C 0 C 0 C C C C C C C 2100 2100 25 0 0 0 O C 0 C C C C C C C C C C C C C C			О	_	0		_	1		_	0		_				1
23 C C O O C O C C C C 2191.50 2187 24 O O C O C O C C C 2100 2100 25 O O O O C C O C C C C 2100 2100 26 C C O O C C C C C 1939.50 1912 27 C C O O C C C C C 1867.50 1845 28 O O O C C C C C C 1800 1800 29 C O C C C C C C C C 1738.50 1722 30 O O O C C C C C <td></td> <td>+</td> <td></td> <td></td> <td>+</td> <td></td> <td></td> <td>1</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>		+			+			1									
24 0 0 C 0 C C C C C C 2100 2100 25 0 0 0 0 0 C C 0 C		1	_	_	1 -	_		_	_		1	-					
25 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		1	_	_	1		_	ı		_	1						1
26 C D D A C		+			 												
27 C C O O C C C C C C C C C L 1845 28 O O O C O C <td></td> <td>_</td> <td></td> <td>_</td> <td>1 -</td> <td>_</td> <td>_</td> <td>1</td> <td></td> <td>_</td> <td></td> <td>_</td> <td>1</td> <td></td> <td></td> <td></td> <td></td>		_		_	1 -	_	_	1		_		_	1				
28 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		i .		-	-	_		1		_	1	_					1912.5
29 C O O C C C C C C O C C C C C C C C C C																	
30 0		1		_	_		_		_		1	_					
31 0 0 0 C 0 0 0 C C C C C C C C C C C C C C C C C C C C		C	_	0	ı			C			1				1		
32 O C C C O O O C C C C C C C C C C C C C		+			0						 						
33 C O C O O O O O O O O O C C C C C C C 1528.50 1488 34 C C O O O O O O O O O O C C C C C C 1483.50 1444 35 O O O O O O O O O O O C C C C C C 1440 1440 36 O C O C C O O O O O C C C C C C 1401 1365 37 C C O O O C C C C O O O O C C C C C C 1363.50 1314 38 C C O C O O O O C O O C C C C C C C C C		0	_		0	0		-			_		- 1				
34 C C O O O O C C C C 1483.50 1444 35 O O O O C C C C C 1440 1440 36 O C O C O O C C C C 1401 1365 37 C C O O C C C C C C 1363.50 1314 38 C C O C O O C C C C 1327.50 1282 39 O C O O C C C C C 1293 1266 40 O O C C C O C C C C 1260 1260 41 O O C C C C C		1	С		0	0		C	С	С	C		_				
35 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0					+			0		0	+						
36 O C O C O C O C O O O C C C C C I401 1365 37 C C O O C C C C O O O C C C C C I363.50 1314 38 C C O C O O O C C C C C C I327.50 1282 39 O C O O O C C C C C C C I293 1266 40 O O O C C C C O C O C C C C C C I260 1260 41 O O C C C C O C C C C C C C C C C C C C		1		_	_	_	С		_	0							1444.5
37 C C O O C C C C O O O C C C C C 1363.50 1314 38 C C O C O C O O O C C C C C C 1327.50 1282 39 O C O O O C C C C C C C 1293 1266 40 O O O C C C C O O C C C C C C 1260 1260 41 O O C C C O O C C C C C C C C C C C C C			_	-		_	_	1	_	_	-						
38 C C O C O C O O C O O C C C C C 1327.50 1282 39 O C O O O C O O C O O C C C C C 1293 1266 40 O O O C C C C O O C O O C C C C C 1260 1260 41 O O C C C O O C C C C C C C 1230 1200 42 O O O O C C C C C O O C C C C C C C 1200 1200 43 O C O O C C C C C C C C C C C C C C C C																	
39 O C O O O C O C O C O C C C C C 1293 1266 40 O O O C C C C O C O O C C C C C 1260 1260 41 O O C C C O O C C C C C C C 1230 1200 42 O O O O O C C C C C C C C C C C 1200 1200 43 O C O O C C C C C C C C C C C C C C C C		1						ı			i .						
40 0 0 0 0 C C C C 0 C 0 0 C C C C 1260 1260 41 0 0 C C C 0 0 C C C C C 1230 1200 42 0 0 0 0 C C C C C C C C C 1230 1200 43 0 C 0 0 C C C C C C C C C C C C C C 1173 1134 44 0 0 C C 0 0 0 C C C C C C C C C C C C C							_	ı			l .						1282.5
41 0 0 0 C C 0 0 0 C C 0 0 0 C C C C C 1230 1200 42 0 0 0 0 0 0 C C C C 0 0 C C C C 1200 1200					_												
42 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				_				ł		_	-						
43 0 C 0 0 C C C C O O C C C C C I173 1134 44 0 O C O O O O O C O C C C C I146 1122 45 C O C O C O O C O C C C C I120.50 1098							-	1		_	1						
44 0 0 C 0 0 0 0 0 0 0 0 C 0 C C C C C 1146 1122 45 C 0 C 0 C 0 0 C 0 C C C C C 1120.50 1098																	
45 C O C O C O O C O C C C 1120.50 1098		1			1			ı					- 1				1134
					ı		0	1		_	1		1		1		
* C = Closed or One O = Open or Off	45	C	0	С	0	С	0	0	0	С	0	С	c	С	С	1120.50	1098
c - crosed or on; o - open or orr	* C = Clo	osed	l o	On	; 0	= (pen	or	off	:			1				<u> </u>

3-102 83325710 G

TABLE 3-22. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tche	es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	211	212	2 ¹³	Early Sectors	in Last Sector
46	С	0	С	0	0	С	0	0	С	0	С	С	С	С	1096.50	1057.50
47	С	0	C	0	C	C	0	0	C	0	C	C	С	С	1072.50	1065
48	<u> </u>	<u>0</u> C	<u>C</u>	0	0	0	C	0	C	-	C	C	C	C	1050	1050
49 50	0	0	0	0	C O	О С	C	0	C C	0	C	C	C	C C	1029 1008	1008 1008
50 51	C	0	С	C	0	C	C	0	C	0	C	C	C	C	988.50	975
52	c	0	0	c	c	C	C	0	C	0	C	C	C	c	970.50	904.50
53	0	C	C	0	0	0	0	С	C	0	C	C	C	C	951	948
54	c	0	0	0	C	0	0	C	C	0	C	C	C	C	934.50	871.50
55	C	0	_ C	c	c	0	0	c	C	ō	C	C	C	С	916.50	909
56	0	0	0	c	0	С	0	С	C	0	Ċ	C	C	c	900	900
57	0	C	0	0	C	С	0	C	C	0	C	C	C	C	885	840
58	ō	0	C	c	C	C	0	C	C	0	C	C	C	C	870	810
59	o	C	C	١٥	o	ō	c	C	Č	o	Č	C	c	c	855	810
60	0	0	0	0	C	0	С	C	C	0	C	С	С	С	840	840
61	С	0	0	C	С	0	С	С		0	С	С	С	С	826.50	810
62	0	С	0	0	0	C	C	С	С	0	С	С	С	С	813	807
63	0	С	_0	С	0	C	С	С	С	0	С	С	С	С	801	738
64	С	С	0	0	С	С	С	С	С	0	С	С	С	С	787.50	787.50
65	С	С	0	C	С	C	С	С	C	0	С	С	С	С	775.50	768
66	0	C	0	0	0	0	0	0	0	С	C	С	С	С	765	675
67	0	С	0	C	0	0	0	0	0	C	С	С	С	С	753	702
68	С	0	0	0	С	0	0	0	0	С	С	С	С	С	742.50	652.50
69	C	0	0	C	C	0	0	0	0	С	C	С	С	C	730.50	726
70	0	0	0	0	0	С	0	0	0	С	С	С	С	С	720	720
71	0	С	С	0	0	C	0	0	0	С	С	С	С	С	711	630
72	<u> </u>	0	_ <u>C</u>	C	0	C	0	0	0	C	<u>C</u>	C	C	C	700.50	664.50
73	C	C	0	0	C	C	0	0	0	C	C	С	C	C	691.50	612
74 75	C	0	0	C	C	C	0	0	0	C	C C	C	C	C C	682.50	577.50 672
76	0 C	0	<u> </u>	0	0	0	C	0	0	C	C	C C	C	C	672 664.50	562.50
77	C	C	0	C	0	0	c	0	0	C	C	C	C	c	655.50	582
 78	c	0	_0	0	<u> </u>	0	C	0	0	C	C	C	С	C	646.50	619.50
79	0	c	_ C	0		0	c	0	0	C	C	С	C	C	639	558
80	0	0	C	C	C	0	C	0	0	C	C	C	C	c	630	630
81	c	0	0	0	0	C	c	0	0	C	C	C	C	C	622.50	600
82	0	C	c	0	0	c	c	_	0	C	C	C	C	c	615	585
83	c	C	Ö	C	o	C	C	o	Ö	C	C	C	C	c	607.50	585
84	o	0	o	0	C	C	c	0	o	c	c	C	C	C	600	600
85	o	0	C	ō	c	C	C	0	0	C	C	С	C	C	594	504
86	С	0	0	C	C	C	C	0	Ö	c	C	С	С	C	586.50	547.50
87	c	0	C	C	C	C	С	0	Ō	С	C	С	С	С	580.50	477
* C = Clo			- On	• 0	= ^	ner	۰	Of F		<u> </u>						
			OH	, 0	- 0	hen	OI.	OLL				Cor	ntinu	ed —		

TABLE 3-22. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹	212	2 ¹³	Early Sectors	in Last Sector
88	0	С	0	0	0	0	0	C	0	С	С	С	С	С	573	549
89	0	С	С	0	0	0	0	С	0	C	С	С	С	C	567	504
90	0	_ <u>C</u>	0	C	0	0	0	_ <u>C</u>	0	C	<u>C</u>	C	С	C	561	471
91	0	С	С	C	0	0	0	С	0	C	С	С	С	C	555	450
92	0	С	0	0	С	0	0	С	0	С	С	С	C	C	549	441
93	0	C	C	0	С	0	0	C	0	C	C	С	С	С	543	444
94	0	С	0	C	С	0	0	С	0	С	С	С	C	C	537	459
95	0	С	С	C	С	0	0	С	0	С	С	С	С	С	531	486
96	0	C	0	0	0	C	0	_ <u>C</u>	0	C	C	С	С	С	525	525
97	C	0	С	0	0	C	0	С	0	С	С	С	С	C	520.50	432
98	C	0	0	C	0	С	0	С	0	C	С	С	С	C	514.50	493.50
99	0	0	С	C	0	C	0	C	0	C	С	C	C	С	510	420
100	0	0	0	0	С	С	0	С	0	С	С	С	С	C	504	504
101	C	С	0	0	С	С	0	С	0	С	С	С	С	C	499.50	450
102	0	C	С	0	C	С	0	С	0	C	С	С	С	С	495	405
103	C	0	0	C	С	С	0	С	0	С	С	С	С	c	490.50	369
104	0	0	С	C	С	С	0	С	0	С	С	С	С	c	486	342
105	0	0	0	0	0	0	С	С	0	С	С	C	С	C	480	480
106	С	С	0	0	0	0	C	С	0	С	С	С	С	С	475.50	472.50
107	C	0	С	0	0	0	С	С	0	С	С	С	С	C	472.50	315
108	_0	0	0	C	0	0	С	С	0	С	C	C	С	С	468	324
109	С	С	0	С	0	0	С	С	0	С	С	С	С	С	463.50	342
110	0	С	C	C	0	0	С	С	0	С	С	С	С	c	459	369
111	C	0	0	0	С	0	С	С	0	С	С	С	С	С	454.50	405
112	0	0	С	0	С	0	С	С	0	С	С	С	С	С	450	450
113	0	С	С	0	С	0	C	С	0	С	С	С	C	C	447	336
114	С	0	0	C	С	0	C	С	0	С	C	C	С	C	442.50	397.50
115	С	С	0	C	С	0	С	С	0	С	С	С	С	С	439.50	297
116	0	С	С	C	С	0	C	С	0	С	С	С	С	c	435	375
117	0	0	0	0	0	С	C	С	0	С	C	C	C	C_	432	288
118	С	С	0	0	0	С	С	С	0	С	С	С	С	С	427.50	382.50
119	С	0	С	0	0	С	С	С	0	С	C	С	С	c	424.50	309
120	0	0	Ō	С	0	C	С	C	Ō	С	C	C	С	C	420	420
121	0	C	0	С	0	C	С	C	0	С	C	С	С	С	417	360
122	0	0	Ċ	C	ō	Ċ	C	C	Ō	С	C	С	С	c	414	306
123	0	С	C	C	0	C	C	C	Ō	c	C	С	С	C	411	258
124	c	0	0	ō	C	C	c	c	ō	C	C	C	C	c	406.50	400.50
125	C	C	o	Ō	C	Ċ	C	C	Ō	c	C	C	c	c	403.50	366
126	C	o	C	o	C	C	C	C	Ō	c	c	C	C	C	400.50	337.50
127	C	c	Ċ	o	C	c	C	C	0	C	C	C	С	C	397.50	315
128	c	ō	ō	c	C	C	c	C	ō	c	C	C	C	c	394.50	298.50
129	C	C	o	C	C	C	c	C	Ō	C	Č	Ċ	C	c	391.50	288
* C = Clo	<u> </u>	l or	On	; 0		pen	L	off		L					<u></u>	<u> </u>
												CO	ntinu	ied —		

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TABLE 3-22. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 2.016 MHz CLOCK AND ROUND-UP METHOD (Contd)

Number							tor	twa	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Last Sector
130	С	0	С	С	С	С	С	С	0	С	С	С	С	n	388.50	283.50
131	C	С	С	C	С	С	C	С	0	C	С	C	С	C	385.50	285
132	С	0	0	0	0	0	0	0	С	C	C	С	C	С	382.50	292.50
133	C	С	О	0	. 0	0	0	0	С	C	С	C	С	С	379.50	306
134	С	0	С	0	0	0	0	0	С	C	С	C	С	С	376.50	325.50
135	C	<u>C</u>	<u>C</u>	0	0	0	0	0	<u></u>	C	С	С	C	С	373.50	351
136	0	0	0	C	0	0	0	0	С	C	С	c	С	С	372	180
137	0	С	0	C	0	0	0	0	С	C	С	C	С	C	369	216
138	0	0	C	<u> c</u>	0	0	0	0	С	C	С	С	C	С	366	258
139	0	С	С	C	0	0	0	0	С	C	С	C	С	С	363	306
140	0	0	0	0	С	0	0	0	С	C	С	C	С	С	360	360
141	С	0	0	0	C	0	0	0	С	C	C	С	С	С	358.50	210
142	С	C	0	0	C	0	0	0	С	C	С	С	С	С	355.50	274.50
143	C	0	С	0	С	0	0	0	С	C	С	C	С	С	352.50	345
144	0	C	С	0	<u>C</u>	0	0	0	С	C	C	С	С	С	351	207
145	0	0	0	C	С	.0	0	0	·C	C	С	C	С	C	348	288
146	С	0	0	C	С	0	0	0	С	C	С	C	С	С	346.50	157.50
147	C	C	0	C	<u>C</u>	0	0	0	С	C	С	С	С	С	343.50	249
148	0	0	С	C	С	0	0	0	С	C	С	C	С	С	342	126
149	0	С	С	C	С	0	0	0	С	C	C	C	С	C	339	228
150	0	0	0	0	0		0	0	C	C	C	C	C	С	336	336
151	C	0	0	0	0	С	0	0	С	C	С	C	С	С	334.50	225
152	0	С	0	0	0	С	0	0	C	C	С	C	С	С	333	117
153	0	0	<u>C</u>	0	0	C	0	0	C	C	С	C	С	С	330	240
154	C	0	С	0	0	С	0	0	С	C	С	C	С	С	328.50	139.50
155	C	С	С	0	0	С	0	0	С	C	С	C	C	С	325.50	273
156	0	0	0	C	_ 0	C	0	0	C	C	C	С	<u> </u>	С	324	180
157	C	0	0	C	0	С	0	0	С	C	С	C	С	С	322.50	90
158	С	С	0	C	0	С	0	0	С	C	С	C	С	С	319.50	238.50
159	0	0	<u>C</u>	C	0	<u>C</u>	0	0	<u>C</u>	C	C	С	C	С	318	156
160	0	C	С	C	0	С	0	0	С	C	С	С	C	С	315	315
161	С	С	С	C	0	С	0	0	С	C	С	C	С	С	313.50	240
162	0	0	0	0	C	C	0	0	С	C	С	С	С	С	312	168
163	C	0	0	0	С		0	0	C	C	С	c	С	С	310.50	99
164	C	С	0	0	С	С	0	0	С	C	С	С	С	С	307.50	277.50
165	0	0	C	10	C		0	0	C	C	<u>C</u>	С	C	С	306	216
166	C	0	_	0	С		0	0	С	C	С	C	С	С	304.50	157.50
167	0	C	С	0	С		0	0	С	C	С	c	С	С	303	102
168	0	0	0	C	С	С	0	0	С	C	С	C	С	С	300	300
* C = Clo	osed	l o	r On	; 0	= (Open	or	off	:	<u>L</u>						

TABLE 3-23. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD

4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	2 ³ C O O O C C C O O	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	2 ⁵ 0 c c c c c c o o o c	C O C O O	2 ⁷ C C C O C O O	0 0 C C C 0	2 ⁹	2 ¹⁰ C O C O C O	2 ¹¹ C C C O O C	0 C C O 0	2 ¹³ 0 0 0 c c c	Usable Sectors 12600 10080 8400 7200 6300 5600	in Runt Sector 0 0 0
5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0	C C C C O O O	0 0 0 0 0 0 0	C O O O O	0 C C O	0 0 0 0	0 C O C O	C C O O	C C O	0 C. C	10080 8400 7200 6300	0 0 0 0
6 7 8 9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0	0 0 0 0 0 0 C C	C O O C C C C C	C C C O O O	0 C C O C	0 0 0 0	C C C O	C C C C	C O C O	0 0	0 0	0 C. C	8400 7200 6300	0 0 0
7 8 9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0	0 0 0 0 0 0 0	C C C O O O	C C O O	C 0 0	C C O	C C C	0 C 0	0	0	C. C	7200 6300	0 0
8 9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0	0000000	0 0 0 C C 0 0	0 0 C C C C	C O O O	C O O	0 0 0	C 0	C C	с 0	0	0	c	6300	0
9 10 11 12 13 14 15 16 17 18 19 20	0 0 0 0 0 0 0	0 C 0 0 0 0 C	0000000	0 0 0 0 0	0 C C C C	0 0 0 0	0 C 0	0 0	0	С	0	- 1	_			-
10 11 12 13 14 15 16 17 18 19 20	0 C 0 0 0 0 0	0 0 0 0 0 0	000000	0 C C C O O	C C C C	0 0 0	C 0 0	0	0			C	0	י כ	5600	1 ^
11 12 13 14 15 16 17 18 19 20	C 0 0 0 0 0 0	C 0 0 0 0 0	00000	C C O O	C C C	0	0	0		0	\boldsymbol{c}					0
12 13 14 15 16 17 18 19 20	0 0 0 0 0 0	0 0 0 0 C 0	0 0 0 0	C C O O	C C	0	0	_				С	0	C	5040	0
13 14 15 16 17 18 19 20	0 0 0 0 0	0 0 0 0	0 0 0	C 0	C	0			0	C	C	C	0	C	4581	9
14 15 16 17 18 19 20	0 0 0 0 0	0 0 C 0	0 0	0	C			<u>C</u>	<u> </u>	C	<u></u>	C	0	C	4200	0
15 16 17 18 19 20	0 0 0 0	0 C 0	0	0			C	С	0	0	0	0	C	C	3876	12
16 17 18 19 20	0 0 0	C 0	0				C	C	C	0	0	0	C	C	3600	0
17 18 19 20	0 0	0	-		0	C	C	C	0	C	0	0	C	C C	3360 3150	0
18 19 20	0		C	0	C O	C	0 C	0	C	_	0 C	_	C	C	2964	12
19 20	0		0	C	С	0	0	0	C	0	C	0	C	C	2800	0
20		0	c	0	0	c	0	C	C	0	c	 	C	C	2652	12
	•	0	0	C	0	C	0	0	0	C	C	0	C	c	2520	0
21	0	0	0	0	0	C	0	C	0	C	С	0	C	c	2400	0
22	0	C	c	C	0	0	6	0	C	c	C	ŏ	C	C	2290	20
23	C	0	ŏ	0	C	C	c	o	c	c	C	0	c	c	2191	7
24	o	0	c	C	0	0	C	C	C	c	C	0	C	c	2100	0
25	0	0	ō	0	0	C	0	0	0	0	0	c	c	c	2016	0
26	ō	Ċ	c	C	Ö	C	c	ō	0	o	Ö	c	C	c	1938	12
27	ō	C	c	0	C	c	o	C	o	o	o	c	c	c	1866	18
28	0	ō	ō	C	C	C	c	C	0	0	0	c	C	c	1800	0
29	С	С	С	0	С	С	0	0	С	0	0	c	C	c	1737	27
30	0	0	0	0	С	С	С	0	С	0	0	С	С	С	1680	0
31	С	С	С	0	0	С	0	С	С	0	0	С	С	С	1625	25
32	С	0	0	С	С	0	С	С	С	0	0	c	С	С	1575	0
33	_ <u>C</u>	0	0	С	0	0	0	0	0	С	0	С	C	С	1527	9
34	0	C	С	0	С	С	0	0	0	С	0	С	С	С	1482	12
35	0	0	0	0	0	С	С	0	0	С	0	c	С	C	1440	0
36	0	0	0	С	0	0	0	C	0	С	0	C	C	С	1400	0
37	0	C	С	С	0	С	0	С	0	С	0	С	С	С	1362	6
38	0	С	0	0	С	0	C	С	0	С	0	C	С	C	1326	12
39	0	0	C	0	С	C	C	С	0	С	0	C	C	C	1292	12
40	0	0	С	0	С	0	0	0	С	С	0	C	С	C	1260	0
41	С	С	0	0	С	С	0	0	С	С	0	c	С	c	1229	11
42	0	0	0	0	C	0	С	0	C	С	0	С	С	С	1200	0
43	0	0	С	С	0	С	C	0	С	C	0	C	С	C	1172	4
44	С	С	С	0	0	0	0	С	С	С	0	С	С	C	1145	20
45	0	0	0	0	0	С	0	С	С	С	0	c	С	С	1120	0
* C = Clo	sed	or	On;	; 0	= 0	pen	or	off				Con	tinu	od		

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TABLE 3-23. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹	2 ¹²	2 ¹³	Usable Sectors	in Runt Sector
46	С	0	0	С	С	С	0	С	С	С	0	С	С	С	1095	30
47	0	0	0	0	С	0	C	С	С	C	0	С	С	С	1072	16
48	0	C	C	0	0	C	C	C	C	C	0	С	С	С	1050	0
49	0	0	С	C	С	С	C	С	С	C	0	С	C	С	1028	28
50	0	0	0	0	C	0	0	0	0	0	C	С	C	C	1008	0 12
<u>51</u> 52	C	0 C	C	0	<u>0</u>	C	0	0	0	0	C	C	C	C C	988 969	12
53	0	C	0	C	0	0	C	0	0	0	C	C	C	C	950	50
54	C	С	0	c	С	0	C	0	0	0	C	C	C	C	933	18
55	ō	0	c	c	<u></u>	C	C	0	0	0	C	С	C	C	916	20
56	o	0	C	C	C	C	c	0	o	0	c	c	C	C	900	0
57	o	0	Ċ	c	0	ō	o	C	o	0	c	C	C	c	884	12
58	0	0	C	C	C	0	0	C	0	0	C	C	C	C	868	56
59	0	С	0	С	0	C	0	С	Ō	0	C	С	С	С	854	14
60	0	О	0	c	С	С	0	С	0	0	С	С	С	С	840	0
61	0	С	С	0	0	0	С	С	0	0	С	С	С	С	826	14
62	0	0	С	0	С	0	С	С	0	0	С	С	С	С	812	56
63	0	0	0_	0	0	C	С	С	0	0	С	С	С	С	800	0
64	С	0	С	С	0	С	С	C	0	0	С	n	С	C	787	32
65	С	0	0	С	С	С	C	С	0	0	С	С	С	С	775	25
66	C	0	C	0	0	0	0	0	С	0	C	C	C	С	763	42
67	0	0	0	0	С	0	0	0	С	0	С	С	С	С	752	16
68	С	С	0	C	С	0	0	0	С	0	С	С	С	С	741	12
69	0	С	C	0	0	<u>C</u>	0	0	<u>C</u>	0	C	С	С	С	730	30
70	0	0	0	0	C	C	0	0	C	0	С	С	C	C	720	0
71	C	C	0	C	С	С	0	0	C	0	C	С	C	C	709	61
72 73	0	0	<u>C</u>	0	0	0	C	0	<u>C</u>	0	C	C	<u>C</u>	<u>C</u>	700	0
73 74	0	C	C C	C	0 C	0	C	0	C C	0	C	C	C	C	690 681	30 6
7 <u>4</u> 75	0	0	0	0	0	C	C	0	C	0	c	C	C	C	672	0
76	C	-	0	C	-	C	C	0	c	0	c	C	C	C	663	12
77	0	c	0	0	С	C	C	0	C	0	c	C	C	C	65 4	42
	0	C	0	C	C	C	C	o	c	0	C	C	C	C	646	12
79	c	c	0	0	0	0	0	C	C	0	C	С	C	C	637	77
80	o	C	Ō	C	Ō	Ō	o	Ċ	C	0	C	c	C	C	630	0
81	0	С	0	0	С	0	0	С	С	0	С	С	С	C_	622	18
82	0	С	0	С	С	0	0	С	С	0	С	С	С	С	614	52
83	С	0	0	0	0	С	0	С	С	0	С	С	С	С	607	19
84	0	0	0	С	0	С	0	С	С	0	С	С	С	С	600	0
85	0	0	0	0	C	С	0	C	C	0	С	С	С	С	592	80
86	0	С	С	0	С	С	0	С	С	0	С	С	C	С	586	4
87	С	0	С	C	С	С	0	С	С	0	С	С	С	С	579	27
* C = Clo	osed	or	On	; 0	= 0	pen	or	off		•,						
												Cor	itinu	ed —		

TABLE 3-23. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number of						Sec	tor	Swi	tch	es*					Bytes in Usable	Bytes
Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ^{1.}	212	2 2 13	Sectors	in Runt Sector
88	0	0	С	0	0	0	С	С	С	0	С	С	С	С	572	64
89	0	С	0	C	0	0	С	С	С	0	С	С	C	C	566	26
90	0	0	0	0	<u>C</u>	0	C	C	C	0	_ <u>C</u> _	C	С	С	560	0
91	C	С	С	0	С	0	C	С	C	0	С	С	С	C	553	77
92	C	0	С	C	С	0	C	С	С	0	С	С	С	С	547	76
93	С	C	0	0	0	С	С	С	C	0	С	С	С	С	541	87
94	0	0	0	C	0	С	C	С	С	0	С	С	С	C	536	16
95	0	С	С	C	0	С	C	С	С	0	С	С	С	C	530	50
96	C	C	0	0	C	С	C	С	С	0	С	С	С	С	525	0
97	С	0	0	C	С	С	C	С	С	0	С	С	С	C	519	57
98	0	С	С	C	С	С	С	С	С	0	С	С	С	c	514	28
99	С	С	0	0	0	0	0	0	0	C	C	C	С	C	509	9
100	0	0	0	C	0	0	0	0	0	С	С	С	C	С	504	0
101	C	0	С	C	0	0	0	0	0	C	С	C	С	c	499	1
102	0	С	0	0	С	0	0	0	0_	C	С	С	С	С	494	12
103	C	С	С	0	С	0	0	0	0	С	С	С	С	C	489	33
104	0	0	С	C	С	0	0	0	0	С	С	С	С	C	484	64
105	0	0	0	0	0	С	0	0	0_	С	С	С	С	С	480	0
106	С	0	С	0	0	С	0	0	0	С	С	С	С	С	475	50
107	C	0	0	C	0	С	0	0	0	С	C	С	С	c	471	3
108	0	С	С	С	0	С	0	0	0	С	С	С	С	С	466	72
109	0	С	0	0	С	С	0	0	0	С	С	С	С	C	462	42
110	0	С	С	0	С	С	0	0	0	С	С	С	С	C	458	20
111	0	C	0	C	C	С	0	0	0	С	С	С	С	С	454	6
112	0	С	С	C	С	С	0	0	0	С	С	С	С	C	450	0
113	0	С	0	0	0	0	С	0	0	C	С	С	С	C	446	2
114	0	C	C	0	0	0	С	0	0	C	С	С	С	C	442	12
115	0	С	0	C	0	0	С	0	0	С	C	С	С	C	438	30
116	0	С	С	C	0	0	С	0	0	С	С	C	С	C	434	56
117	0	_ <u>C</u>	0	0	_ <u>C</u>	0	С	0	0	С	C	С	С	С	430	90
118	С	0	С	0	С	0	С	0	0	С	С	С	С	C	427	14
119	С	0	0	C	С	0	C	0	0	С	С	C	С	C	423	63
120	0	0	_C	C	_C	0	С	0	0	С	C	С	С	С	420	00
121	0	0	0	0	0	С	С	0	0	С	C	С	С	C	416	64
122	С	С	0	0	0	С	С	0	0	С	С	С	С	C	413	14
123	С	С	С	0	0	C	C	0	0	С	С	С	С	С	409	93
124	0	С	0	С	0	С	C	0	0	C	С	C	С	С	406	56
125	С	0	С	C	0	С	С	0	0	С	С	С	C	c	403	25
126	0	0	0	0	С	C	C	0	0	С	С	С	С	C	400	0
127	0	0	С	0	С	С	С	0	0	С	С	С	С	С	396	108
128	С	C	С	0	С	С	С	0	0	С	С	С	С	С	393	96
129	0	С	0	C	С	С	С	0	0	С	С	С	С	C	390	90
* C = Clo	osed	or	On	; 0	= C	pen	or	Off				Cor	ntinu	ıed —		

TABLE 3-23. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-DOWN METHOD (Contd)

Number of						Sec	tor	Swi	.tch	es*					Bytes in Usable	Bytes in Runt
Sectors	20	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Sectors	Sector
130	С	0	С	С	С	С	С	0	0	С	С	С	С	С	387	90
131	0	0	0	0	0	0	0	С	0	С	C	С	С	С	384	96
132	С	С	0	0	0	0	0	С	0	С	С	С	С	С	381	108
133	0	C	С	0	0	0	0	С	0	C	С	C	С	С	378	126
134	0	0	0	C	0	0	0	С	0	C	С	С	С	С	376	16
135	C	C	0	С	0	0	0	С	0	С	С	С	С	С	373	45
136	0	С	С	С	0	0	0	С	0	С	С	C	С	C	370	80
137	C	0	0	0	С	0	0	С	0	C	С	C	С	С	367	121
138	C	<u>C</u>	0	0	С	0	0	C	0	C	С	_ <u>C</u> _	С	С	365	30
139	0	С	С	0	С	0	0	С	0	С	С	С	С	С	362	82
140	0	0	0	C	С	0	0	С	0	С	C	C	С	C	360	0
141	C	<u>C</u>	0	C	_ <u>c</u> _	0	0	<u> </u>	<u> </u>	C	C	<u></u>	C		357	63
142	0	C	С	C	C	0	0	C	0	C	C	C	C	C	354	132
143	0	0	0	0	0	С	0	C	0	C	C	C	C	C	352	64
144	0	_ <u>c</u>	<u> </u>	0	0	_ <u>C</u> _	0	<u></u>	<u> </u>	C	<u>c</u>	<u></u>	C	C	350	0
145	C	0	C	0	0	C	0	С	0	C	C	C	C	C	347	85
146	C	С	С	0	0	С	0	C	0	C	C	C	C	C	345	30
147	0	<u>C</u>	0	C	0	<u>C</u>	0	_ <u>C</u> _	<u> </u>	C	C	<u></u>	C	C	342	126
148 149	0	0	C C	C	0	C C	0	C	0	C	C C	C C	C	C	340 338	80 38
150	0	0	0	0	O C	С	0	C	0	C	С	C	C	C	336	0
151	C	$\frac{c}{c}$	-	0	_ C	c	0	c	0	C	C	$\frac{c}{c}$	C	C	333	117
152	C	0	C	0	C	c	0	С	0	c	C	C	C	C	331	88
153	C	c	C	0	C	C	0	C	0	c	C	C	C	C	329	63
154	c	0	0	c	c	c	0	C	0	C	c	c	C	C	327	42
155	C	C	0	c	C	Ċ	0	C	o	c	C	c	C	C	325	25
156	C	0	C	C	C	C	0	C	o	C	C	C	C	C	323	12
157	C	C	C	C	C	C	0	C	0	C	C	C	C	С	321	3
158	0	C	Ō	0	Ō	0	C	Ċ	0	c	C	C	C	С	318	156
159	0	0	С	0	0	0	С	С	0	С	С	С	С	c	316	156
160	С	0	С	0	0	0	C	С	0	С	С	С	С	С	315	0
161	C	С	С	0	0	0	С	С	0	С	С	С	С	С	313	7
162	С	0	0	С	0	0	С	С	0	С	С	С	С	С	311	18
163	С	С	0	С	0	0	С	С	0	С	С	С	С	С	309	33
164	С	0	С	С	0	0	С	С	0	С	С	С	С	С	307	52
165	C	С	С	С	0	0	C	С	0	С	С	С	С	С	305	75
166	C	0	0	0	C	0	С	С	0	С	С	С	С	С	303	102
167	C	С	0	0	С	О	C	С	0	С	С	С	С	С	301	133
168	0	0	С	0	С	0	С	С	0	C	С	С	С	С	300	0
* C = Clo	osed	l or	On	; 0	= C	pen	or	off	:	l		·			_	

TABLE 3-24. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-UP METHOD

Number						Sec	tor			es*					Bytes in	Bytes
of Sectors	20	21	22	23	24	2 ⁵	26	2 ⁷	28	29	2 ¹⁰	21:	L ₂ 12	2 ¹³	Early Sectors	in Last Sector
4	0	0	0	С	0	0	С	С	0	С	С	С	0	0	12600	12600
5	0	0	0	0	0	С	0	С	0	0	О	C	С	0	10080	10080
6	0	0	0	0	C	С	0	0	C	C	C	С	С	0	8400	8400
7	0	0	0	0	0	С	C	С	C	C	0	0	0	С	7200	7200
8	0	О	С	0	0	С	C	0	С	C	С	0	0	С	6300	6300
9	0	0	0	0	0	C	0	0	0	C	0	С	0	С	5600	5600
10	0	0	0	0	С	0	C	0	0	0	С	С	0	C	5040	5040
11	0	С	0	C	С	0	0	0	0	C	С	С	0	C	4582	4580
12	0	0	0	C	C	0	0	<u>C</u>	C	C	С	С	0	C	4200	4200
13	C	С	0	C	С	0	C	С	0	0	0	0	С	C	3877	3876
14	0	0	0	0	C	С	C	С	С	0	О	0	С	C	3600	3600
15	0	0	0	0	0	С	C	C	0	С	0	0	С	C	3360	3360
16	0	С	0	0	С	С	0	С	С	C	0	0	С	c	3150	3150
17	C	С	0	C	0	С	C	0	0	0	С	0	С	C	2965	2960
18	0	0	0	0	C	0	0	0	C	0	C	0	C	C	2800	2800
19	C	С	0	0	0	С	0	С	С	0	С	0	С	С	2653	2646
20	0	0	0	C	0	С	0	0	0	C	С	0	С	С	2520	2520
21	0	0	0	0	0	C	0	C	0	C	C	0	С	_C	2400	2400
22	C	0	С	С	0	0	0	0	С	C	С	0	С	C	2291	2289
23	0	0	0	0	С	С	C	0	С	C	С	0	С	С	2192	2176
24	0	0	<u>_C</u>	C	0	0	C	<u>_C</u>	С	C	C	0	C	C	2100	2100
25	0	0	C ·	0	0	С	0	0	0	0	О	С	С	С	2016	2016
26	C	0	С	C	0	С	C	О	0	0	0	С	С	С	1939	1925
27	C	0	C	0	<u>C</u>	C	0	C	0	0	0	С	С	C	1867	1858
28	0	0	0	C	С	C	C	С	0	0	О	С	С	C	1800	1800
29	0	С	С	0	С	С	0	0	С	0	0	С	С	C	1738	1736
30	0	0	0	0	<u>C</u>	C	C	0	C	0	0	C	С	_ <u>C</u>	1680	1680
31	0	С	С	0	0	C	0	С	С	0	0	С	С	С	1626	1620
32	C	0	0	C	С	0	C	С	С	0	0	С	С	C	1575	1575
33	0	0	_0_	C	0	_0_	0	_0_	_0_	C	0	C	C	_C	1528	1504
34	C	0	C	0	C	C	0	0	0	C	0	C	C	C	1483	1461
35	0	0	0	0	0	C	C	0	0	C	0	C	C	C	1440	1440
36	0	0	0	L C	0	0	0	C	<u> </u>	C	0	<u> </u>	C_	<u>C</u>	1400	1400
37	C	0	C	C	0	C	0	C	0	C	0	C	C	С	1363	1332
38	C	0	0	0	C	0	C	C	0	C	0	C	C	C	1327	1301
39	C	<u></u>	<u> </u>	<u> </u>	<u>C</u>	<u> </u>	C	<u>C</u>	<u> </u>	C	0	<u></u>	C	C	1293	1266
40	0	0	C	0	C	0	0	0	C	C	0	C	C	C	1260	1260
41	0	С	0	0	С	С	0	0	С	С	0	С	С	C	1230	1200
42	10	0	0	<u> </u>	C	0	C	0	<u></u>	C	0	<u></u>	C	C	1200	1200
43	C	C	0	C	0	C	C	0	C	C	0	C	C	C	1173	1134
44	0	С	С	0	0	0	0	С	С	С	0	С	С	С	1146	1122
45	0	0	0	0	0	С	0	С	С	C	0	С	C	С	1120	1120
* C = Cl	osed	or	On	; 0	= C	pen	or	off	•				- 4. 9 —			
												CO	ntinu	iea —		

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TABLE 3-24. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor		tch	es*					Bytes in	Bytes
of Sectors	20	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	211	l ₂ 12	2 ¹³	Early Sectors	in Last Sector
46	0	0	0	С	С	С	0	С	С	С	0	С	С	С	1096	1080
47	С	С	С	C	0	0	C	С	С	C	0	С	С	С	1073	1042
48	0	С	С	0	0	С	C	C	C	C	0	С	C	C.	1050	1050
49	С	С	0	C	С	С	C	С	C	C	0	С	С	С	1029	1008
50	0	0	0	0	С	0	0	0	0	0	С	С	С	С	1008	1008
51	С	C	0	0	0	С	0	0	0	0	C	С	C	С	989	950
52	0	С	С	0	С	С	0	0	0	0	С	С	С	С	970	930
53	С	0	0	C	0	0	С	0	0	0	С	С	C	С	951	948
54	0	<u></u>	0	C	C	0	C	0	0	0	<u> </u>	<u>C</u>	C	C	934	898
55 56	C	C	0	C	0	C	C	0	0	0	C	С	C	C	917	882
56 57	0	0	C	C	C	C	C	0	0	0	C	С	C	C	900	900
<u>57</u> 58	C	<u>C</u>	0	C	0	<u> </u>	0	<u>C</u>	0	0	C	C	C	C	885 869	840 867
58 59	C	C 0	0	C	C 0	0	0	C	0	0	C	C C	C	C	869 855	810
60	0	0	0	C	C	C	0	C	0	0	C	C	C	C	840	840
61	C	0	c	0	0	0	C	C	0	0	c	C	C	C	827	780
62	C	C	0	0	С	0	C	C	0	0	C	C	C	C	813	807
63	0	0	0	0	0	C	C	С	0	0	c	C	C	C	800	800
64	0	0	C	C	0	C	C	C	0	0	C	C	C	C	788	756
65	0	0	0	c	C	C	C	C	0	0	C	C	C	C	776	736
66	0	0	C	١٥	0	0	0	0	C	0	C	C	c	C	764	740
67	C	c	c	c	0	0	0	0	C	0	C	C	С	C	753	702
68	0	C	Ö	l c	C	Ö	١٥	0	C	l ŏ	С	C	c	C	742	686
69	C	0	C	١٥	0	c	١٥	0	C	0	C	C	c	C	731	692
70	0	- 0	-	0	c	c	0	0	Ċ	ō	c	C	c	C	720	720
71	o	c	o	C	C	Č	١٥	o	C	0	c	C	c	C	710	700
72	0	0	C	١٥	0	ō	c	o	C	0	C	c	c	C	700	700
73	C	0	C	c	0	ō	c	ō	C	o	C	C	c	С	691	648
74	٥	C	Ċ	0	C	ō	c	o	Ċ	١٥	C	C	c	C	682	614
75	0	Ō	0	0	O	C	C	O	C	٥	Ċ	Ċ	С	С	672	672
76	0	0	0	С	0	С	С	0	С	0	C	С	С	С	664	600
77	С	0	0	0	С	С	С	0	С	0	С	С	С	С	655	620
78	С	0	0	С	С	C	С	0	С	0	С	С	С	С	647	581
79	0	С	0	0	0	0	0	С	С	0	С	С	С	С	638	636
80	0	С	0	C	0	0	0	С	С	0	С	С	С	С	630	630
81	С	0	0	0	С	0	0	С	С	0	С	С	С	С	623	560
82	С	0	0	С	С	0	0	С	С	0	С	С	С	С	615	585
83	0	0	0	0	0	С	0	С	С	0	С	С	С	C	608	544
84	0	0	0	С	0	С	0	С	С	0	C	С	С	C	600	600
85	С	С	С	С	0	C	0	С	С	0	C	С	С	С	593	588
86	C	0	С	0	С	C	0	С	С	0	С	С	С	С	587	505
87	0	0	С	C	С	С	0	С	С	0	С	С	С	С	580	520
* C = Clo	osed	l or	On	; 0	= (pen	or	off	•	•		Col	ntinu	ied —		

TABLE 3-24. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec			.tch	es*					Bytes in	Bytes
of Sectors	2 ⁰	21	22	2 ³	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	211	L 2 ¹²	2 ¹³	Early Sectors	in Last Sector
88	С	C	0	0	0	0	С	С	С	0	С	С	С	C	573	549
89	С	0	0	C	0	0	С	С	С	0.	C	С	С	С	567	504
90	0	0	0	0	C	0	С	С	С	0	С	С	С	С	560	560
91	0	С	С	0	С	0	С	C	С	0	С	С	C	С	554	540
92	0	0	С	C	С	0	С	С	С	0	C	С	С	С	548	532
93	0	_C	0	0	0	С	С	С	C	0	С	С	С	С	542	536
94	C	С	С	0	0	С	C	С	С	0	С	С	С	С	537	459
95	C	0	С	C	0	С	C	С	C	0	С	С	С	С	531	486
96	C	C	0	0	C	С	C	С	C	0	С	С	С	С	525	525
97	0	0	0	C	С	С	C	С	С	0	С	С	С	С	520	480
98	C	0	С	C	С	С	C	С	С	0	С	С	С	С	515	445
99	0	С	0	0	0	0	0	0	0	C	С	С	С	С	510	420
100	0	0	0	C	0	0	0	0	0	С	С	С	С	С	504	504
101	0	0	С	C	0	0	0	0	0	С	С	С	С	С	500	400
102	C	0	0	0	C	0	0	0	0	С	С	С	С	С	495	405
103	0	С	С	0	С	0	0	0	0	С	С	С	С	C	490	420
104	C	С	0	C	С	0	0	0	0	С	C	С	С	С	485	445
105	0	0	0	0	0	С	0	0	0	С	С	С	С	С	480	480
106	0	0	С	0	0	C	0	0	0	С	С	C	C	C	476	420
107	0	0	0	C	0	С	0	0	0	С	С	С	С	С	472	368
108	C	0	C	C	0	С	0	0	0	C	С	С	С	С	467	431
109	C	0	0	0	С	С	0	0	0	C	С	C	С	C	463	396
110	C	О	С	0	С	С	0	0	0	С	С	С	С	С	459	369
111	C	0	0	C	C	C	0	0	0	C	С	С	С	С	455	350
112	0	С	С	C	С	С	0	0	0	С	С	С	С	С	450	450
113	C	0	0	0	0	0	С	0	0	С	С	С	С	C	447	336
114	C	0	C	0	0	0	C	<u> </u>	0	С	C	С	С	C	443	341
115	С	0	0	C	0	0	С	0	0	С	С	С	С	C	439	354
116	C	0	С	C	0	0	С	0	0	С	С	С	С	C	435	375
117	C	_0	0	0	C	0	C	<u> </u>		С	C	C	С	С	431	404
118	0	0	С	0	С	0	C	0	0	С	С	С	С	C	428	324
119	0	0	0	С	С	0	С	0	0	С	С	С	С	С	424	368
120	0	0	C	C	<u>C</u>	0	C	0	0	С	C	С	С	C	420	420
121	C	C	C	C	С	0	C	0	0	C	C	С	С	C	417	360
122	0	C	0	0	0	C	С	0	0	C	C	С	C	C	414	306
123	0	_ <u>c</u>	<u>C</u>	0	0	<u> </u>	C	0	0	C	<u>c</u>	С	C	C	410	380
124	C	0	0	C	0	C	C	0	0	C	C	С	C	С	407	339
125	0	0	C	C	0	C	C	0	0	C	C	С	C	C	404	304
126	0	0	0	0	<u> </u>	<u> </u>	C	0	0	C	C	C	С	С	400	400
127	C	C	0	0	C	C	C	0	0	C	C	С	C	С	397	378
128	0	С	С	0	С	С	С	0	0	С	С	С	С	С	394	362
129	C	0	0	C	С	С	С	0	0	С	С	С	С	С	391	352
* C = Clo	osed	or	On	; 0	= C	pen	or	off				Cor	ntinu	od		
												COI	i L TIIU	eu		

TABLE 3-24. SECTOR SELECT SWITCH SETTINGS -- 1236 MB DRIVES USING 3.024 MHz (BYTE) CLOCK AND ROUND-UP METHOD (Contd)

Number						Sec	tor	Swi	tch	es*					Bytes in	Bytes
of Sectors	2 ⁰	21	22	23	24	2 ⁵	2 ⁶	2 ⁷	28	29	2 ¹⁰	2 ¹¹	2 ¹²	2 ¹³	Early Sectors	in Las Sector
130	0	0	С	С	С	С	С	0	0	С	С	С	С	С	388	348
131	C	С	С	C	С	С	C	О	0	C	С	С	С	С	385	350
132	0	C	0	0	_0	0	0	C	0	C	_ <u>C</u>	_C	C	С	382	358
133	C	0	С	0	0	0	0	С	0	C	С	C	С	С	379	372
134	C	С	С	0	0	0	0	С	0	C	С	С	С	С	377	259
135	0	<u>C</u>	0	C	0	_0	0	С	0	C	C	C	<u>C</u>	С	374	284
136	C	0	С	C	0	0	0	C	0	C	C	C	C	C	371	315
137	0	0	0	0	C	0	0	C	0	C	C	C	C	C	368	352
138	0	_ <u>C</u>	<u> </u>	10	C	0	0	_ <u>C</u>	<u> </u>	C	<u> </u>	<u>_</u>	<u> </u>	C	366	258
139	C	0	С	0	C	0	0	C	0	C	C	C	С	C	363	306
140	0	0	0	C	C	0	0	С	0	C	C	C	С	С	360	360
141 142	0	_ <u>C</u>	0	C	C C	_0_	0	_ <u>C</u>	0	C	_ <u>C</u> _	<u> </u>	C	C	358 355	280
142	C	0	C	C	C	0	0	C	0	C	C	C	C	C	353 353	345 274
143	0	C	0	0	0	С	0	C	0	C	C	c	C	C	353 350	350
145	0	0	c	1 6	0	c	0	<u> </u>	0	c	C	c	C	C	348	288
146	0	C	C	0	0	c	0	C	o	C	C	c	C	C	346	230
147	C	o	Ö	C	o	c	0	C	o	c	C	c	C	c	343	322
148	c	C	0	c	0	C	0	c	0	c	c	č	C	C	341	273
149	C	0	C	C	0	c	0	C	0	C	C	c	c	C	339	228
150	0	0	ō	0	C	C	١٥	C	ō	c	C	c	C	C	336	336
151	0	C	<u>-</u>	0	C	C	o	C	0	c	C	c	C	C	334	300
152	0	0	Ċ	0	C	Ċ	٥	C	o	C	C	c	C	c	332	268
153	0	C	C	١٥	C	C	o	C	ō	C	C	С	C	C	330	240
154	0	0	0	C	C	C	0	C	ō	c	C	С	С	С	328	216
155	0	C	0	C	C	C	0	С	Õ	C	C	c	C	C	326	196
156	0	0	С	c	С	С	0	С	0	C	С	c	С	С	324	180
157	0	C	С	С	С	С	0	С	0	C	C	С	C	С	322	168
158	C	0	0	0	0	0	С	С	0	c	С	c	С	С	319	317
159	С	C	0	0	0	0	С	С	0	C	С	С	С	С	317	314
160	С	0	С	0	0	0	С	С	0	С	С	С	С	С	315	315
161	0	С	С	0	0	0	С	С	0	C	С	c	С	С	314	160
162	0	0	0	C	0	0	С	С	0	c	С	С	С	С	312	168
163	0	С	0	С	0	0	С	С	0	С	С	С	С	C	310	180
164	0	0	С	C	0	0	С	С	0	C	С	c	С	С	308	196
165	0	C	С	C	0	0	С	C	0	C	С	С	С	С	306	216
166	0	0	0	0	С	0	C	С	0	C	С	С	С	C	304	240
167	0	С	0	0	С	0	С	С	0	C	С	С	С	С	302	268
168	0	0	С	0	С	0	C	С	0	C	С	c	С	С	300	300
166 167	0 0 0	0 C 0	0 C	0 0 0	C C C	0 0 0	C C	C C	0 0	C	C	C C	C C	O C	304 302	

CHECKOUT

After completing installation of the drive, follow the sequence outlined below for initial startup in either remote or local. Refer to section 2 of this manual for information about operation of the drive.

CHECKOUT IN REMOTE MODE

- Set On/Standby switch at rear of power supply in On (1) position, and observe that power supply cooling fan operates.
- 2. Set drive logical address. Logical address for each drive in a system must have a unique number.
- Press START switch on drives with operator panel or status/control panel.

The power on sequence continues when power sequence Pick or Hold (ground) signal is available from controller. With R/L (Remote/Local) switch in Remote position, power on sequence to each drive is delayed. Length of delay is determined by the logical address number used, in increments of 5 seconds.

For example:

Logical Address 0 = 0 second delay Logical Address 7 = 35 second delay

- 4. If drive has an operator panel or a status/control panel, observe (with delay complete) that the Ready indicator (located in START switch) flashes rapidly, indicating that power on is in progress.
- 5. Observe that Ready indicator on operator panel or status/control panel lights steadily within 90 seconds. This indicates that drive motor is up to speed and that heads are at track 0. If drive has no operator panel or status/control panel, observe that RDY Maintenance LED (on control board) lights to indicate that drive is ready.

If all of these events occurred, the drive is now ready for online operation. If any did not occur, a problem exists in the drive. Then refer to troubleshooting information either in appendix A of this manual or in the maintenance manual.

CHECKOUT IN LOCAL MODE

NOTE

For drives without operator panel or status/control panel, power on sequence begins when the power supply On/Standby switch is placed in On (1) position.

- Set On/Standby switch at rear of power supply in On (1) position, and observe that power supply cooling fan operates.
- 2. Set drive logical address. Logical address for each drive in a system must have a unique number.
- Press START switch on drives with operator panel or status/control panel.
- 4. If drive has an operator panel or a status/control panel, observe that Ready indicator (located in START switch) flashes rapidly, indicating that power on is in progress.
- 5. Observe that Ready indicator on operator panel or status/control panel lights steadily within 90 seconds. This indicates that drive motor is up to speed and that heads are at track 0. If drive has no operator panel or status/control panel, observe that RDY Maintenance LED (on control board) lights to indicate that drive is ready.

If all of these events occurred, the drive is now ready for online operation. If any did not occur, a problem exists in the drive. Then refer to troubleshooting information either in appendix A of this manual or in the maintenance manual.

Appendix A Diagnostic Testing and Status Code Summary

CAUTION

When servicing the drive, observe all precautions listed under Electrostatic Discharge Protection in section 3 of this manual. Failure to observe these precautions can result in serious damage to electronic assemblies.

INTRODUCTION

This appendix presents information on interpreting the information displayed on the status/control panel and running offline diagnostic tests. More complete troubleshooting information can be found in the maintenance manual. You should be familiar with the contents of that manual before doing any troubleshooting.

For trouble analysis, a status/control panel is needed. If the drive installation does not include a status/control panel, we advise that you connect one to the drive during troubleshooting. Refer in the parts data manual for part numbers of the status/control panel and its interconnect cable. Refer to section 3 of this manual for instructions on connecting the panel to the drive.

The following types of troubleshooting information are presented in this appendix:

- Power On Test -- Describes drive self tests that occur when dc power is applied to the drive
- Diagnostic Testing -- Describes how to use diagnostic testing to isolate drive malfunctions
- Drive Status Codes -- Provides information on correcting problems associated with drive power on/power off and with servo operation

POWER ON TEST

When dc power is applied to the drive, the Control MPU performs a series of self-tests. During these tests the Address, Ready, FAULT, and WRITE PROTECT lights on the operator panel or status/control panel will be lit. After approximately four seconds, the FAULT light will go off, indicating successful completion of the self-tests. If the Ready and FAULT lights remain on constantly, it indicates the drive failed the power on self-test. The Address lights will then indicate which self-test routine the drive failed to complete. Refer to table A-1 to determine the test that failed and what action to take.

In addition to the individual tests listed in table A-1, the Control MPU attempts to communicate with the status/control panel. If this communication is unsuccessful, the Control MPU displays the message PANEL FAILURE on the LCD of the panel.

TABLE A-1. POWER ON TEST FAILURE

Add	ress	Lig	hts*	Hex Code	Test Failed** & Actions
8	4	2	1		
•	•	•	• .	F	RAM Test
					Action: 1. Replace control board.
•	•	•	o	E	ROM Test
					Action: 1. Replace control board.
			_	T.	I/O Chin Mont
•	•	0	•	D	I/O Chip Test
					Actions: 1. Replace I/O board. 2. Replace control board.
•	•	0	0	С	Peripheral Chip (Test 1)
					Action: 1. Replace control board.
		_		n	Danishanal Chin (Mast 2)
•	O	•	•	В	Peripheral Chip (Test 2)
					Action: 1. Replace control board.
•	0	•	0	A	Motor MPU Test
					Action: 1. Replace control board.

^{*} Darkened circles indicate light is On.

^{**}Ready and FAULT lights remain on constantly, indicating drive failed four second power on self test.

DIAGNOSTIC TESTING

The drive's offline diagnostics are initiated and monitored via the keyboard and LCD of the status/control panel. Figure A-1 shows the placement of switches and indicators on both the status/control panel and the operator panel. Table A-2 describes the maintenance features of the status/control panel.

TEST SELECTION PROCEDURE

To run offline diagnostic tests on the drive, place the drive in diagnostic mode and select the tests desired. To do this:

- Press the DIAG MODE switch to enter diagnostic mode and observe that the LCD reads DIAG TEST XX.
- Enter two hexadecimal characters on the keyboard and press EXEC switch to select the first test.

Directions for running the individual diagnostic tests appear later in this section. When the EXEC switch is pressed one more time, the test ends and the LCD again reads DIAG TEST XX. At this point you have two choices:

- Enter two hexadecimal characters on the keyboard and press EXEC switch to select another test, or
- Press the DIAG MODE switch to leave diagnostic mode and observe that the LCD displays drive operating status.

TABLE A-2. MAINTENANCE SWITCHES AND INDICATORS

Switch or Indicator	Function				
s	STATUS/CONTROL PANEL ONLY				
Diagnostic Keyboard	Used to enter diagnostic tests and parameters.				
Liquid Crystal Display	Displays drive status, faults, and current cylinder address.				
DIAG MODE (Diagnostic Mode) Switch	Places drive in diagnostic mode and disables the I/O.				
SPACE and BACK SPACE Switches	Used to enter the desired diagnostic tests.				
EXEC (Execute) Switch	Starts and stops diagnostic tests.				

STATUS/CONTROL PANEL

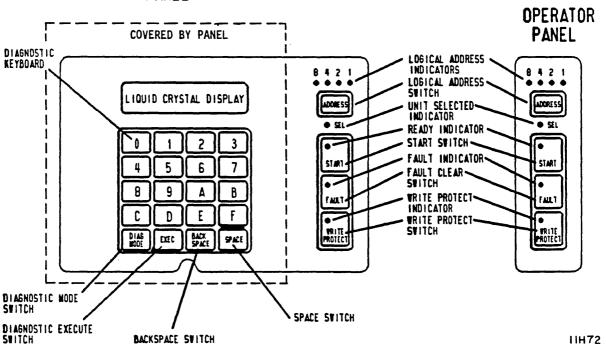


Figure A-1. Status/Control and Operator Panels

TEST DESCRIPTIONS

Test 00 -- Display Drive Operating Status Log

This test displays the 8 most recently generated drive status codes. After test selection, the display provides a hexadecimal status code from the internal log. This code is preceded by a character (0-7) and a colon, indicating the position of the status code in the status log. To execute test 00, perform the following steps:

- Enter Test 00, then press EXEC switch. The LCD will display DRIVE LOG: 0:XX where the number 0 indicates the position in the log and XX represents the status code entered in that position.
- 2. Press SPACE switch repeatedly until you see a code preceded by an asterisk (for example, DRIVE LOG: *5:XX). The asterisk identifies that code as the most recent entry in the status log.
- Use the SPACE switch to step through from the 8th most recent to the most recent status.
- 4. Press EXEC switch to end the test and return to test selection.

Test 01 -- Display Fault Log

This test displays the 8 most recently stored fault codes. After test selection, a hexadecimal fault code will be displayed. This code is preceded by a character (0-7) and a colon, indicating the position of the fault code in the fault log. To execute test Ol, perform the following steps:

- Enter Test 01, then press EXEC switch. The LCD will display FAULT LOG: 0:XX where the number 0 indicates the position in the log and XX represents the fault code entered in that position.
- 2. Press SPACE switch repeatedly until you see a code preceded by an asterisk (for example, FAULT LOG: *5:XX). The asterisk identifies that code as the most recent entry in the fault log.
- 3. Use the SPACE switch to step through from the 8th most recent to the most recent fault.
- 4. If more than one fault occurs simultaneously (multiple faults), more than one bit in the fault code will be set. Check the bit-mapped locations of each fault by referring to the following list:

<u>Bit</u>		<u>Definition</u>
0	(LSB)	Read•Write Fault (01)
1		(Read+Write) •Off Cylinder Fault (02)
2		First Seek Fault (04)
3		Write Fault (08)
4		Write•Write Protected Fault (10)
5		Head Select Fault (20)
6		Voltage Fault (40)
7	(MSB)	Not Used (80)

5. Press EXEC switch to end the test and return to test selection.

Test 04 -- Calculate Three Most Likely Field Replaceable Units

NOTE

Do not execute Tests 05, 06, or 07 prior to running Test 04.

This test uses the fault status and the drive operating status history (Tests 00 and 01) to predict the most likely cause of drive failure. Table A-3 lists the individual codes and their corresponding replacement part. To execute Test 04, perform the following steps:

- Enter Test 04, then press EXEC switch. The LCD will display FRUS: XX XX. Upon test completion, the three field replaceable units will be displayed, with the first hexadecimal code being the most likely cause of the failure.
- 2. Press EXEC switch to end the test and return to test selection.

TABLE A-3. CODING OF FIELD REPLACEABLE UNITS

Hex Display	Field Replaceable Unit
01	Control Board
02	Module
03	Power Supply
04	I/O Board
05	Control Board
06	Module

Test 05 -- Servo Test

This test clears both the drive status log and fault log. Because Test 04 relies on status history that would be cleared by Test 05, you might want to run Test 04 before running Test 05. Test 05 automatically performs several types of seek operations. They are as follows:

<u>Operation</u>	Number Of Times Executed
RTZ	1
l Track Seek	16
RTZ	1
Partial Servo Recalibrate	1
RTZ	1
Maximum Length Seek	16
RTZ	1

Execution stops when an error is detected or the test completes. To execute Test 05, perform the following steps:

- Enter Test 05, then press EXEC switch. Upon successful completion of the test, the LCD will display OK, CYL: 000. If an error occurs, the LCD will display SERVO ERROR: XX. Drive status codes (servo errors) are defined in table A-4 later in this section.
- 2. Press EXEC switch to end the test and return to test selection.

Test 06 -- Clear Drive Operating Status Log

This test clears the drive status log resident in program RAM. Because Test O4 relies on status history that would be cleared by Test O6, you might want to run Test O4 before running Test O6. To execute Test O6, perform the following steps:

- Enter Test 06 and press EXEC switch. The LCD will display DRIVE LOG CLEAR.
- 2. Press EXEC switch to end the test and return to test selection.

Test 07 -- Clear Fault Log

This test clears the fault log. Because Test 04 relies on status history that would be cleared by Test 07, you might want to run Test 04 before running Test 07. To execute Test 07, perform the following steps:

- Enter Test 07 and press EXEC switch. The LCD will display FAULT LOG CLEAR.
- Press EXEC switch to end the test and return to test selection.

Test 08 -- Direct Or Continuous Seeks

This test performs direct or continuous seeks between cylinder O and the desired cylinder address. Operation stops if an error occurs or if the EXEC switch is pressed. To execute Test O8, perform the following steps:

- 1. Enter Test 08 and press EXEC switch.
- 2. The display HEX CYL XXX asks you to supply a valid destination address (between 0 and 662 inclusive on 741 MB, 1123 MB, and 1236 MB drives; between 0 and 564 on 851 MB drives). Enter three characters and press SPACE switch.

- 3. The display DIR OR CONT? D/C asks you to select either DIRect (D) or CONTinuous (C) seeks. Enter either C or D to start test.
- 4. If D was entered and the direct seek was successful, the LCD will display OK, CYL: XXX, where XXX is the destination address previously entered. Press EXEC switch to end the test and return to test selection.
- 5. If either D or C was entered and an error occurs, the LCD will display SERVO ERROR: XX. Drive status codes (servo errors) are defined in table A-4 later in this section. By running Test 05 or Test 0E, you can clear the seek error.
- 6. If C was entered, press EXEC switch to end the test and return to test selection.

Test 09 -- Random Seek

This test performs random seeks between cylinder 0 and the maximum cylinder address (0-1634 on 741 MB, 1123 MB, and 1236 MB drives; 0-1380 on 851 MB drives). Operation stops if an error occurs or if the EXEC switch is pressed. To execute Test 09, perform the following steps:

- Enter Test 09 and press EXEC switch.
- The LCD will display OK, CYL: XXX if execution was successful.
- 3. If an error occurs during the test, the LCD will display SERVO ERROR: XX. Drive status codes (servo errors) are defined in table A-4 later in this section.
- 4. Press EXEC switch to end the test and return to test selection.

Test OC -- Display EPROM Part Number

This test displays the 8-digit part number of the control microprocessor EPROM. To execute Test OC, perform the following steps:

- Enter Test OC and press EXEC switch. The LCD will display the 8-digit part number of the EPROM (for example, EPROM# = 12345678).
- 2. Press EXEC switch to end the test and return to test selection.

Test OE -- Return To Zero

This test initiates a return to zero command. To execute Test OE, perform the following steps:

- 1. Enter Test OE and press EXEC switch. The LCD will display OK, CYL: 000.
- 2. If an error occurs during the test, the LCD will display SERVO ERROR: XX. Drive status codes (servo errors) are defined in table A-4 later in this section.
- Press EXEC switch to end the test and return to test selection.

DRIVE STATUS CODES

Whenever the drive is in a power on condition (dc power active), the Control MPU is periodically checking the operation of the drive and generating appropriate operating status codes.

Table A-4 lists the status codes and a definition of each code. If a drive malfunction occurs, observe the error code and perform Diagnostic Test 04 to calculate the action to be taken.

Figure A-2 shows an example of the LCD during normal operation and when a fault occurs. During normal operation, the LCD displays current drive status, current cylinder address, and which drive channel is selected and/or reserved.

If a fault occurs, the LCD displays the type of fault(s) that occurred along with the current drive status. If more than one fault occurred, use the SPACE switch to step through the fault log to determine what faults are present.

Pressing the Fault Clear switch on the status/control panel clears the LCD fault display and the FAULT indicator, provided that the fault condition is no longer present. However, a Fault Clear signal from the controller clears only the FAULT indicator.

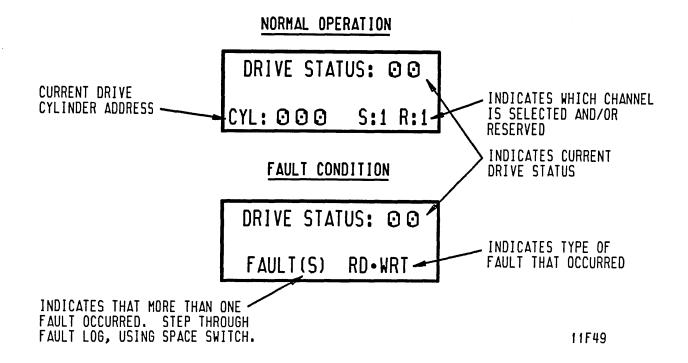


Figure A-2. Example of LCD (Liquid Crystal Display)

TABLE A-4. DRIVE STATUS CODES

Code	Title	Description			
	Normal Start/Stop Status				
00	Ready & On Cylinder	Indicates that the drive is on cylinder and ready to perform normal operations.			
02	Motor Stopping	Indicates that the motor is coasting down or that motor braking is in progress.			
03	Motor Stopped	Indicates that the motor is stopped.			
	Table Continued on Next Page				

TABLE A-4. DRIVE STATUS CODES (Contd)

Code	Title	Description
	Normal Start	t/Stop Status (Contd)
04	First Load/Calibrate	Indicates that the heads are moving from the landing zone to track O and servo calibration is being performed.
05	Sequence Delay	Indicates, in Remote mode, that a power sequence delay is occurring. The delay depends on the drive's unit address.
06	START Switch Pressed and Waiting for Power Sequence Signals	Indicates, in Remote mode, that the START switch was pressed and that the drive is waiting for power sequence signals from the controller before starting the sequence delay.
07	Starting Motor	Indicates that start conditions are present and that the spindle motor is starting.
08	Motor Up To Speed	Indicates that the spindle motor has reached full speed.
	SWEER	CYCLE STATUS
OA	Drive in Sweep Segment	The drive is executing a series of seeks for a sweep cycle. On Cylinder and Seek End are inactive at this time.
ОВ	Heads Left on Last Cylinder of Sweep	Current head position determined by last sweep cycle not by a controller-requested seek. On Cylinder and Seek End are inactive at this time.
	Table Cont	inued on Next Page

TABLE A-4. DRIVE STATUS CODES (Contd)

Code	Title	Description
	Seek	Error Status
46	Seek Timeout	Indicates that during a normal seek the drive took longer than 100 milliseconds to reach on cylinder.
4B	Off Track Seek Error	Indicates that either the drive failed to stay on cylinder or cylinder pulses were detected during track-following.
4D	Illegal Cylinder Address	Indicates that during a normal seek, the controller issued too high a cylinder address (>1634 for 741 MB, 1123 MB, and 1236 MB drives; >1380 for 851 MB drives).
4F	Seek Error On Settle In	Indicates that the drive could not settle in on the destination cylinder.
	First S	eek Fault Status
54	First Seek Fault On Retract	Indicates that the drive failed to complete the retract portion of the first seek.
55	First Seek Fault On Load	Indicates that the drive failed to load the heads.
56	First Seek Fault On RTZ	Indicates that the drive failed to complete the return to zero (RTZ) portion of the first seek.
57	First Seek Fault On Calibrate	Indicates that the drive did not complete the velocity calibration operation.
	Table Cont	inued on Next Page

TABLE A-4. DRIVE STATUS CODES (Contd)

Code	Title	Description			
	Error C	ondition Status			
58	Speed Loss	Indicates that the spindle speed fell below 3564 r/min. When this happens, the drive activates the Write Protect line, drops the Ready signal, and performs a retract operation.			
59	Motor Can't Start Due to Error	Indicates a problem in the motor control circuitry.			
5A	Emergency Retract	Indicates that the heads moved to the landing zone due to a power loss (-Low Vcc active), or that a servo-controlled retract failed.			
	Motor and Servo MPU Errors				
60	Motor MPU Failure	Indicates that the Control MPU was unable to communicate with the Motor MPU.			
61	Servo MPU Failure	Indicates that the Control MPU was unable to communicate with the Servo MPU.			

:			

Appendix B Reference Material for Sector Selection

INTRODUCTION

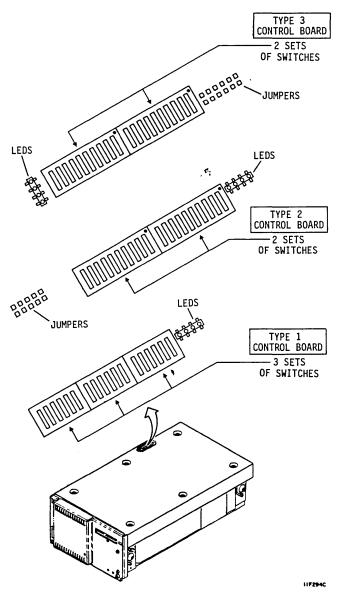
The information in this appendix supplements the instructions for sector selection given in section 3. The appendix is divided into two parts — one part applying to Type 1 control boards and the other part applying to Type 2 and 3 control boards. To determine the type of control board in your drive, examine the arrangement of DIP switches on the board. By uncovering slots in the drive top cover, you can see the DIP switches. The following picture shows both older and newer boards:

Type 1 control boards have 3 groups of switches with 6 switches in each group. Type 1 boards were used on early 741 and 851 MB drives only.

For Type 1 boards, all instructions on setting sector switches appear in this appendix.

Type 2 and Type 3 control boards have 2 groups of switches, with 10 switches in each group. The main difference between Type 2 and Type 3 boards is the location and function of jumpers that select certain features.

For Type 2 and Type 3 boards, section 3 provides basic instructions for sector selection. This appendix supplements the information in section 3.



REFERENCE MATERIAL FOR TYPE 1 CONTROL BOARDS

This topic describes how to set the sector switches on Type 1 control boards (shown in figure B-1). Refer to the subsystem or controller reference manual to determine the number of sectors (or the bytes/sector) required by the controller. To achieve that requirement, the following settings are required:

- Sector switches
- Sector Clock switch (.8/1.6)

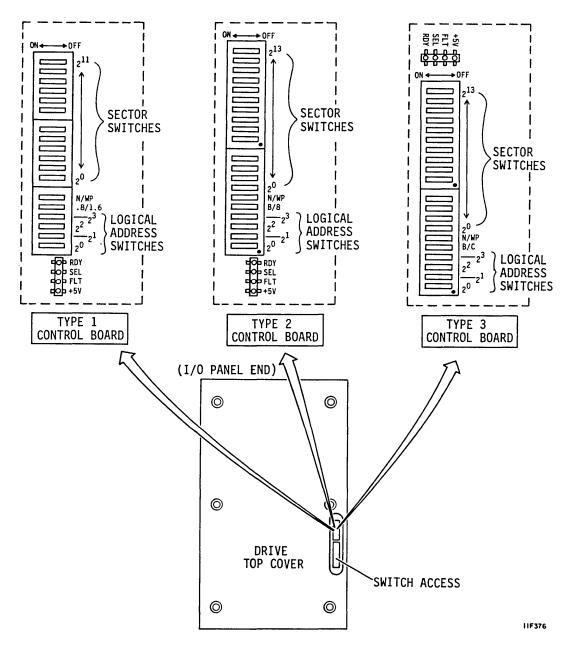


Figure B-1. Sector Switch Locations

The information for Type 1 control boards is organized as follows:

- What the Sector Switches Do
- How to Set Desired Numbers of Sectors
- How to Set Desired Sector Lengths
- How to Calculate Sector Lengths

WHAT THE SECTOR SWITCHES DO

The sector switches supply a preset count to the sector counter circuit. This circuit counts sector clock pulses, starting at the preset value when index appears and after each new sector pulse is issued. When the sector counter has received a specific number of clock pulses, it issues another sector pulse and begins at the preset value to count again. A switch selection determines whether the sector clock frequency is 0.8 MHz or 1.6 MHz.

Two numbers enter into all of the calculations presented here. They are the number of sector clock pulses per disc rotation and the number of bytes per track. Table B-1 summarizes these numbers.

TABLE B-1. SECTOR CALCULATION DATA FOR TYPE 1 BOARDS

Characteristics	741 MB Drives	851 MB Drives
Bytes per track	30 240	41 088
Sector clock pulses per disc		
	13 440	13 696
.8/1.6 switch set at .8 .8/1.6 switch set at 1.6	26 880	27 392
. 5/1.0 Switch Set at 1.0	20 000	27 332
Bytes per sector clock pulse		
.8/1.6 switch set at .8	2.25	3
.8/1.6 switch set at 1.6	1.125	1.5
Sector length adjustment (bytes)		
.8/1.6 switch set at .8	2.25	3
.8/1.6 switch set at 1.6	4.00	5.25
	<u> </u>	

The individual sector switches are identified by their binary values. Each sector switch represents a binary and decimal value of clock pulses (as counted in the logic). The values related to each switch are as follows:

Binary Value	Decimal Value
20	1
21	. 2
22	4
23	8
24	16
2 ⁵	32
26	64
27	128
28	256
29	512
210	1024
2 ¹¹	2048

HOW TO SET DESIRED NUMBERS OF SECTORS

This topic describes the process used for calculating sector switch settings and provides an example of the calculation. The process makes use of numbers given in table B-1. Here are the steps required:

- Calculate the number of selected clock pulses per sector. Use the following formula:
 - Selected Clock Pulses = Sector Clock Pulses/Rotation 1
 Number of Sectors Desired
- 2. If the result in step 1 is not an integer, either round it up or round it down. (The number 313.6, for example, can be rounded up to 314 or can be rounded down to 313.) Figure B-2 shows the effects of using the round-up and round-down methods, and section 3 contains some discussion of the two methods.
- 3. Express the result of step 2 as a sum of individual sector switch values (shown above). When a switch value appears in the sum, that switch must be placed in the Closed (On) position.

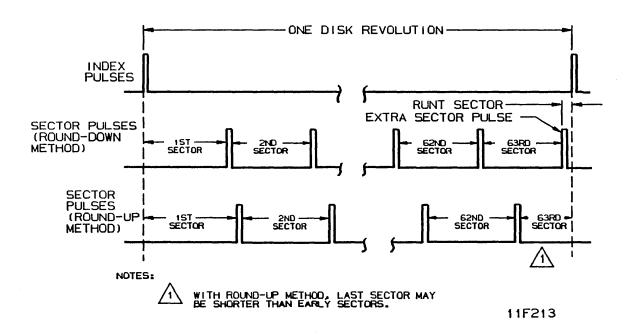


Figure B-2. Round-up and Round-down Methods (Type 1)

Here is an example of determining the switch settings for selecting 63 sectors on a 741 MB drive, using the 0.8 MHz Sector Clock and the round-down method:

1. Selected Clock Pulses =
$$(\frac{13 \ 440}{63} - 1) = (213.33 - 1)$$

= 212.33

- 2. For the round-down method, this result becomes 212. (For the round-up method, it would be 213.)
- 3. Determine which switches to place in the Closed (On) position as follows:

Select	ed cloc	ck pulses		212
Clock	pulses	selected by switch	27	- <u>128</u>
		(Difference)	_	84
Clock	pulses	selected by switch	26	- <u>64</u>
		(Difference)		20
Clock	pulses	selected by switch	2 ⁴	- <u>16</u>
		(Difference)	_	4
Clock	pulses	selected by switch	22	- <u>4</u>
		(Difference)		0

Thus, placing switches 2^2 , 2^4 , 2^6 , and 2^7 in the Closed (On) position selects 63 sectors. Since a remainder existed in the formula and was rounded down, an additional sector pulse will appear just before index.

Refer to How to Calculate Sector Lengths (later in this section) for information on finding the number of bytes in the usable sectors and in the last sector.

For future reference, you may want to record the switch settings you made. Use the following worksheet:

Number of	Sectors:			
Switch Se 2 ⁰ 2 ⁴ 2 ⁸	2 ¹ 2 ⁵ 2 ⁹	2 ² 2 ⁶ 2 ¹⁰	2 ³ 2 ⁷ 2 ¹¹	
.8/1.6	Switch:			
Sector Le	ngth:	_ bytes		

How to Set Desired Sector Lengths

This topic describes the process used for calculating sector switch settings for a desired sector length. It also provides an example of the calculation.

With this method, there is no attempt to divide the disc rotation into nearly equal segments. Instead, the method starts with a known sector length (data field plus overhead, expressed in bytes). The process makes use of numbers given in table B-1. Here are the steps required:

- Calculate the number of selected clock pulses per sector. Use the following formula:
 - Selected Clock Pulses = $\frac{\text{Sector Length in Bytes}}{\text{Bytes per Sector Clock Pulse}} 1$
- If the result in step 1 is not an integer, round it up. (The number 313.6, for example, would be rounded up to 314.)
- 3. Express the result of step 2 as a sum of individual sector switch values (see page B-4). When a switch value appears in the sum, that switch must be placed in the Closed (On) position.

Here is an example of determining the switch settings for selecting 572-byte sectors (512-byte data field plus 60 bytes of overhead) on an 851 MB drive, using the 1.6 MHz Sector Clock:

- 1. Selected Clock Pulses = $(\frac{572}{1.5} 1)$ = (381.3 1) = 380.3
- 2. Because this is not an integer, round it up to 381.
- 3. Determine which switches to place in the Closed (On) position as follows:

Selected cloc	ck pulses		381
Clock pulses	selected by switch	28	- <u>256</u>
	(Difference)		125
Clock pulses	selected by switch	26	- <u>64</u>
•	(Difference)		61
Clock pulses	selected by switch	25	- <u>32</u> 29
-	(Difference)		29
Clock pulses	selected by switch	2 ⁴	- <u>16</u>
_	(Difference)		13
Clock pulses	selected by switch	23	-8
-	(Difference)		- <u>8</u> 5
Clock pulses	selected by switch	22	-4
-	(Difference)		- <u>4</u> 1
Clock pulses	selected by switch	20	- <u>1</u>
-	(Difference)		ō

Thus, placing switches 2^0 , 2^2 , 2^3 , 2^4 , 2^5 , 2^6 , and 2^8 in the Closed (On) position selects the sectors with the required length.

Refer to How to Calculate Sector Lengths (later in this section) for information on finding the number of bytes in the usable sectors and in the last sector.

For future reference, you may want to record the switch settings you made. Use the following worksheet:

Sector Length:	bytes
Switch Settings:	
20 21	2 ² 2 ³
24 25	26 27
28 29	210 211
.8/1.6 Switch:	

HOW TO CALCULATE SECTOR LENGTHS

This method for calculating sector lengths uses the actual sector switch settings as a starting point. It works for any combination of switch settings regardless of how they were derived. This topic describes the process involved and provides an example of how to do the calculation.

- 1. Examine the sector switches and note which ones are in the Closed (On) position. Also note the position of the .8/1.6 switch.
- 2. For each sector switch noted in step 1, write down its decimal value given on page B-4.
- 3. Add the numbers accumulated in step 2. If you used the calculation formula in either of the preceding topics to obtain the sector switch settings, take note. The sum in this step is the same number derived in step 2 of those topics.
- 4. Add 1 to the result in step 3 to obtain the length of a sector expressed in sector clock pulses.
- 5. Multiply the result in step 4 by the number of bytes per sector clock pulse (given in table B-1) to obtain the number of bytes per sector.

Here is an example of determining the sector length for a 741 MB drive:

- 1. You observe that the following switches are in the Closed (On) position: 2^0 , 2^1 , 2^2 , 2^4 , 2^5 , and 2^7 . The .8/1.6 switch is set at .8 (0.8 MHz sector clock).
- 2. The Closed sector switches have the following values:

20	1
21	2
22	4
24	16
2 ⁵	32
2 ⁷	128

- 3. These numbers total 1 + 2 + 4 + 16 + 32 + 128 = 183.
- 4. By adding 1, you get 183 + 1 = 184.
- 5. A 741 MB drive with a 0.8 MHz sector clock has 2.25 bytes per sector clock pulse (from table B-1). You get the sector length by multiplying 184 by 2.25; it is 414 bytes.

You may be wondering at this point how long the last sector is. You have found that each of the early sectors is 414 bytes long. Refer to table B-1 to find the number of bytes per track (30 240 for this drive).

To find the number of full-length sectors, divide 30 240 by 414. It is 73 with a remainder of 18. This calculation implies that there are 73 sectors, each 414 bytes long, followed by a runt sector of 18 bytes.

These sector lengths are idealized in that the numbers do not reflect an adjustment that must be made to calculate the actual lengths of the first sector (logical sector 0) and the last sector. Table B-1 indicates the sector length adjustment (2.25 bytes for this example).

Including this adjustment, the sector lengths are as follows:

Sector 0 = 414.00 + 2.25 = 416.25 bytes Sectors 1 thru 72 = 414.00 bytes Runt Sector = 414.00 - 2.25 = 411.75 bytes

REFERENCE MATERIAL FOR TYPE 2 AND TYPE 3 CONTROL BOARDS

The information in this appendix for Type 2 and Type 3 control boards supplements the instructions for sector selection given in section 3. Section 3 provides a number of options for sector selection and uses tables to indicate switch settings and sector lengths. For most installations section 3 provides all the information required.

The information in this appendix is intended more for systems designers who are matching the drive's sector length to the nonstandard requirements of certain controllers. This information is organized as follows:

- What the Sector Switches Do
- How to Set Desired Numbers of Sectors
- How to Set Desired Sector Lengths
- How to Calculate Sector Lengths

WHAT THE SECTOR SWITCHES DO

The sector switches supply a preset count to the sector counter circuit. This circuit counts sector clock pulses, starting at the preset value when index appears and after each new sector pulse is issued. When the sector counter has received a specific number of clock pulses, it issues another sector pulse and begins at the preset value to count again. A switch selection allows the choice of two values for the sector clock frequency.

Two numbers enter into all of the calculations presented here. They are the number of sector clock pulses per disc rotation and the number of bytes per track. Table B-2 summarizes these numbers.

TABLE B-2. SECTOR CALCULATION DATA FOR TYPE 2 AND TYPE 3 BOARDS

Characteristics	741 MB Drives	851 MB Drives	1123 MB Drives	1236 MB Drives
Bytes per track	30 240	41 088	45 792	50 400
Sector clock pulses per disc rotation				
B/C switch set at C [B/8 = 8]	26 880	27 392	30 528	33 600
B/C switch set at B [B/8 = B]	30 240	41 088	45 792	50 400
Bytes per sector clock pulse				
B/C switch set at C [$B/8 = 8$]	1.125	1.5	1.5	1.5
B/C switch set at B [B/8 = B]	1.0	1.0	1.0	1.0
Sector length adjustment (bytes)				
B/C switch set at C [B/8 = 8]	4.5	6.0	6.0	7.5
B/C switch set at B $[B/8 = B]$	5.0	6.0	7.0	8.0

The individual sector switches are identified by their binary values (see section 3). Each sector switch represents a binary and decimal value of clock pulses (as counted in the logic). The values related to each switch are as follows:

Binary Value	Decimal Value
20	1
21	2
22	4
23	8
24	16
2 ⁵	32
26	64
2 ⁷	128
28	256
29	512
2 ¹⁰	1024
2 ¹¹	2048
2 12	4096
2 ¹³	8192

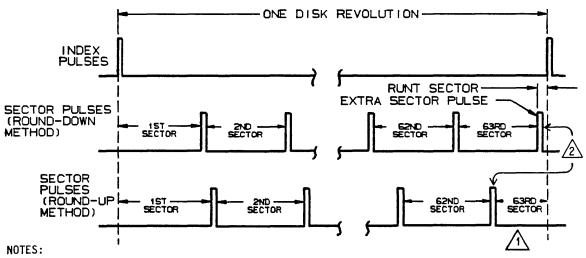
HOW TO SET DESIRED NUMBERS OF SECTORS

This topic describes the process used for calculating sector switch settings for a desired number of sectors per disc rotation. This process is the basis for all the sector selection tables found in section 3. After a description of the process, there is an example of the calculation. The process makes use of numbers given in table B-2. Here are the steps required:

1. Calculate the number of selected clock pulses per sector. Use the following formula:

Selected Clock Pulses = Sector Clock Pulses/Rotation - 1
Number of Sectors Desired

2. If the result in step 1 is not an integer, either round it up or round it down. (The number 313.6, for example, can be rounded up to 314 or can be rounded down to 313.) Figure B-3 shows the effects of using the round-up and round-down methods, and section 3 contains some discussion of the two methods.



/1\ with round-up method, Last sector may BE SHORTER THAN EARLY SECTORS.

2 CONNECTING "RUNT" JUMPER SUPPRESSES SECTOR PULSE PRECEDING SHORT LAST SECTOR.

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Figure B-3. Round-up and Round-down Methods (Types 2 and 3)

Express the result of step 2 as a sum of individual sector switch values (see previous page). When a switch value appears in the sum, that switch must be placed in the Open (Off) position.

Here is an example of determining the switch settings for selecting 63 sectors on a 741 MB drive, using the 1.6 MHz Sector Clock and the round-down method:

- Selected Clock Pulses = $(\frac{26\ 880}{1} 1) = (426.67 1)$ 1. = 425.67
- 2. For the round-down method, this result becomes 425. (For the round-up method, it would be 426.)

3. Determine which switches to place in the Open (Off) position as follows:

Selected clock pulses	425
Clock pulses selected by switch 2^8	- <u>256</u>
(Difference)	169
Clock pulses selected by switch 2^7	- <u>128</u>
(Difference)	41
Clock pulses selected by switch 2 ⁵	- <u>32</u>
(Difference)	9
Clock pulses selected by switch 2 ³	- <u>8</u>
(Difference)	1
Clock pulses selected by switch 2^0	- <u>1</u>
(Difference)	0

Thus, placing switches 2^0 , 2^3 , 2^5 , 2^7 , and 2^8 in the Open (Off) position selects 63 sectors. Since a remainder existed in the formula and was rounded down, an additional sector pulse will appear just before index.

Refer to How to Calculate Sector Lengths (at the end of this section) for information on finding the number of bytes in the usable sectors and in the last sector. If the last sector is too short to be compatible with controller requirements, consider the option of runt suppression.

For future reference, you may want to record the switch settings you made. Use the following worksheet:

Number of Sectors:
Switch Settings:
B/C [or B/8] Switch: On Off RUNT jumper: Connected Disconnected Sector Length: bytes

HOW TO SET DESIRED SECTOR LENGTHS

This topic describes the process used for calculating sector switch settings for a desired sector length. It also provides an example of the calculation.

With this method, there is no attempt to divide the disc rotation into nearly equal segments. Instead, the method starts with a known sector length (data field plus overhead, expressed in bytes). The process makes use of numbers given in table B-2. Here are the steps required:

- 1. Calculate the number of selected clock pulses per sector. Use the following formula:
 - Selected Clock Pulses = $\frac{\text{Sector Length in Bytes}}{\text{Bytes per Sector Clock Pulse}} 1$
- If the result in step 1 is not an integer, round it up. (The number 313.6, for example, would be rounded up to 314.)
- 3. Express the result of step 2 as a sum of individual sector switch values (see page B-11). When a switch value appears in the sum, that switch must be placed in the Open (Off) position.

Here is an example of determining the switch settings for selecting 572-byte sectors (512-byte data field plus 60 bytes of overhead) on an 851 MB drive, using the 1.6 MHz Sector Clock:

- 1. Selected Clock Pulses = $(\frac{572}{1.5} 1)$ = (381.3 1) = 380.3
- 2. Because this is not an integer, round it up to 381.
- 3. Determine which switches to place in the Open (Off) position as follows:

Selected c	clock pulses	381
Clock puls	ses selected by switch 2 ⁸	- <u>256</u>
	(Difference)	125
Clock puls	ses selected by switch 2 ⁶	$- \frac{64}{61}$
	(Difference)	61
Clock puls	ses selected by switch 2 ⁵	- <u>32</u> 29
	(Difference)	29
Clock puls	ses selected by switch 24	-16
_	(Difference)	$-\frac{16}{13}$
Clock puls	ses selected by switch 2^3	- <u>8</u> 5
	(Difference)	
Clock puls	ses selected by switch 22	- <u>4</u>
	(Difference)	ī
Clock puls	ses selected by switch 20	$-\frac{1}{0}$
	(Difference)	0

Thus, placing switches 2^0 , 2^2 , 2^3 , 2^4 , 2^5 , 2^6 , and 2^8 in the Open (Off) position selects the sectors with the required length.

Refer to How to Calculate Sector Lengths (at the end of this section) for information on finding the number of bytes in the usable sectors and in the last sector. If the last sector is too short to be compatible with controller requirements, consider the option of runt suppression.

For future reference, you may want to record the switch settings you made. Use the following worksheet:

Sector Length: ______ bytes

Switch Settings:

20 __ 21 __ 22 __ 23 __ 24 __

25 __ 26 __ 27 __ 28 __ 29 __

210 __ 211 __ 212 __ 213 __

B/C [or B/8] Switch: On _____ Off ___

RUNT jumper: Connected __ Disconnected ___

HOW TO CALCULATE SECTOR LENGTHS

This method for calculating sector lengths uses the actual sector switch settings as a starting point. It works for any combination of switch settings regardless of how they were derived. This topic describes the process involved and provides an example of how to do the calculation.

- 1. Examine the sector switches and note which ones are in the Open (Off) position. Also note the position of the B/C [or B/8] switch.
- For each sector switch noted in step 1, write down its decimal value given on page B-11.
- 3. Add the numbers accumulated in step 2. If you used the calculation formula in either of the preceding topics to obtain the sector switch settings, take note. The sum in this step is the same number derived in step 2 of those topics.
- 4. Add 1 to the result in step 3 to obtain the length of a sector expressed in sector clock pulses.
- 5. Multiply the result in step 4 by the number of bytes per sector clock pulse (given in table B-2) to obtain the number of bytes per sector.

Here is an example of determining the sector length for an 851 MB drive:

- You observe that the following switches are in the Open 1. (Off) position: 2^1 , 2^2 , 2^4 , 2^5 , 2^6 , and 2^8 . The B/C switch is set at C (1.6 MHz sector clock).
- 2. The Open sector switches have the following values:

2 ¹	2
22	4
24	16
2 ⁵	32
2 ⁶	64
28	256

- These numbers total 2 + 4 + 16 + 32 + 64 + 256 = 374. 3.
- By adding 1, you get 374 + 1 = 375.
- An 851 MB drive with a 1.6 MHz sector clock has 1.5 bytes per sector clock pulse (from table B-2). You get the sector length by multiplying 375 by 1.5; it is 562.5 bytes.

You may be wondering at this point how long the last sector is. You have found that each of the early sectors is 562.5 bytes long. Refer to table B-2 to find the number of bytes per track (41 088 for this drive).

To find the number of full-length sectors, divide 41 088 by 562.5. It is 73 with a remainder of 25.5. This calculation implies that there are 73 sectors, each 562.5 bytes long, followed by a runt sector of 25.5 bytes.

These sector lengths are idealized in that the numbers do not reflect an adjustment that must be made to calculate the actual lengths of the first sector (logical sector 0) and the last sector. Table B-2 indicates the sector length adjustment (6.0 bytes for this example).

Including this adjustment, the sector lengths are as follows:

Sector 0 = 562.5 + 6.0 = 568.5 bytes Sectors 1 thru 72 = 562.5 bytes Runt Sector = 25.5 - 6.0 = 19.5 bytes

With the RUNT jumper connected (see section 3), runt sector pulses are suppressed. In this case, sector 72 contains 562.5 + 19.5 = 582 bytes.

Appendix C German Instructions

EINLEITUNG

Dieser Anhang enthält Informationen für gefahrlose(n) Anschluß, Betrieb und Wartung des Laufwerks.

SICHERHEITSHINWEISE

- Um die Zuverlässigkeit der im Laufwerk eingebauten Sicherheitseinrichtungen zu gewährleisten, darf die Installation und Wartung des Gerätes nur von qualifiziertem Wartungspersonal unter Verwendung von Original-Seagate-Ersatzteilen durchgeführt werden.
- Beim Ausbrechen von Feuer oder in anderen Notfällen ist die Verbindung zum Hauptstromnetz durch das Ziehen des Netzsteckers aus der Steckdose zu unterbrechen. Sollte dies nicht möglich oder unpraktisch sein, so ist der Hauptstromunterbrecher des Systems zu bedienen, um die Laufwerke vom Hauptstromnetz zu trennen.
- Bei einem IT-Stromnetz ist eine Trennvorrichtung erforderlich, die alle Phasen- und Mittelleiter gleichzeitig unterbricht.
- Wenn das Laufwerk in einem Geräteeinschub oder Gehäuse montiert ist, versichern Sie sich, daß die Temperatur im Einschub oder Gehäuse die in diesem Handbuch festgelegten Werte nicht überschreitet. Sind Geräte übereinander angeordnet, achten Sie besonders auf das obere Gerät, da dort die Temperatur gewöhnlich am höchsten ist.
- Das Gerät ist konstruiert zum Anschluß und Betrieb in Übereinstimmung mit UL1950, IEC380, IEC950, EN60950, CSA C22.2 154, CSA C22.2 220, und VDE806.
- Geräteschalter schaltet nur die Sekundärseite des Netzgerätes. Schalterstellung 1: Normal- und Ersatzgerät ist eingeschaltet. Schalterstellung 0: Normal- und Ersatzgerät ist ausgeschaltet. Vorsicht: in beiden schalterstellungen liegt die Netzspannung an den Geräten an.
- Das Netzgerät ist für den Anschluß an ein IT-Netz vorgesehen.
- Das Netzteil ist nicht im feld reparierbar. Versuchen Sie deshalb nicht, es zu zerlegen. Im Falle eines Defekts ist es komplett auszutauschen.

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Falls kein empfohlenes Seagate Netzgerät verwendet wird, stellen Sie bitte sicher, daß das Netzgerät den Spezifikationen in diesem Manual entspricht und konstruiert ist für den Betrieb entsprechend UL1950, IEC380, IEC950, EN60950, CSA C22.2 154, CSA C22.2 220, und VDE806.

ANSCHLUSS-ERFORDERNISSE

Der Installationsort muß dem in den folgenden Tabellen und Skizzen dargelegten Vorschriften entsprechen:

TABELLE C-1. UMGEBUNGSBEDINGUNGEN

		y
<u>TEMPERATUR</u>	BEREICH	MAX. ÄNDERUNGSWERT
Lagerung (verpackt)	-10 bis 50°C	15°C / h
Transit (verpackt)	-40 bis 60°C	20°C / h
in Betrieb	10 bis 45°C	15°C / h
RELATIVE FEUCHTE		
Lagerung (verpackt)	5% bis 95% (kein	e Kondensation erlaubt)
Transit (verpackt)	5% bis 95% (kein	e Kondensation erlaubt)
in Betrieb	20% bis 80% (kei	ne Kondensation erlaubt)
<u>ATMOSPHÄRENDRUCK</u>		
Lagerung (verpackt)	-305 m bis 3000 m kPa	m oder 104 kPa bis 69
Transit (verpackt)	-305 m bis 12 193 kPa	2 m oder 104 kPa bis 19
in Betrieb	-305 m bis 3000 m kPa	m oder 104 kPa bis 69

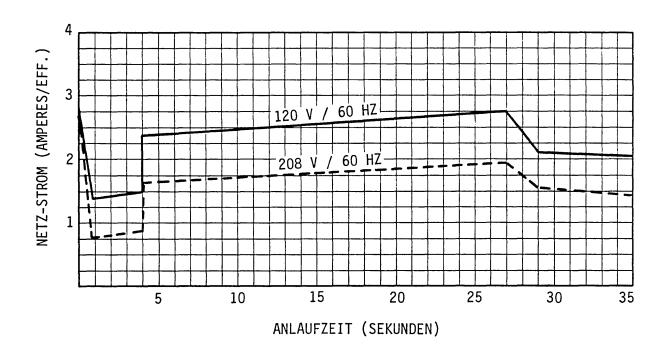
C-2

TABELLE C-2. ERFORDERNISSE FÜR EL. ANSCHLUSS

	WERTE		
SPEZIFIKATION	100 - 120 V ac	208 - 240 V ac	
Spannungsbereich	85 bis 132 V	177 bis 264 V	
Nominale Netz-Frequenz	50/60 Hz	50/60 Hz	
Frequenzbereich	48,0 bis 62,0 Hz	48,0 bis 62,0 Hz	
Phase	einphasig	einphasig	
Leistungsbedarf*	0,147 - 0,153 kW	0,149 - 0,144 kW	
Stromaufnahme*	2,6 - 2,3 A	1,6 - 1,4 A	
Phasenwinkel *Cos phi	0,57 - 0,56	0,46 - 0,43	
Anlaufstrom	siehe Abbildung C-1.	siehe Abbildung C-1.	

^{*} gemessen bei rotierendem Plattenstapel und Kopfschlitten in Bewegung.

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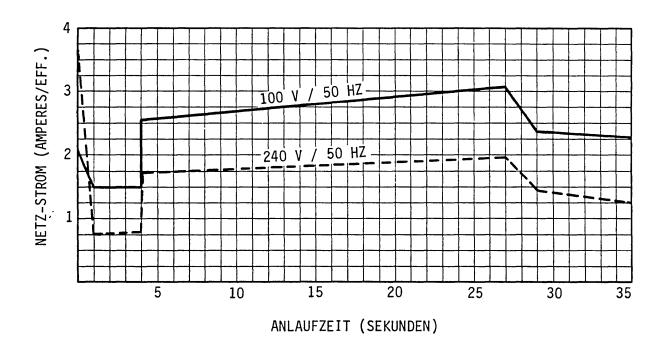
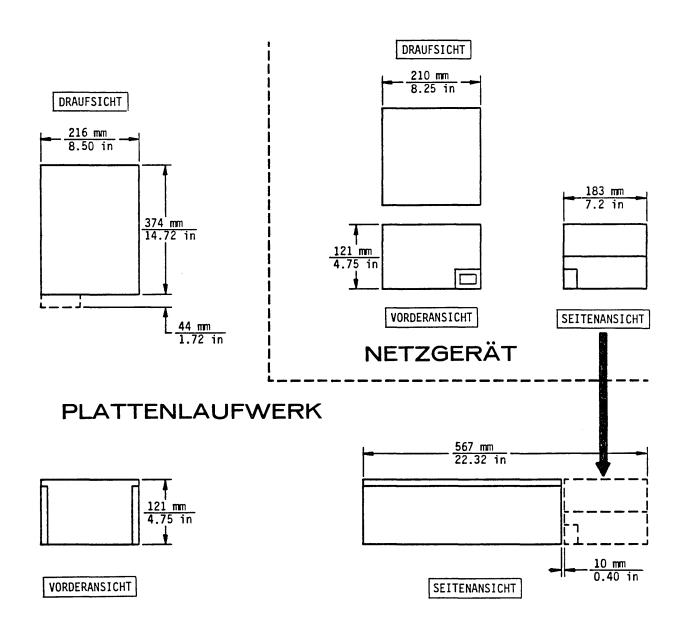


Abbildung C-1. Typischer Anlaufstrom im Verhältnis zur Zeit

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HINWEISI:

- 1. GEWICHT

 GERAT ALLEINE = 14,8 kg (32,8 lb)

 NETZGERAT ALLEINE = 3,6 kg (8 lb)
- 2. DIE ANGEGEBENEN ABMESSUNGEN SIND NOMINAL-WERTE

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Abbildung C-2. Abmessungen und Gewicht des Laufwerks

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BETRIEBSANLEITUNG (mit Status/Control Panel oder Operator Panel)

EINSCHALTSEQUENZ

- 1. Am Netzgerät Schalter ON/STANDBY auf ON stellen.
- 2. START-Schalter drücken.
 - Wurde der R/L (Remote/Local)-Schalter an der I/O-Karte während des Installationsvorganges auf "L" (Local) gestellt, beginnt die Einschaltsequenz unmittelbar.
 - Wurde der R/L-Schalter während des Installationsvorganges auf "R" (Remote) gestellt, ist die Einschaltsequenz verzögert, bis das Laufwerk das entsprechende Signal vom Kontroller erhält.
- Die Bereitschaftsanzeige (kleine Lampe im START-Schalter) blinkt in rascher Folge, solange die Startsequenz abläuft.
- 4. Beobachten Sie, daß die Bereitschaftsanzeige innerhalb von 90 sec auf Dauerlicht wechselt, nachdem das Laufwerk Nenndrehzahl erreicht hat und die Magnetköpfe eingefahren sind.
- 5. Die FAULT-Anzeige muß unbeleuchtet sein, zum Zeichen, daß kein Fehler auftrat und das Gerät zum Lesen oder Schreiben von Daten bereit ist.

AUSSCHALTSEQUENZ

- START-Schalter drücken. Wurde während des Installationsvorgangs der R/L-Schalter an der I/O-Karte auf "R" gestellt, so kann der Kontroller das Laufwerk veranlassen, die Ausschaltsequenz zu starten (Drücken des START-Schalters nicht erforderlich).
- Die Bereitschaftslampe (kleine Lampe in START-Schalter) blinkt langsam, solange die Ausschaltsequenz abläuft.
- 3. Beobachten Sie, daß die Bereitschaftsanzeige nach etwa 60 sec verlöscht, zum Zeichen, daß die Ausschaltsequenz abgeschlossen ist. Die Magnetköpfe sind jetzt in der Landezone positioniert und der Plattenstapel rotiert nicht mehr.
- Zur Unterbrechung der Gleichstromverbindung zum Laufwerk wird am Netzgerät der Schalter ON/STANDBY auf STANDBY gestellt.

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<u>BETRIEBSANLEITUNG</u> (ohne Status/Control Panel oder Operator Panel)

EINSCHALTSEQUENZ

Am Netzgerät ON/STANDBY-Schalter auf ON stellen.

- Wurde während des Installationsvorganges der Schalter R/L (Remote/Local) an der I/O-Karte auf "L" (Local) gestellt, beginnt die Einschaltsequenz unmittelbar und ist innerhalb 90 sec abgeschlossen.
- Wurde der R/L (Remote/Local)-Schalter an der I/O-Karte während des Installationsvorganges auf "R" (Remote) gestellt, dann startet die Einschaltsequenz, wenn das Laufwerk das entsprechende Signal vom Kontroller erhält.

AUSSCHALTSEQUENZ

Der Ausschaltvorgang läuft unterschiedlich ab, abhängig von der Position, in welcher der R/L (Remote/Local)-Schalter steht.

Steht der R/L-Schalter an der I/O Karte auf L (Local):

- ON/STANDBY-Schalter auf STANDBY stellen, um Gleichstromverbindung zu unterbrechen und die Ausschaltsequenz zu starten.
- Die Ausschaltsequenz ist etwa 60 sec nach ihrem Start abgeschlossen. Dabei kommt die Rotation des Plattenstapels zum Stillstand und die Magnetköpfe werden in der Landezone positioniert.

Steht der R/L-Schalter auf R (Remote), bestehen zwei Möglichkeiten zur Einleitung der Ausschaltsequenz:

- Fernbedienung: Hierbei veranlaßt der Kontroller das Laufwerk, die Ausschaltsequenz zu starten. Nach deren Abschluß besteht aber der Gleichspannungsanschluß zum Netzteil fort, wenn nicht der ON/STANDBY- Schalter auf STANDBY gestellt wird.
- Lokale Bedienung: Der ON/STANDBY-Schalter wird auf STANDBY gestellt. Dadurch wird der Gleichspannungsanschluß zum Laufwerk unterbrochen und die Ausschaltsequenz eingeleitet.
- Bei beiden Methoden ist die Ausschaltsequenz innerhalb von 60 sec nach deren Start abgeschlossen. Der Plattenstapel rotiert nicht mehr und die Magnetköpfe befinden sich in der Landezone.

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Appendix D French Instructions

INSTRUCTIONS D'INSTALLATION ET DE FONCTIONNEMENT D

INTRODUCTION

Cet appendice contient les informations sécuritaires indispensables pour l'installation, le fonctionnement et la maintenance de l'appareil.

INFORMATION DE SECURITE

- Afin d'assurer l'intégralité des conditions sécuritaires installées dans l'appareil, l'installation et la maintenance doivent être accomplies exclusivement par un personnel qualifié utilisant des pièces recommandées Seagate.
- En cas d'incendie ou autres états d'urgence, isolez l'appareil de la source de courant en retirant la fiche secteur de la prise de courant. Pour les situations ou il n'est pas possible ou praticable de retirer la fiche, utilisez la déconnection générale du système pour isoler les appareils de la source de courant.
- Dans un système électrique IT, un dispositif de déconnexion devra être fourni pour couper simultanément tous les fils de phase et fils neutres.
- Si l'appareil est monté en rack ou en armoire, assurez-vous que la température interne du rack ou de l'armoire ne dépasse pas les limites définies pour l'appareil. Lorsque les appareils sont empilés verticalment, portez votre attention sur la partie supérieure du rack ou de l'armoire où les températures sont généralement plus élevées.
- Cet appareil est concu pour être installé et de fonctionner en accordance avec UL1950, IEC380, IEC950, EN60950, CSA C22.2 154, CSA C22.2 220, et VDE806.
- L'interrupteur de l'alimentation ne coupe pas la tension secteur. L'interrupteur placé sur On (1) permet la distribution des tensions continues, sur la position Standby ((b)) elles sont bloquées. Mais dans les deux cas la tension secteur est présente à l'intérieur de l'alimentation.
- L'alimentation électrique a été conçue pour être connectée à un réseau IT.
- Abstenez vous de décortiquer l'alimentation. Elle n'est pas reparable sur place. Remplacez l'alimentation complète en cas depanne.

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• Si vous n'utilisez pas une alimentation recommandée Seagate, assurez vous que l'alimentation soit conforme aux spécifications de ce manuel et qu'elle soit concue pour être utilisée en accordance avec UL1950, IEC380, IEC950, EN60950, CSA C22.2 154, CSA C22.2 220, et VDE806.

CONDITIONS D'INSTALLATION

Le lieu d'installation doit être conforme aux spécifications données dans les tableaux et figures suivants.

TABLEAU D-1. CONDITIONS DE L'ENVIRONNEMENT

TEMPERATURE	<u>MARGE</u>	FLUCTUATION MAXIMALE	
Stockage (Emballée)	-10 à 50°C	15°C par heure	
Transport (Emballée)	-40 à 60°C	20°C par heure	
En service	+10 à 45°C	15°C par heure	
HUMIDITE RELATIVE			
Stockage (Emballée)	5% à 95% (pas de condensation autorisée)		
Transport (Emballée)	5% à 95% (pas de condensation autorisée)		
En service	20% à 80% (pas de condensation autorisée)		
PRESSION BAROMETRIQUE			
Stockage (Emballée)	-305 m à 3000	m ou 104 kPa à 69 kPa	
Transport (Emballée)	-305 m à 12 1	.92 m ou 104 kPa à 19 kPa	
En service	-305 m à 3000 m ou 104 kPa à 69 kPa		

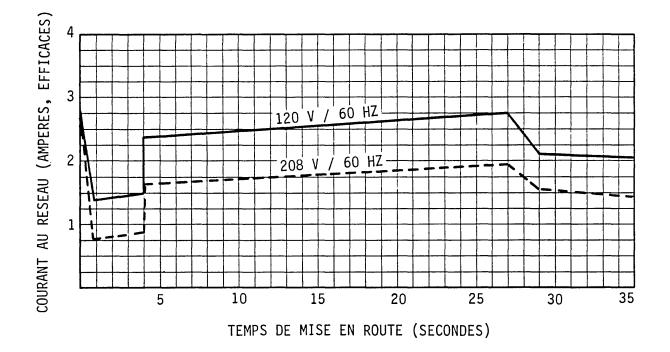
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TABLEAU D-2. EXIGENCES POUR L'ALIMENTATION

	VALEURS		
SPECIFICATIONS	100 - 120 V ac	208 - 240 V ac	
Marge de tension	85 à 132 V	177 à 264 V	
Fréquence nominale du réseau	50/60 Hz	50/60 Hz	
Marge de fréquence	48.0 à 62.0 Hz	48.0 à 62.0 Hz	
Phase éxigée	Monophasé	Monophasé	
Puissance consomée*	0.147 - 0.153 kW	0.149 - 0.144 kW	
Courant au réseau*	2.6 - 2.3 A	1.6 - 1.4 A	
Cosinus Phi*	0.57 - 0.56	0.46 - 0.43	
Courant de mise en route	Voir figure D-l.	Voir figure D-1.	

^{*} Mesuré lorsque les disques sont en rotation et que le chariot soit en mouvement.

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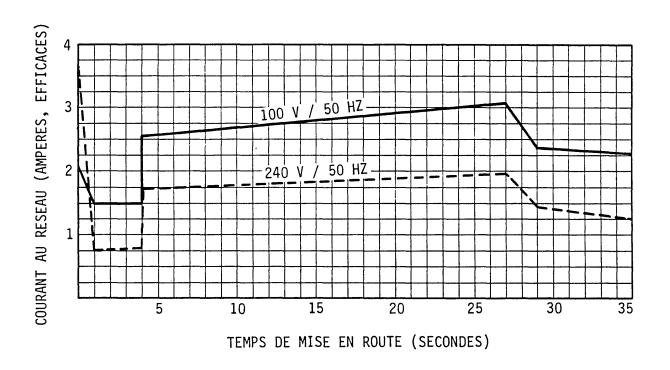
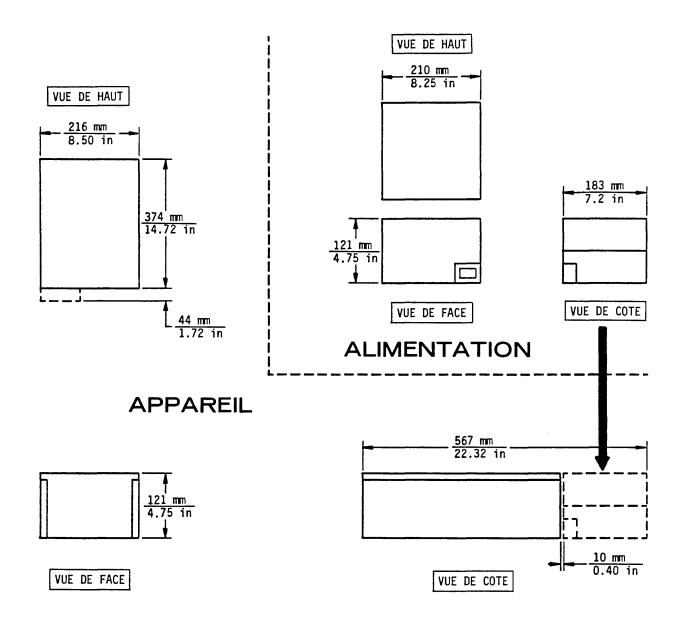


Figure D-1. Courant typique par rapport au temps de mise en route



NOTES:

- 1. MASSE APPAREIL SEUL = 14.8 kg (32.8 lb) ALIMENTAIRE SEULE = 3.6 kg (8 lb)
- 2. LES DIMENSIONS DONNEES SONT NOMINALES.

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Figure D-2. Dimensions et poids des appareils

INSTRUCTIONS DE FONCTIONNEMENT (avec panneau status/control ou operator)

PROCEDURE DE MISE EN ROUTE

- 1. Placez l'interrupteur On/Standby de l'alimentation sur ON.
- 2. Pressez l'interrupteur START.
 - Si le commutateur R/L (Remote/Local) situé sur la carte I/O à été placé sur L (Local) à l'installation, la séquence de mise en route démarre aussitôt.
 - Si le commutateur R/L est place sur R (Remote) à l'installation, la séquence est retardée jusqu'a ce que l'appareil recoive le signal approprié du contrôleur.
- 3. Observez que l'indicateur Ready (placé dans l'interrupteur START) clignote rapidement pour signaler que la séquence de mise en route est en progression.
- 4. Observez que l'indicateur Ready luise continuellement dans un délai de 90 secondes pour signaler que les disques ont atteints leur vitesse nominale et que les têtes sont chargées.
- 5. Observez que l'indicateur FAULT soit éteint pour signaler l'absence d'erreurs et que l'appareil est prêt à lire ou écrire des données.

PROCEDURE D'ARRET

- Pressez l'interrupteur START. Si le commutateur R/L placé sur la carte I/O à été placé sur R à l'installation, le contrôleur peut signaler à l'appareil de démarrer la séquence d'arrêt (il n'est pas nécessaire de presser l'interrupteur START).
- Observez que l'indicateur Ready (placé dans l'interrupteur START) clignote lentement pour signaler que le séquence d'arrêt est en progression.
- 3. Observez que l'indicateur Ready s'éteigne après environ 60 secondes pour signaler que l'arrêt est complet. Les têtes sont maintenant positionnées dans la zone d'atterissage et les disques ne sont pas en rotation.
- 4. Retirez la tension continue de l'appareil en placant l'interrupteur On/Standby de l'alimentation sur Standby.

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<u>INSTRUCTIONS DE FONCTIONNEMENT</u> (sans panneau status/control ou operator)

PROCEDURE DE MISE EN ROUTE

Placez l'interrupteur On/Standby de l'alimentation sur On.

- Si le commutateur R/L (Remote/Local) situé sur la carte I/O à été placé sur L (Local) à l'installation, la séquence de mise en route démarre aussitôt et est terminée dans un délai aux environs de 90 secondes.
- Si le commutateur R/L à été placé sur R (Remote) à l'installation, la séquence de mise en route démarre lorsque l'appareil recoit le signal approprié du contrôleur. La séquence est complète environ 90 secondes après que l'appareil ait recu ce signal.

PROCEDURE D'ARRET

La procédure d'arrêt varie selon la position sur laquelle le commutateur R/L (Remote/Local) est placé.

Si le commutateur R/L sur la carte I/O est sur L (Local):

- Placez l'interrupteur On/Standby sur Standby afin de retirer la tension continue et démarrer la séquence d'arrêt.
- La séquence d'arrêt est complète environ 60 secondes après son départ. Après celà les disques sont arrêtés et les têtes sont dans la zone d'atterrissage.

Si le commutateur R/L est sur R (Remote) il y à deux moyens pour démarrer une séquence d'arrêt:

- Méthode Remote: Ici, le contrôleur signale à l'appareil de démarrer la séquence d'arrêt. Lorsque la séquence est complète, l'alimentation continue de fournir les tensions continues à l'appareil tant que l'interrupteur On/Standby n'est pas placé sur Standby.
- Méthode Local: Vous placez l'interrupteur On/Standby sur Standby. Ceci retire les tensions continues de l'appareil pour démarrer la séquence.
- Avec les deux méthodes la séquence d'arrêt est complète environ 60 secondes après son départ. Après celà les disques sont arrêtés et les têtes sont dans la zone d'atterissage.

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Appendix E Spanish Instructions

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INTRODUCCION

Este apéndice contiene información pertinente a la instalación, operación y mantenimiento seguro del disco.

INFORMACION DE SEGURIDAD

- Para asegurar la integridad de las caracteristicas de seguridad integradas en el disco, la instalación y el mantenimiento deben ser realizadas solamente por personal de servicio cualificado utilizando solamente piezas originales de Seagate.
- En caso de fuego u otra emergencia, aislar el disco de la fuente principal de energía, desenchufando el cable de corriente del disco de la salida de corriente alterna. En situaciones donde quitar el enchufe no es posible ni práctico, utilize el sistema de desconexión principal para aislar los discos de las fuentes de energía.
- En un sistema de alimentación IT, se proporcionará un dispositivo de desconexión que interrumpe simultáneamente todos los conductores de fase y neutros.
- Cuando el disco está montado en un estante de equipos o en un armario, asegúrese de que la temperatura interna del armario no sobrepase los limites definidos para el disco. Cuando las unidades están almacenadas verticalmente, preste particular atención a la parte superior donde las temperaturas son normalmente más altas.
- Este disco está diseñado para ser instalado y operado de acuerdo a las normas UL1950, IEC380, IEC950, EN 60950, CSA C22.2 154, CSA C22.2 220 y VDE806.
- El interruptor de la fuente de alimentación no controla la entrada de corriente alterna. Poniendo el interruptor a "On" (1) abilita las salidas de corriente continua, y poniendolo en "Standby" (1) las desabilita. Pero en ambos casos la corriente alterna está presente dentro de la alimentación.
- La fuente de alimentación se ha diseñado para conectarse con una red IT.
- No intente desensamblar la fuente de alimentación. Ha de ser reparada en fábrica. Reemplaze la fuente de alimentación en su totalidad en caso de ser defectuosa.

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Si no utiliza una fuente de alimentación recomendada por Seagate asegúrese que la fuente de alimentación cumple las especificaciones de este manual y está diseñada para ser usada de acuerdo con las normas UL1950, IEC380, IEC950, EN 60950, CSA C22.2 154, CSA C22.2 220 y VDE806.

REQUISITOS DE INSTALACION

El local de la instalación debe cumplir las especificaciones dadas en las siguientes tablas y cifras.

TABLA E-1. REQUISITOS AMBIENTALES

<u> </u>				
TEMPERATURA	RANGO	VARIACION MAXIMO		
Sin operar (desempaquetada)	–10 a 50 <u>o</u> C	15 <u>o</u> C por hora		
Almacenada (embalada)	-40 a 60 <u>o</u> C	20 <u>o</u> C por hora		
Operando	10 a 45 <u>o</u> C	15 <u>o</u> C por hora		
HUMEDAD RELATIVA				
Sin operar (desempaquetada)	5% a 95% (sin condensación)			
Almacenada (embalada)	5% a 95% (sin co	ondensación)		
Operando	20% a 80% (sin o	condensación)		
PRESION BAROMETRICA				
Sin operar (desempaquetada)	-305 m a 3000 m	ó 104 kPa a 69 kPa		
Almacenada (embalada)	-305 m a 12 192	m ó 104 kPa a 19 kPa		
Operando	-305 m a 3000 m	ó 104 kPa a 69 kPa		

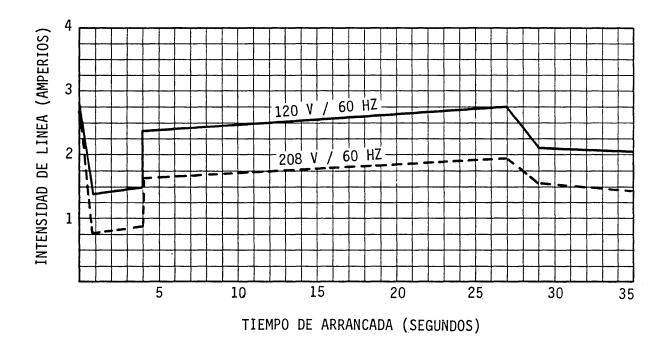
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TABLA E-2. REQUISITOS DE ENERGIA

	VALORES			
ESPECIFICACIONES	Unidad de 100/120	Unidad de 208/240		
Margen de tensión	85 a 132 V	177 a 264 V		
Frecuencia nominal	50/60 Hz	50/60 Hz		
Margen de Frecuencia	48 a 62 Hz	48 a 62 Hz		
Fases	Monofásico	Monofásico		
Consumo de Potencia*	0,147 - 0,153 kW	0,149 - 0,144 kW		
Consumo de corriente*	2,6 - 2,3 A	1,6 - 1,4 A		
Factor de potencia	0,57 - 0,56	0,46 - 0,43		
Corriente de arranque	Vea la figura E-l	Vea la figura E-1		

^{*} Medidas cuando los discos están girando y el actuador moviendose.

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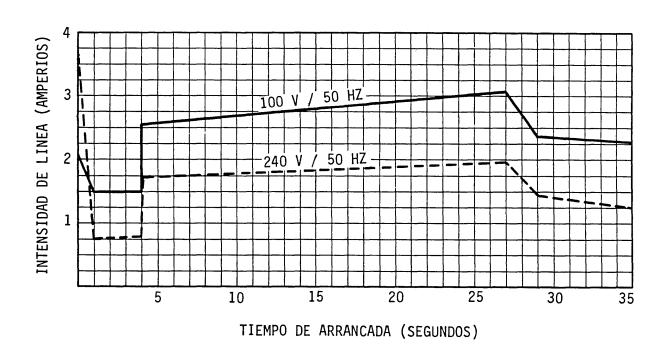
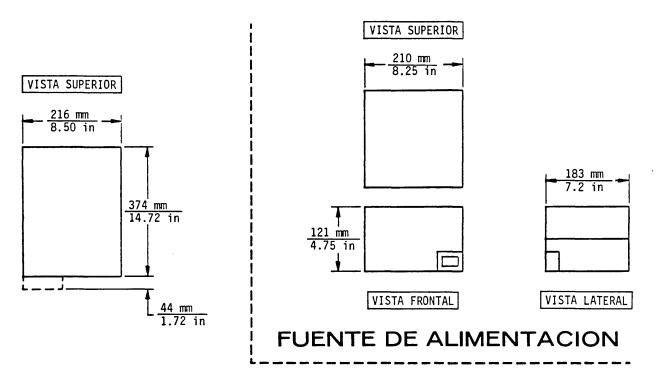
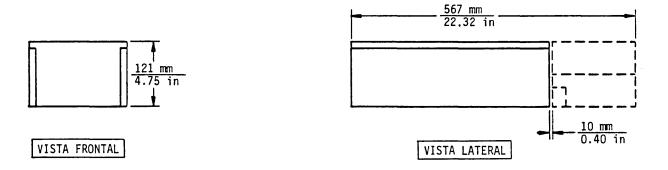


Figura E-1. Intensidad de linea y tiempos de arrancada

12J20



UNIDAD DE DISCO



NOTAS:

- 1. MASA
 UNIDAD DE DISCO SOLAMENTE = 14,8 kg (32,8 LIBRAS)
 FUENTE DE ALIMENTACION SOLAMENTE = 3,6 kg (8 LIBRAS)
- 2. LAS DIMENSIONES SON NOMINALES.

12J34

Figura E-2. Dimensiones y peso de la unidad de disco

INSTRUCCIONES DE OPERACION (Con Panel Status/Control ó Operator)

PROCEDIMIENTO DE ENCENDIDO

- 1. Fijar el interruptor "On/Standby" de la fuente de alimentación a la posición ON.
- Pulse el interruptor "START".
 - Si el interruptor "R/L" (remoto/local) de la tarjeta de I/O ha sido puesto en L (local) durante la instalación, la secuencia de encendido comienza inmediatamente.
 - Si el interruptor "R/L" ha sido puesto en R (remoto) durante la instalación. La secuencia se retrasa hasta que el disco recibe la señal apropiada desde el controlador.
- Observe que el indicador de "READY" (localizado en el interuptor "START") parpadea rapidamente indicando que la secuencia de encendido está en marcha.
- 4. Observe que 90 segundos más tarde el indicador de "READY" queda luciendo fijo, indicando que los discos están a su velocidad de régimen y las cabezas están cargadas.
- 5. Observe que el indicador de "FAULT" permanece apagado indicando que no han ocurrido errores y la unidad está disponible para leer o escribir datos.

PROCEDIMIENTO DE APAGADO

- 1. Pulse el interuptor de "START". Si el interruptor R/L en la tarjeta de I/O de la unidad ha sido puesto a R durante la instalación, el controlador puede ordenar al disco que inicie la secuencia de apagado (en ese caso no es necesario pulsar el interruptor "START").
- Observe que el indicador de "READY" (localizado en el interruptor de "START") parpadea lentamente indicando que que la secuencia de apagado está en marcha.
- 3. Observe que aproximadamente 60 segundos despues el indicador de "READY" se apaga indicando que la secuencia de apagado ha finalizado. Las cabezas están ahora posicionadas en la zona de aterrizaje y los discos no están girando.
- 4. Retire la corriente continua de la unidad de discos fijando el interruptor "On/Standby" de la fuente de alimentación a la posición STANDBY.

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INSTRUCCIONES DE OPERACION (Sin Panel Status/Control ó Operator)

PROCEDIMIENTO DE ENCENDIDO

Fije el interruptor "On/Standby" de la fuente de alimentación a la posición ON.

- Si el interruptor R/L (remoto/local) de la tarjeta de I/O fue fijado a L (local) durante la instalación, la secuencia de encendido comienza inmediatamente y tarda aproximadamente 90 segundos en finalizar.
- Si el interruptor R/L fue fijado a R (remoto) durante la instalación, la secuencia de encendido empieza cuando el disco recibe la señal apropiada desde el controlador y finaliza 90 segundos aproximadamente despues de recibida tal señal.

PROCEDIMIENTO DE APAGADO

El procedimiento de apagado varia dependiendo de como ha sido fijado el interruptor R/L (remoto/local).

Si el interruptor R/L (remoto/local) de la tarjeta de I/O está en L (Local).

- Fije el interruptor "On/Standby" a la posición STANDBY para retirar la corriente continua de la unidad y comenzar la secuencia de apagado.
- La secuencia de apagado se completa 60 segundos despues de iniciada. Deja a los discos parados con las cabezas en la zona de aterrizaje.

Si el interruptor R/L está en R (remoto), hay dos maneras de iniciar la secuencia de apagado.

- Método remoto: Aqui, la señal del controlador ordena al disco que inicie la secuencia de apagado. Cuando la secuencia finaliza, la fuente de alimentación continua suministrando corriente continua al disco a menos que el interruptor "On/Standby" se fije a la posición STANDBY.
- Método local: Fijando el interruptor de "On/Standby" a la posición STANDBY. Retira la corriente continua de la unidad para iniciar la secuencia.
- Con cualquier método, la secuencia de apagado finaliza 60 segundos despues de iniciada. Y deja a los discos parados con las cabezas en la zona de aterrizaje.

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Cut along line

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